

MI40-030

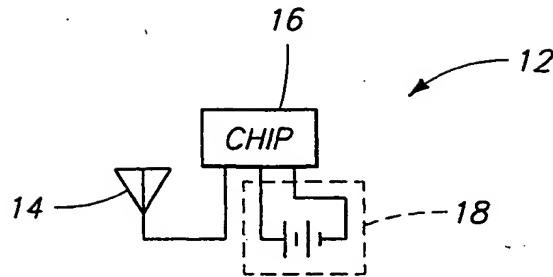


FIG. 1

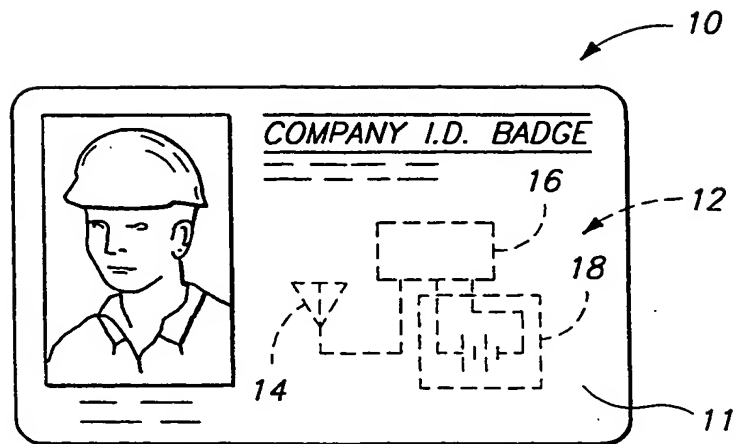


FIG. 2

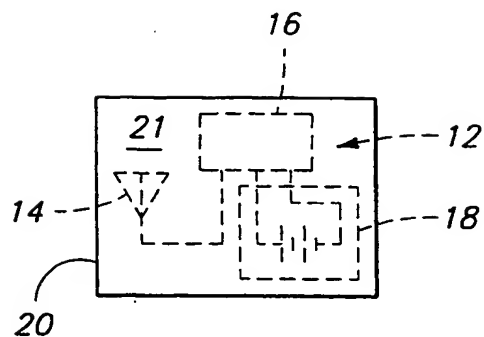


FIG. 1

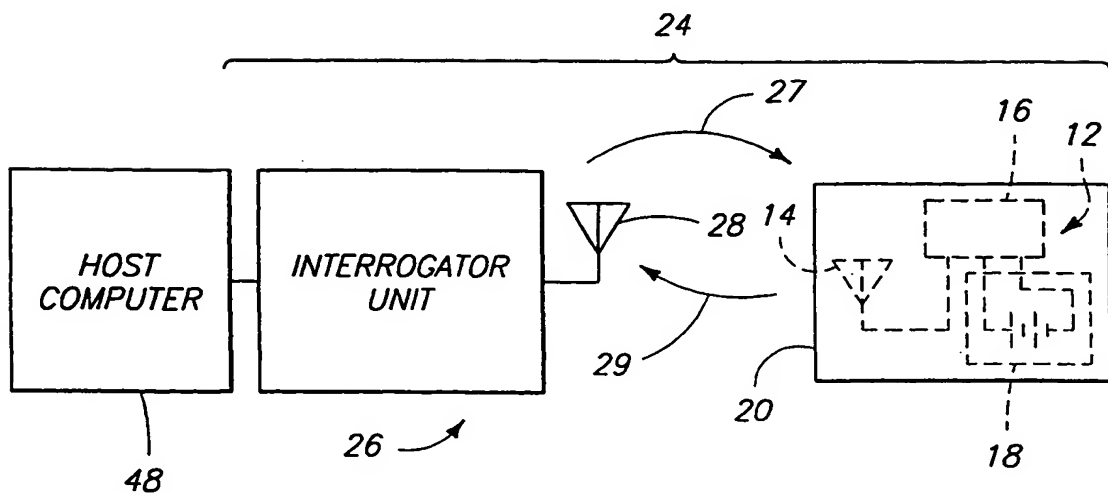
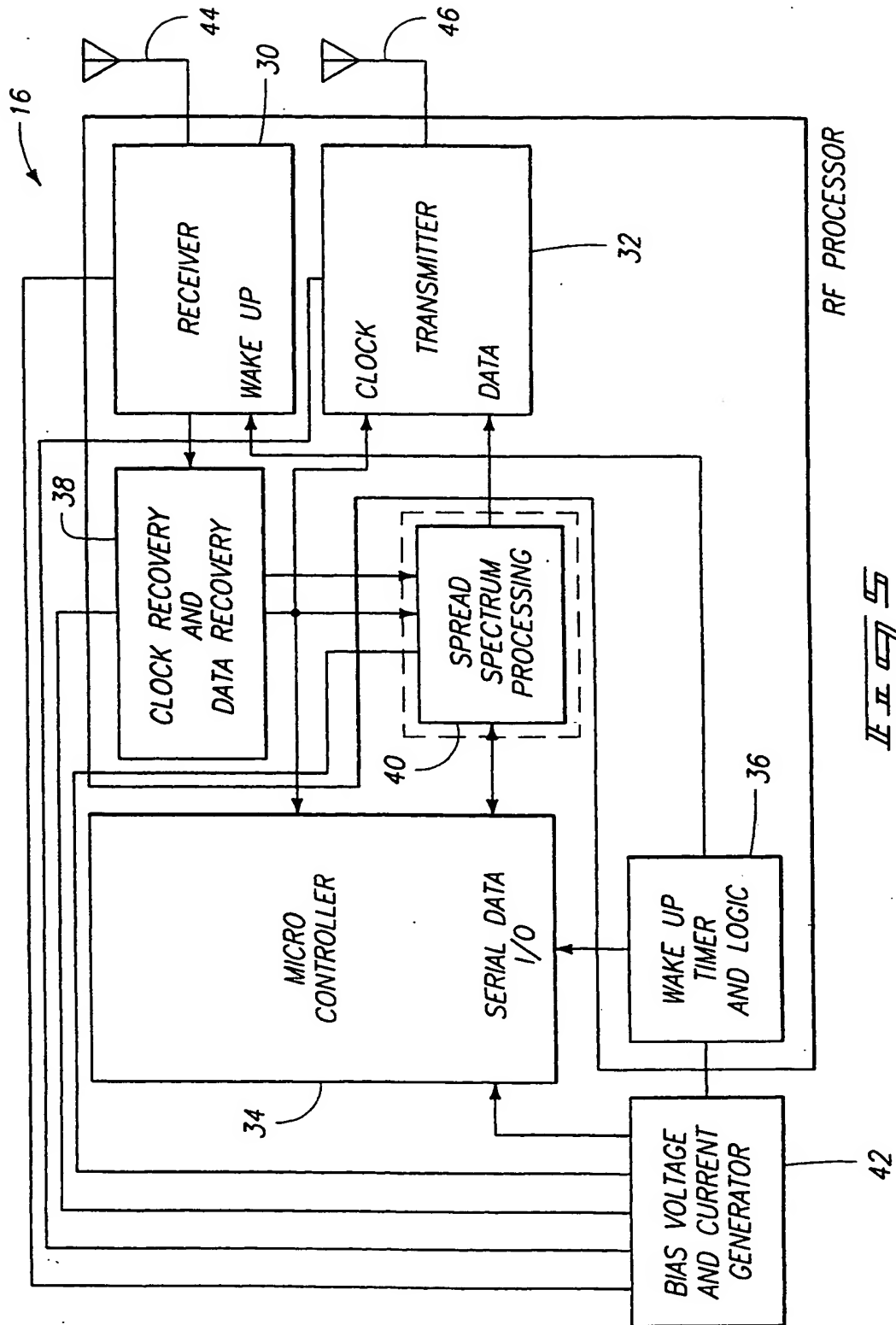


FIG. 2



16

6AA	6AB	6AC	6AD	6AE	6AF	6AG	6AH	6AI	6AJ	6AK
6BA	6BB	6BC	6BD	6BE	6BF	6BG	6BH	6BI	6BJ	6BK
6CA	6CB	6CC	6CD	6CE	6CF	6CG	6CH	6CI	6CJ	6CK
6DA	6DB	6DC	6DD	6DE	6DF	6DG	6DH	6DI	6DJ	6DK
6EA	6EB	6EC	6ED	6EE	6EF	6EG	6EH	6EI	6EJ	6EK

MI 40-030

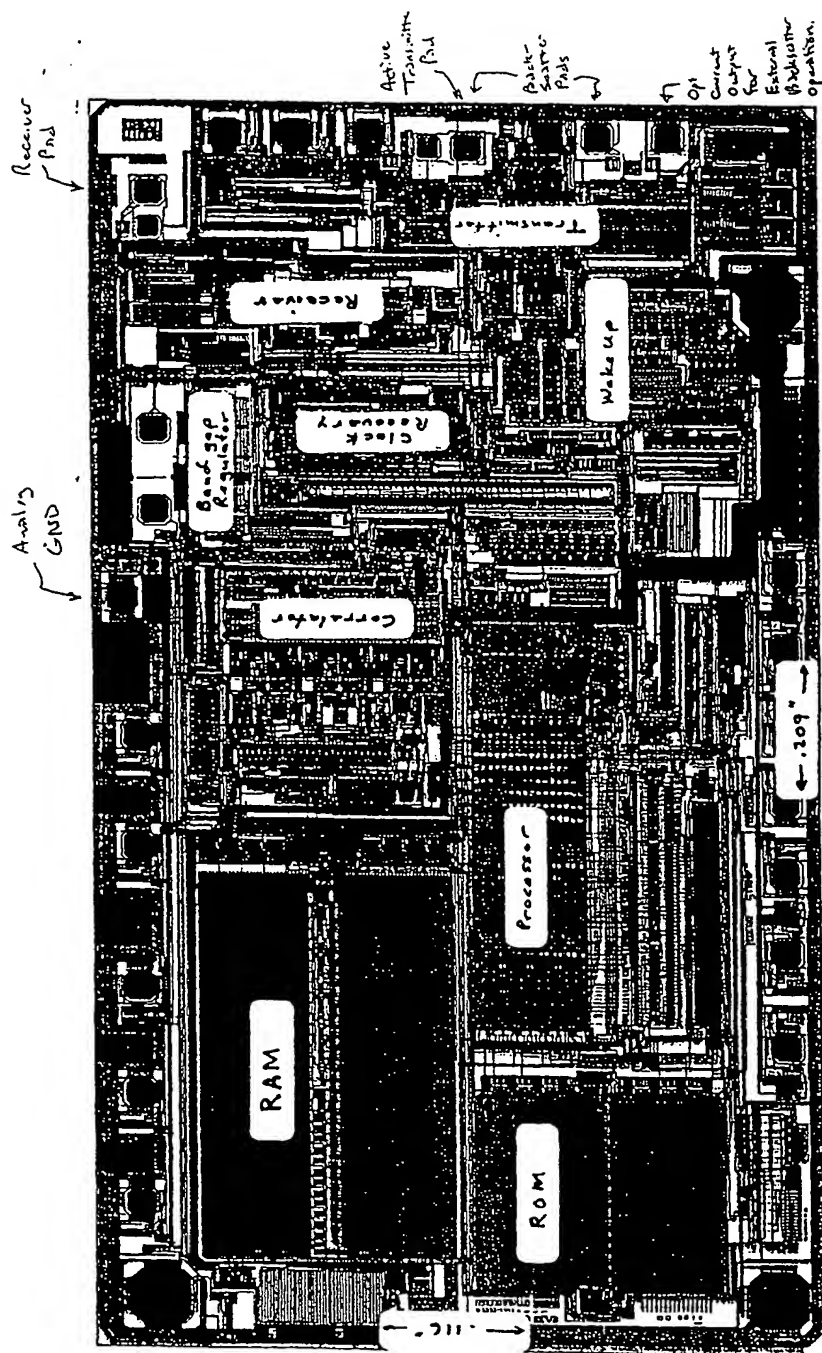
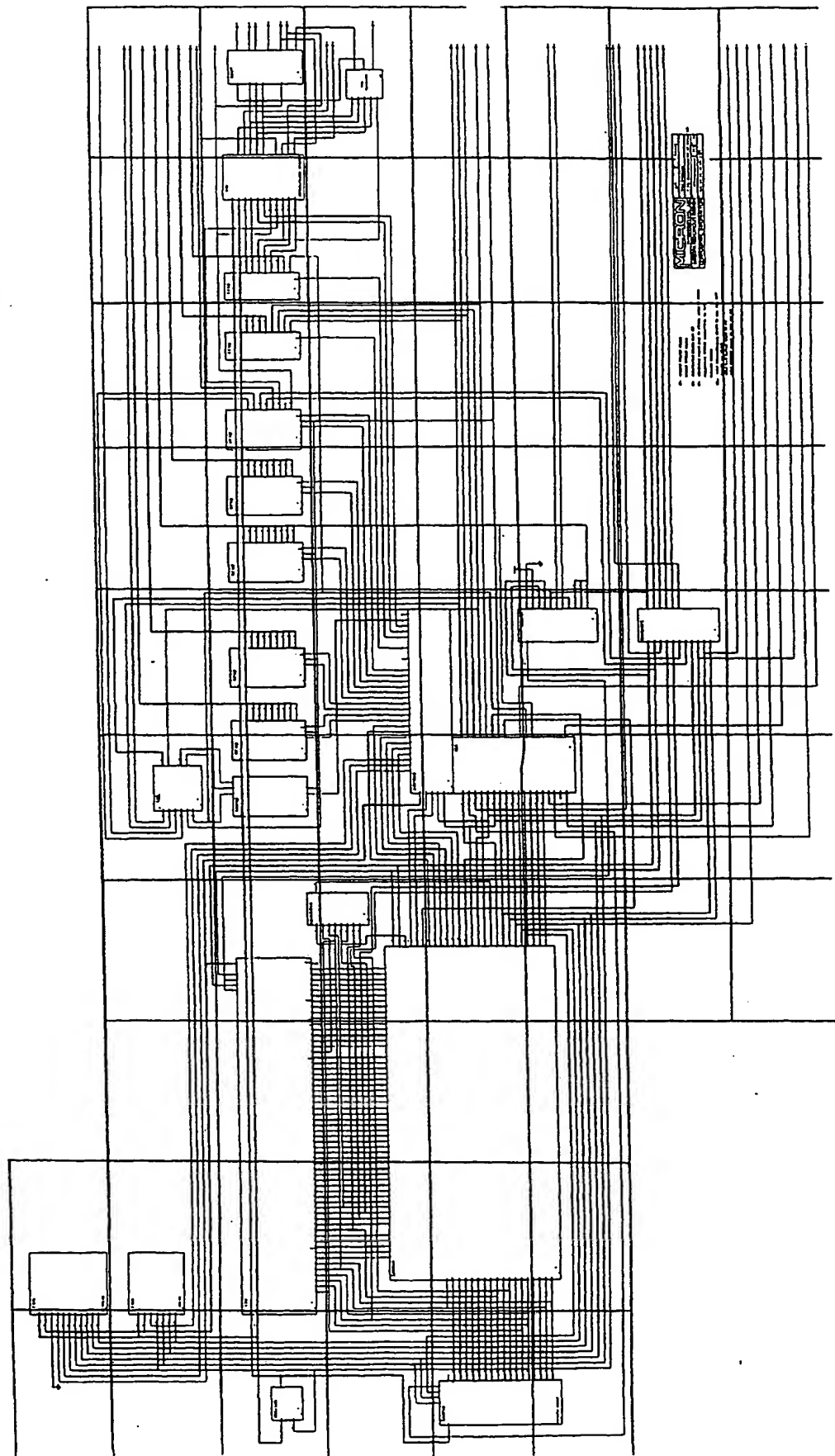


FIG. 6.01

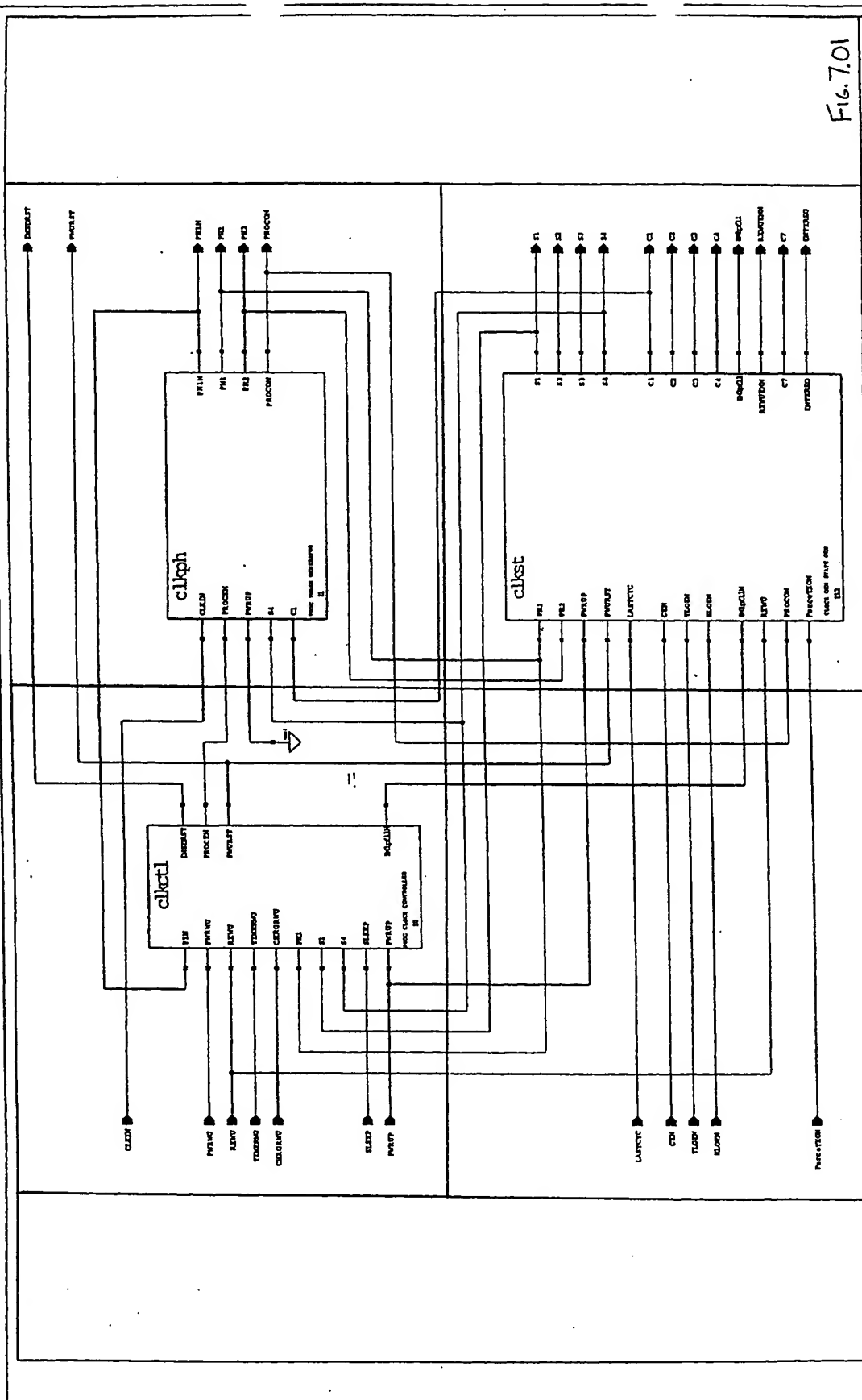
7AA	7AB		7BD	7BE	7BF	7BG	7BH	7BI	7BJ
7BA	7BB	7BC	7BD	7BE	7BF	7BG	7BH	7BI	7BJ
7CA	7CB	7CC	7CD	7CE	7CF	7CG	7CH	7CI	7CJ
7DA	7DB	7DC	7DD	7DE	7DF	7DG	7DH	7DI	7DJ
7EA	7EB	7EC	7ED	7EE	7EF	7EG	7EH	7EI	7EJ
7FA	7FB	7FC	7FD	7FE	7FF	7FG	7FH	7FI	7FJ
			7GD	7GE	7GF	7GG	7GH	7GI	7GJ
			7HD	7HE	7HF	7HG	7HH	7HI	7HJ

$$\frac{\pi}{\pi + \pi}$$



7.01AA	7.01AB
7.01BA	7.01BB

EX-701



```

EZ: created ENUP'all signal
    added ENTREC logic

all: added hard lockout to clkst

```

NICARON

COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

Rotzoll

Generator

[illegible]

81367-0/03

clk	1
clk	1

2 1996	
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7.0101AA	7.0101AB
7.0101BA	7.0101BB

U.S. DEPT. OF JUSTICE

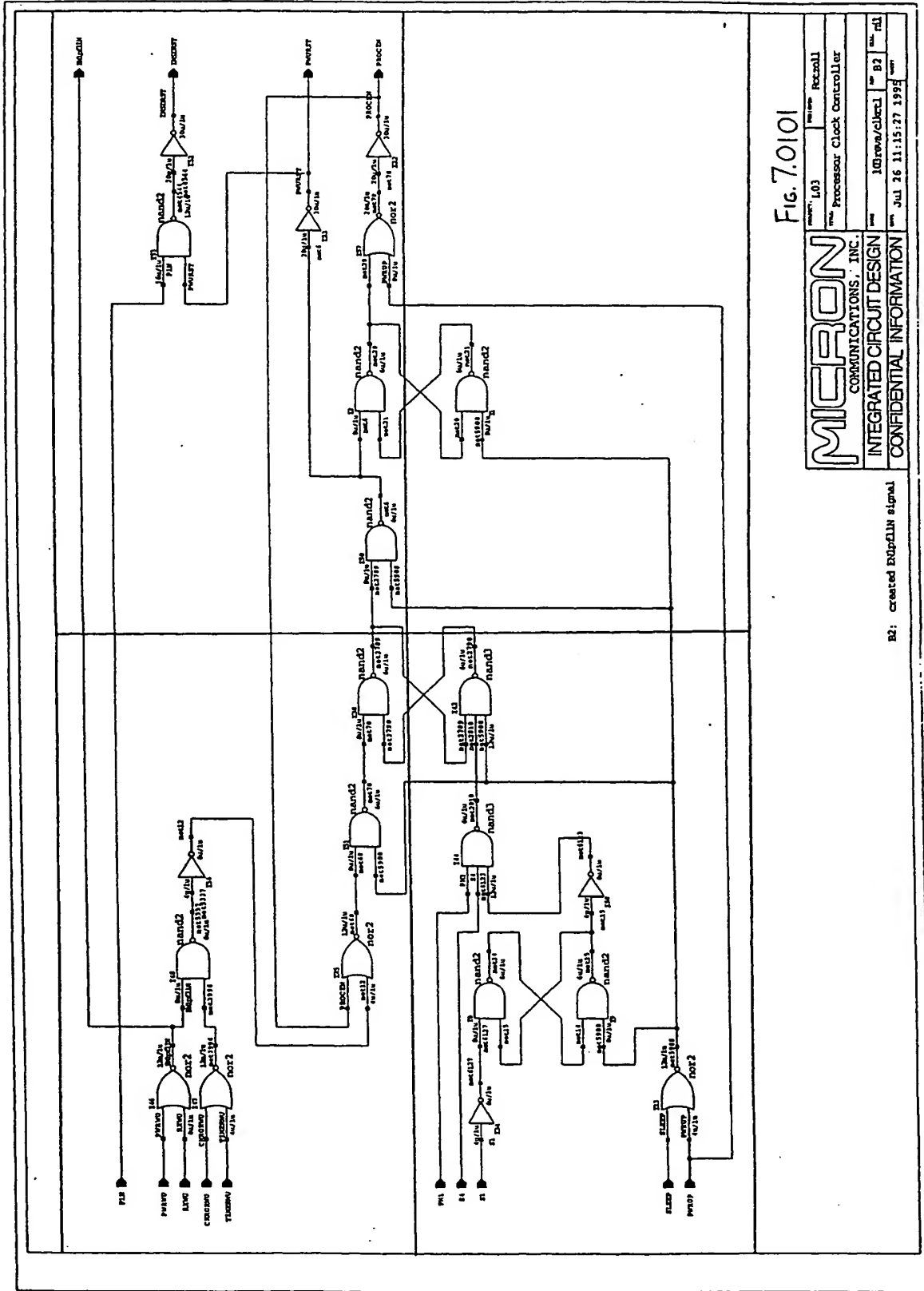


Fig. 7.0101

MICRON COMMUNICATIONS, INC. INTEGRATED CIRCUIT DESIGN CONFIDENTIAL INFORMATION	REV. 1.03	RECALL
	PROCESSOR Clock Controller	
	10/20/95/01/01	02
	Jul 26 11:15:27 1995	01

B2: created SUPPLIN signal

7.0102BA	7.0102BB	7.0102BC	7.0102BD	7.0102BE	7.0102BF	7.0102AG	7.0102AH	7.0102AI	7.0102AJ
7.0102CA	7.0102CB	7.0102CC	7.0102CD	7.0102CE	7.0102CF	7.0102CG	7.0102CH	7.0102CI	7.0102CJ
7.0102DA	7.0102DB	7.0102DC	7.0102DD	7.0102DE	7.0102DF	7.0102DG	7.0102DH	7.0102DI	7.0102DJ

7.0102

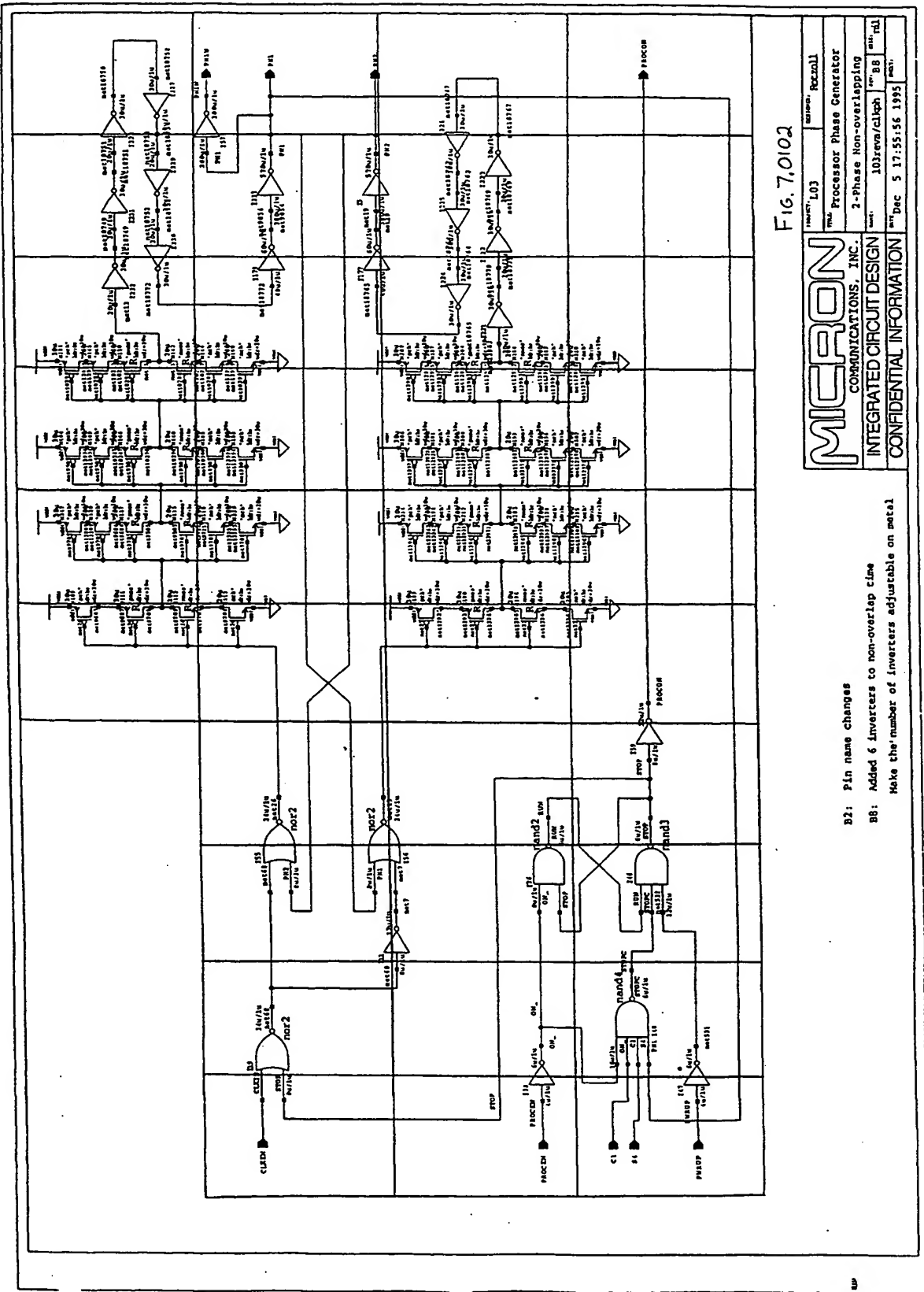


Fig. 7.0102

MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

PROCESSOR Phase Generator
2-Phase Non-overlapping
1037nm/cliph
1037nm/cliph
1037nm/cliph

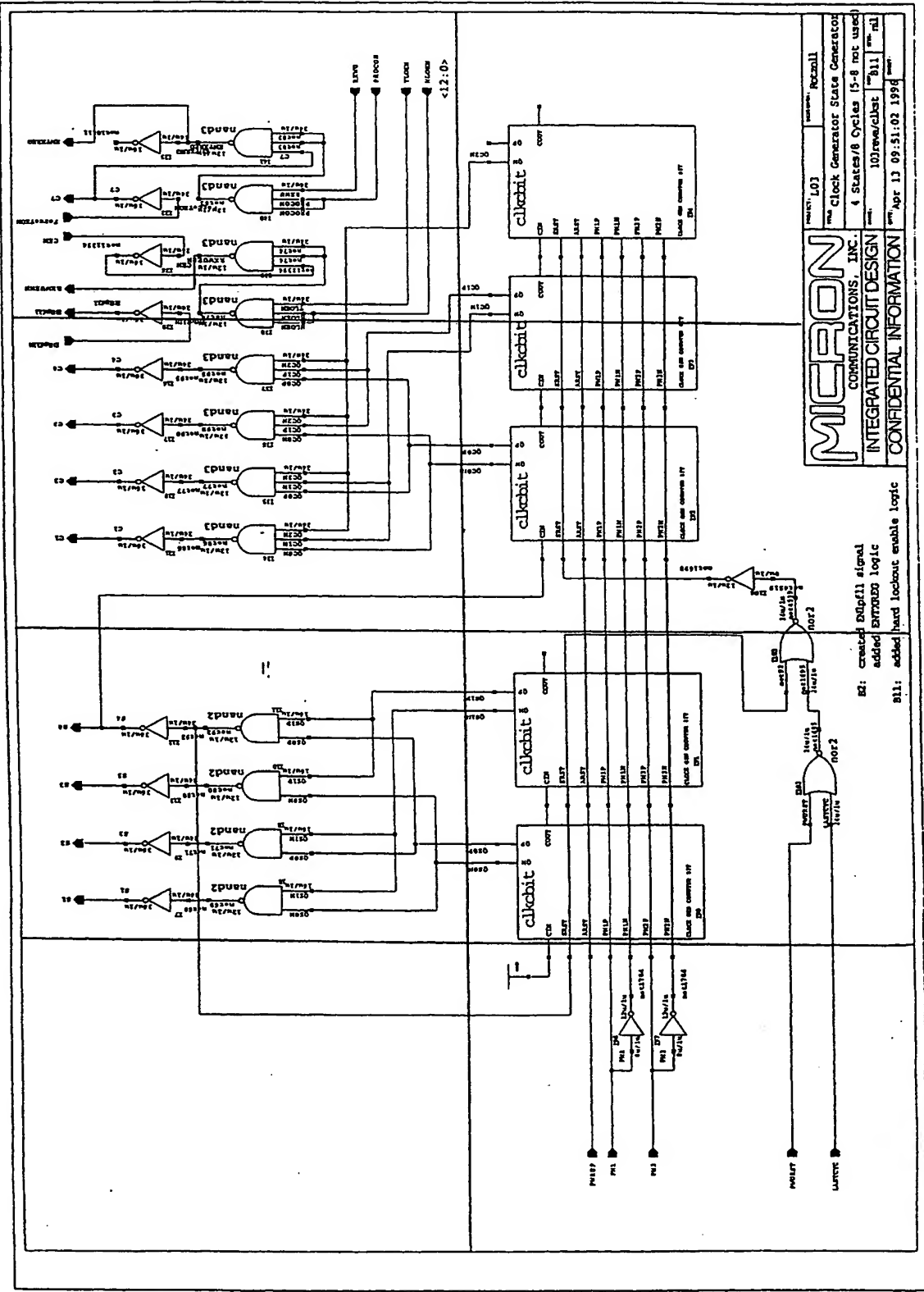
82: Pin name changes
88: Added 6 inverters to non-overlap time
Make the number of inverters adjustable on metal

CONFIDENTIAL INFORMATION
Dec 5 17:55:56 1995

7.0103AA	7.0103AB	7.0103AC	7.0103AD
7.0103BA	7.0103BB	7.0103BC	7.0103BD

7.0103

Fig. 7.0103



MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

PROJECT: L01
Clock Generator State Generator
4 States/8 Cycles (5-8 not used)
101700/clockbit
811
Apr 13 09:51:02 1996

82: created D0pf11 signal
added EXPR000 logic
811: added hard lockout enable logic

7.010301AA	7.010301AB
7.010301BA	7.010301BB

SECRET 7.010301

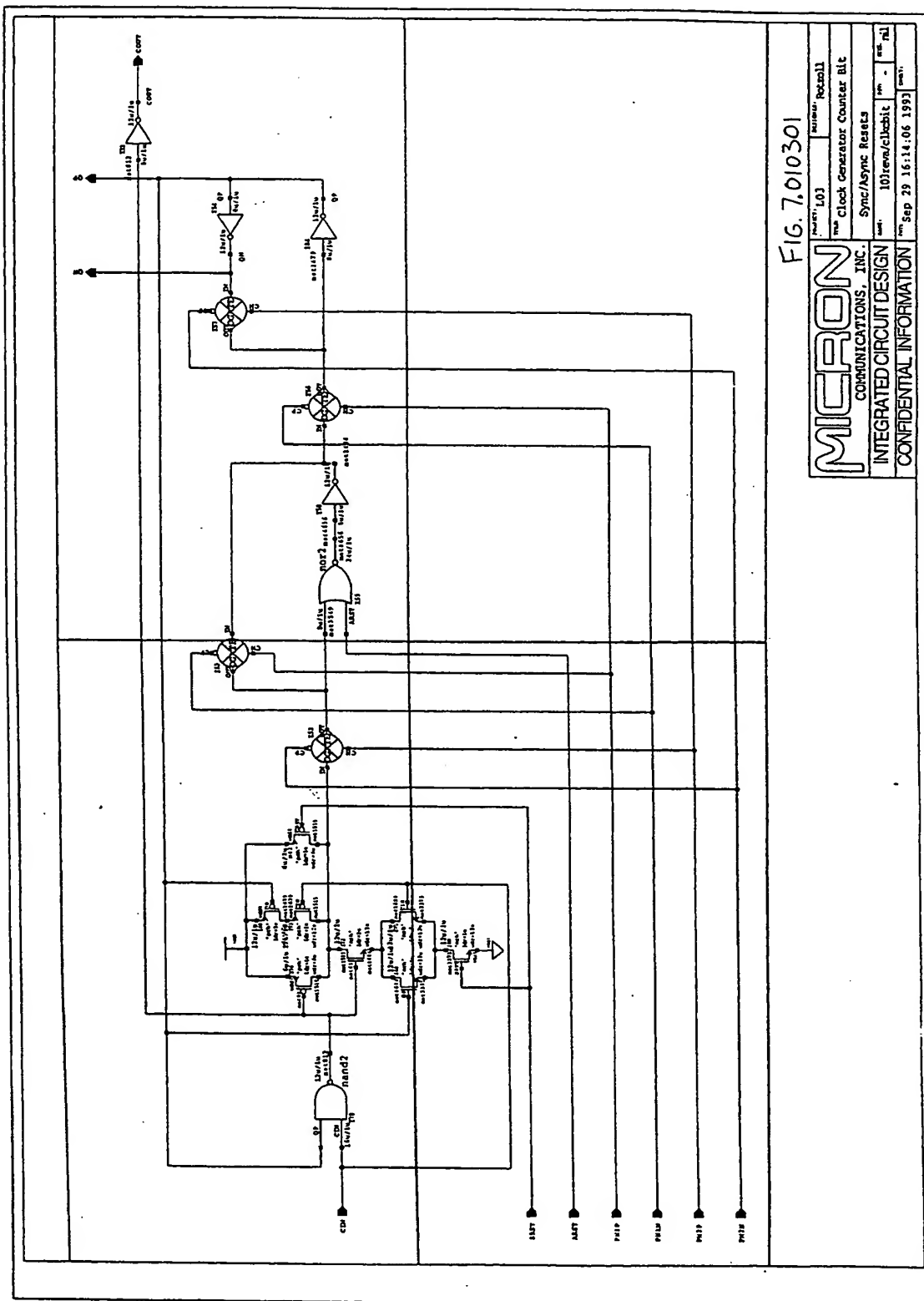


FIG. 7.010301

MICRON	Product: L03	Revision: 100001
	Title: Clock Generator Counter Bit	
	Sync/Async Resets	
	Rate: 103revs/clockbit	pin - 100
INTEGRATED CIRCUIT DESIGN		
CONFIDENTIAL INFORMATION		
Date: Sep 29 16:14:06 1993		

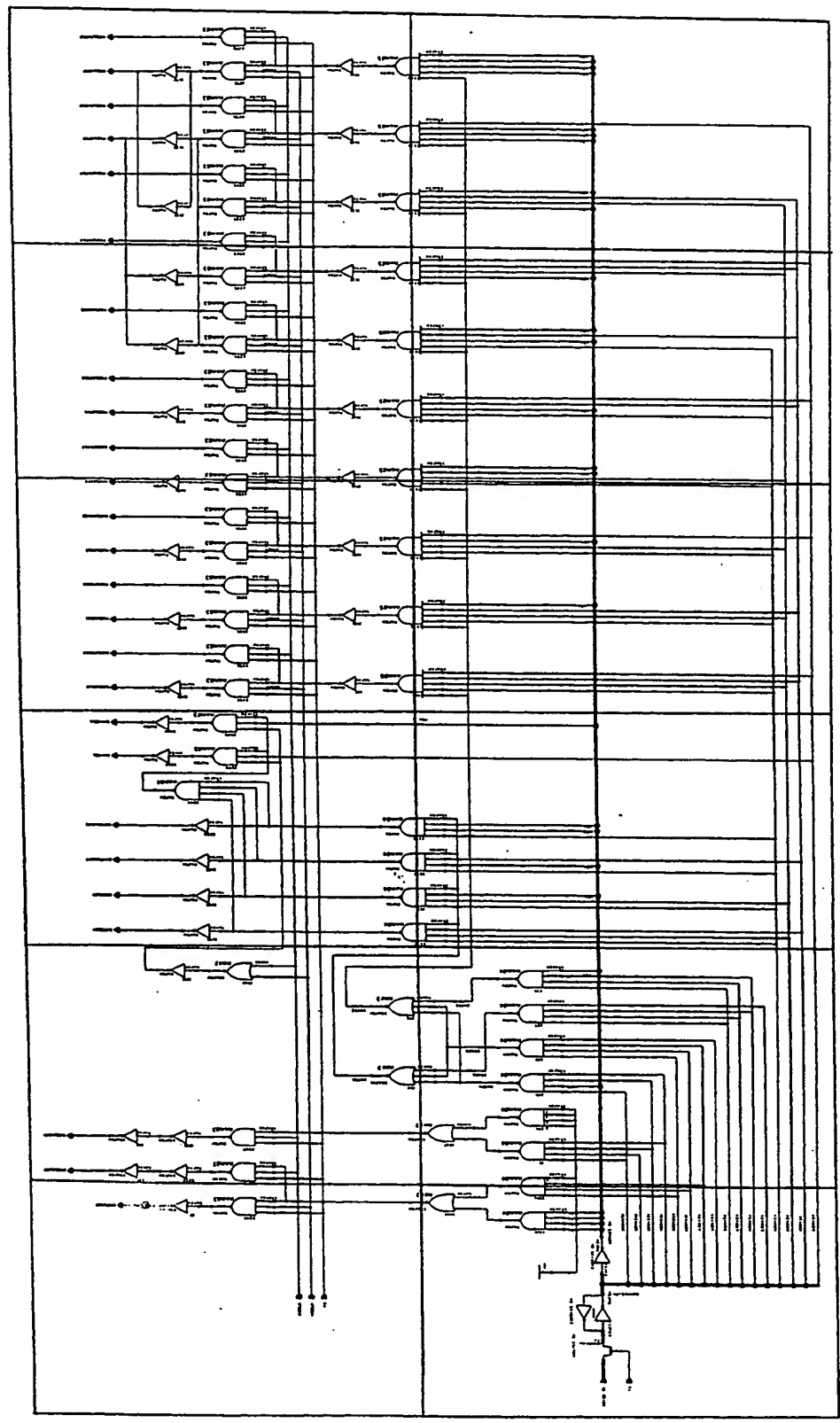
7.02AA	7.02AB	7.02AC	7.02AD	7.02AE	7.02AF
7.02BA	7.02BB	7.02BC	7.02BD	7.02BE	7.02BF

U.S. 100

Fig. 7.02

MICRON	
COMMUNICATIONS, INC.	Address: []
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	

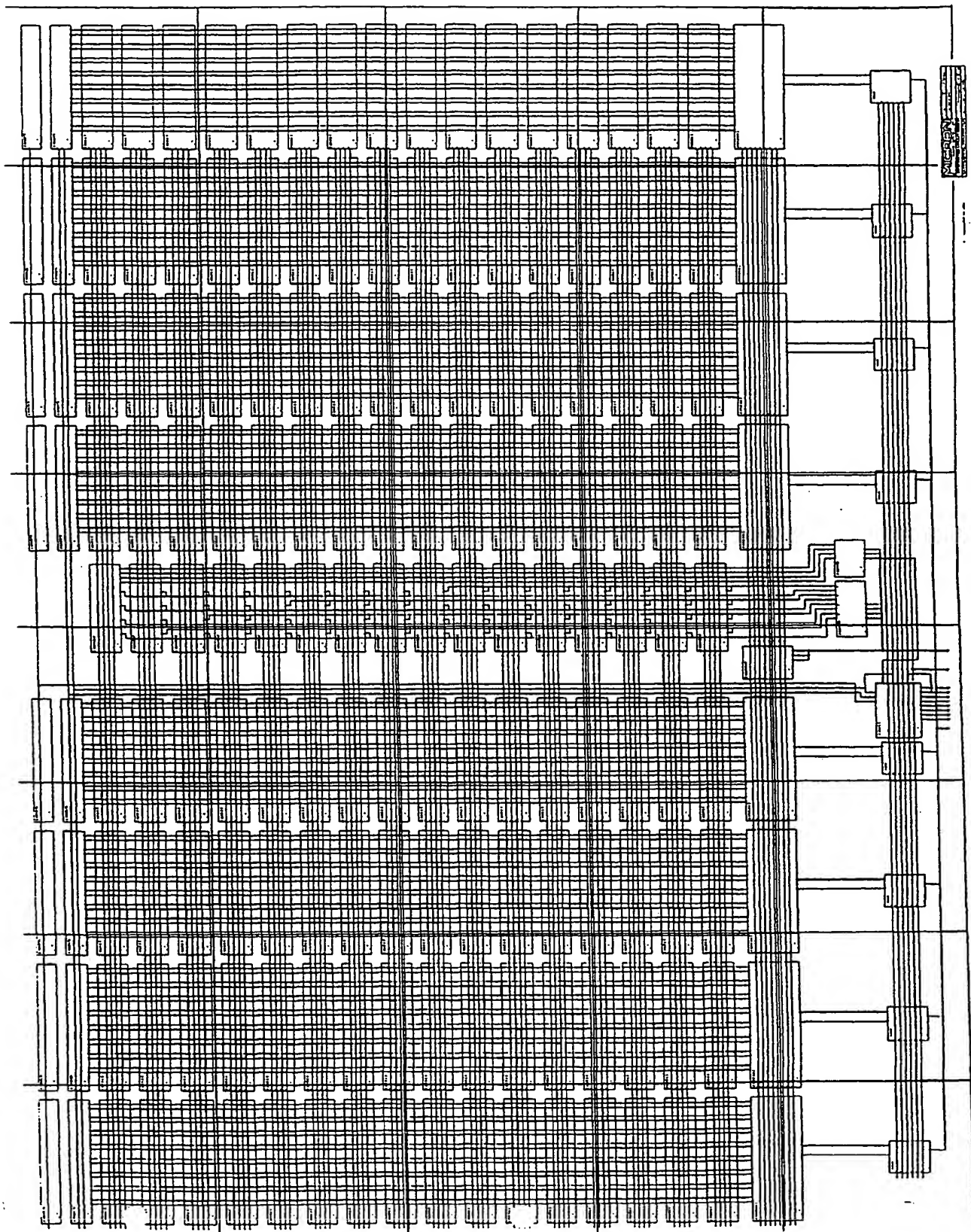
86. 800-111-1111



7.03AA	7.03AB	7.03AC	7.03AD	7.03AE	7.03AF	7.03AG	7.03AH
7.03BA	7.03BB	7.03BC	7.03BD	7.03BE	7.03BF	7.03BG	7.03BH
7.03CA	7.03CB	7.03CC	7.03CD	7.03CE	7.03CF	7.03CG	7.03CH
7.03DA	7.03DB	7.03DC	7.03DD	7.03DE	7.03DF	7.03DG	7.03DH
7.03EA	7.03EB	7.03EC	7.03ED	7.03EE	7.03EF	7.03EG	7.03EH

II II III IIII

Fig 7.03



7.0301AA	7.0301AB
7.0301BA	7.0301BB

THE END OF THE

Handwritten: *Handwritten*

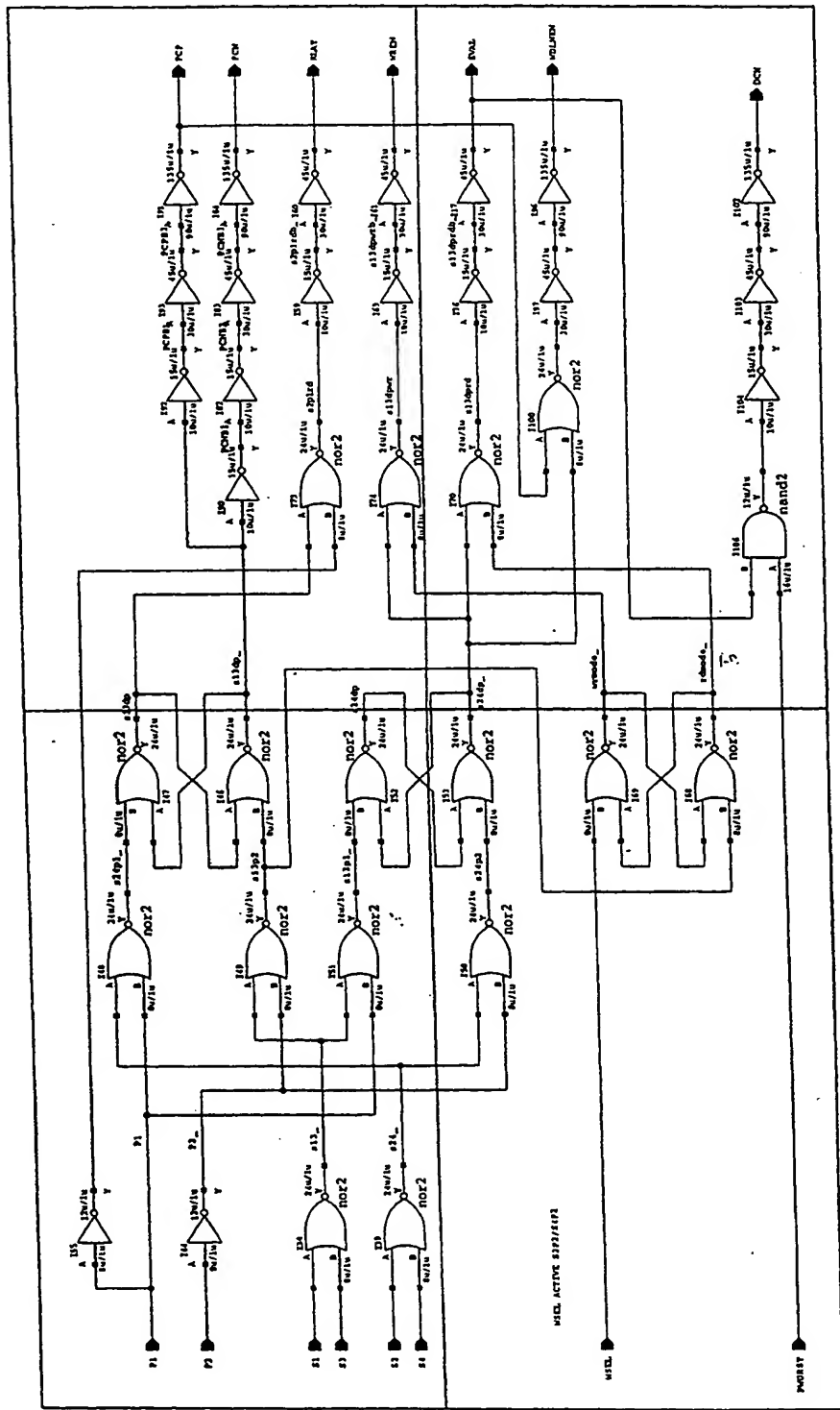


Fig. 7.0301

MICRON		PROJECT: L03 Af3	DESIGNED: Rozell
COMMUNICATIONS, INC.		TMA RAM Control	
INTEGRATED CIRCUIT DESIGN		DATE: 10/19/74	REV: 1
CONFIDENTIAL INFORMATION		DATE: Feb 11 16:47:36 1994	DATE:

7.0302AC

7.0302AB

7.0302AA

EX 1.0302

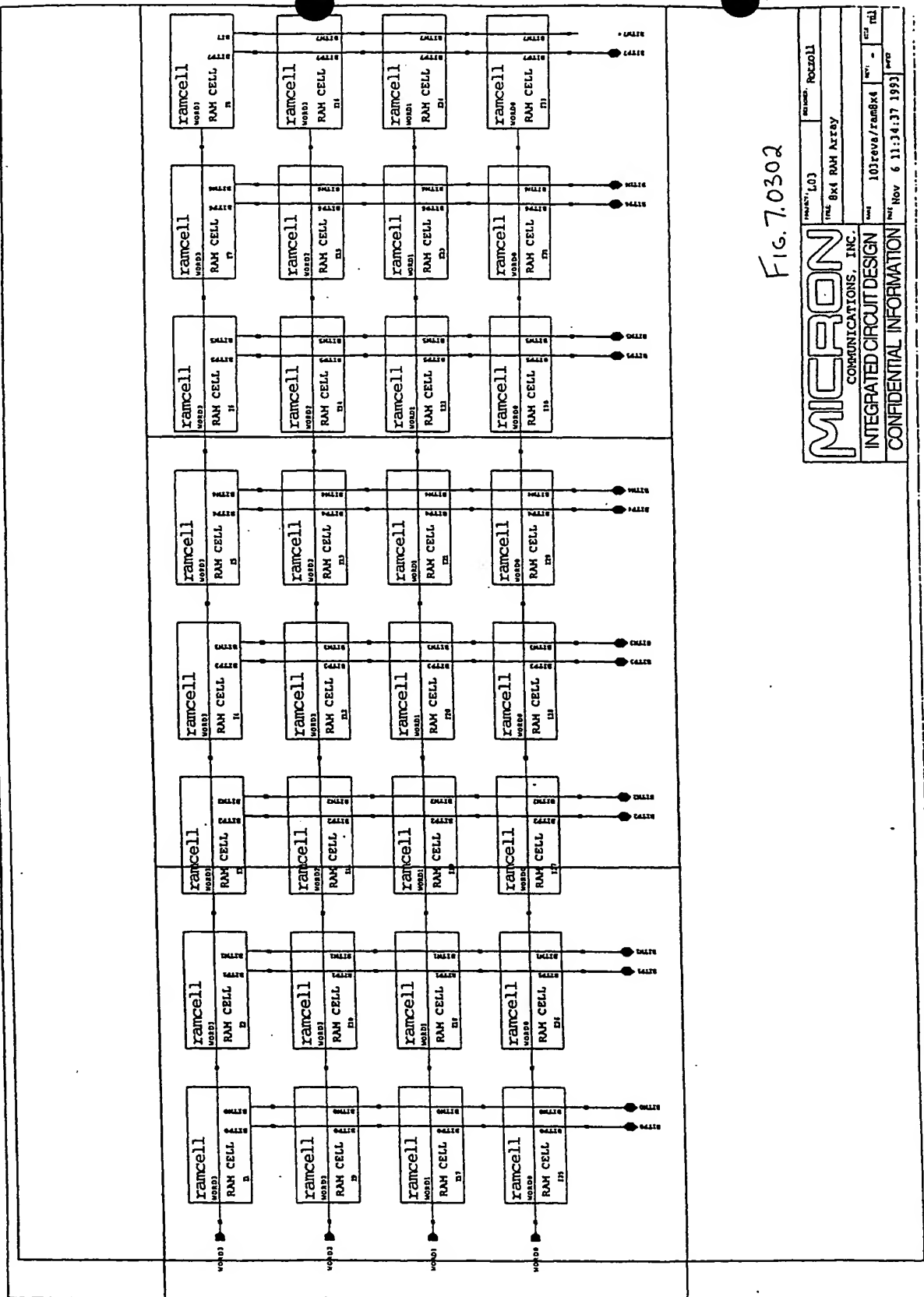


Fig. 7.0302

MICRON		PROJECT: L03	REVISION: Rev 001
COMMUNICATIONS, INC.		12x 8x4 RAM Array	
INTEGRATED CIRCUIT DESIGN		DATE: 10/18/93	REV: 0
CONFIDENTIAL INFORMATION		Nov 6 11:34:37 1993	

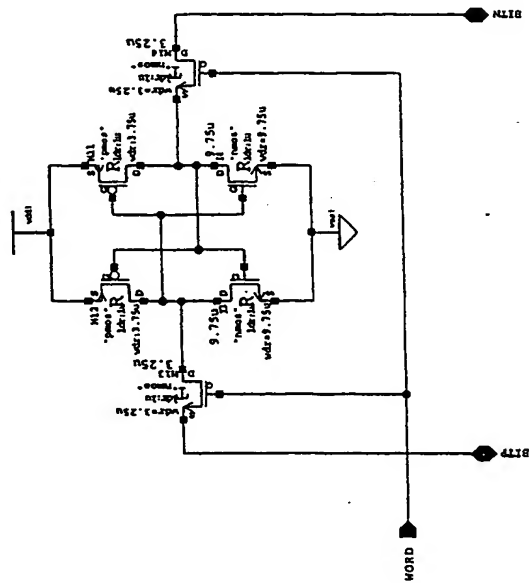


Fig. 7.030201

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: 6T RAM Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/rancell	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 6 11:34:48 1993	SHEET: A

7.0303AA	7.0303AB	7.0303AC	7.0303AD
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SECRET

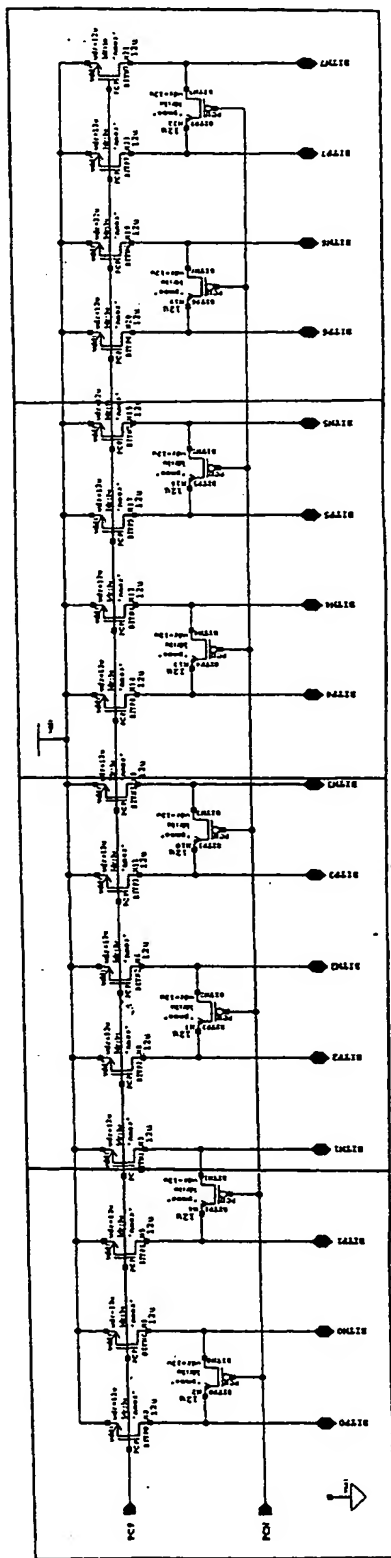


FIG. 7.0303

MICRON COMMUNICATIONS, INC. INTEGRATED CIRCUIT DESIGN CONFIDENTIAL INFORMATION	PART NO. 1403 NAME RAM Precharge	DESIGNED BY Rotzoll DATE 10/19/93 REV. 1
	PROJECT 1019eva/rampch DATE Nov 12 02:58:36 1993	DESIGNED BY DATE
	PROJECT 1019eva/rampch DATE Nov 12 02:58:36 1993	DESIGNED BY DATE
	PROJECT 1019eva/rampch DATE Nov 12 02:58:36 1993	DESIGNED BY DATE

7.0304AA	7.0304AB	7.0304AC	7.0304AD
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EX-70304

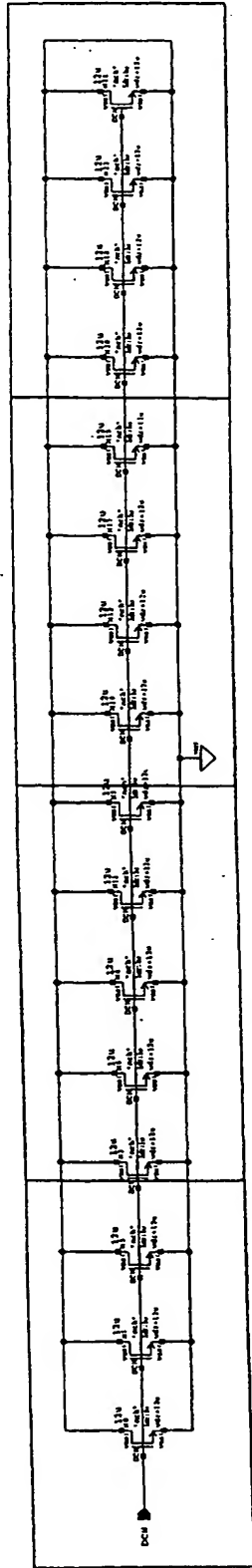
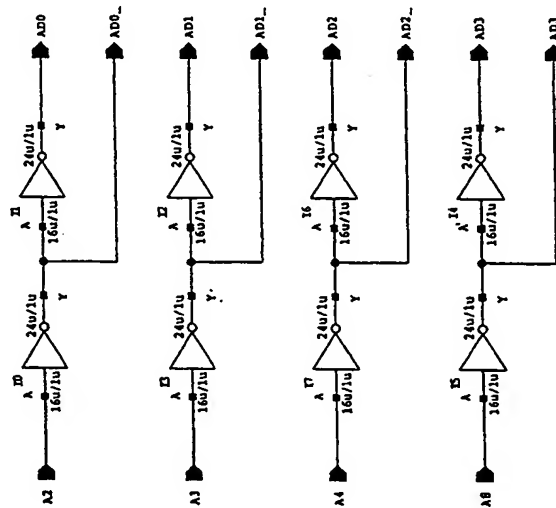


FIG. 7.0304

MICRON		PROPERTY L03	SECTION: J07000LE
COMMUNICATIONS, INC.		NAME: RAM Precharge	
INTEGRATED CIRCUIT DESIGN		DATE: 103revA/randch	REV: B8
CONFIDENTIAL INFORMATION		DATE: Jan 28 09:51:27 1996	REV: n1

B8: disconnected dch devices from bit lines and tied to vss



PROJECT: L03		DESIGNER: Rotzoll	
TITLE: RAM Address Buffer			
NAME:	103revA/ramadb	REV:	-
DATE:	Sep 29 16:04:01 1993	SIZE:	A
CONFIDENTIAL INFORMATION			

FIG. 7.0305

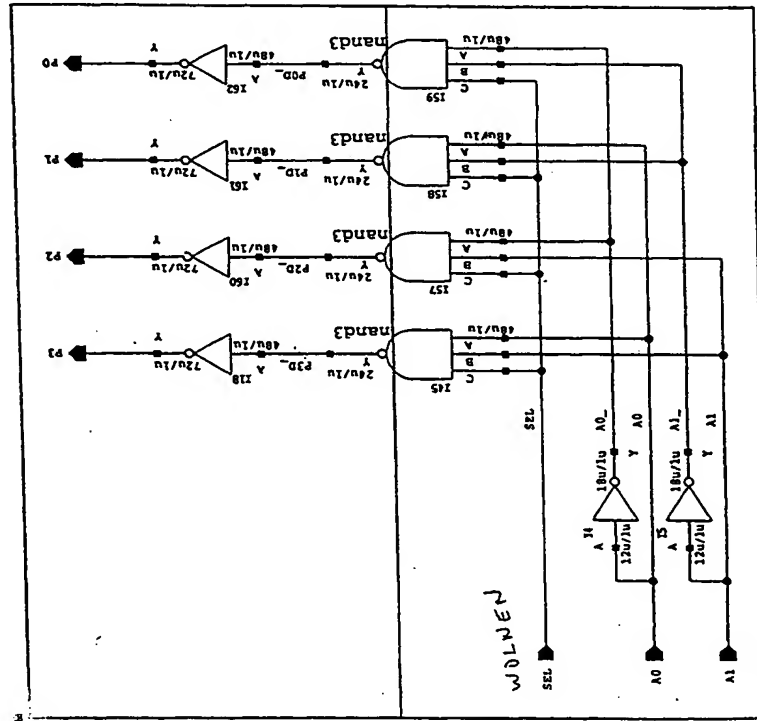
MI40-030

7.0306AA

7.0306BA

7.0306 7.0306

FIG. 7.0306



PROJECT: L03		DESIGNER: Rotzoll	
TITLE: RAM Word Line Driver			
MADE: 103reva/ramwdr		REV: -	STD: A
DATE: Sep 29 16:04:16 1993		PART:	

MICRON

COMMUNICATIONS, INC.

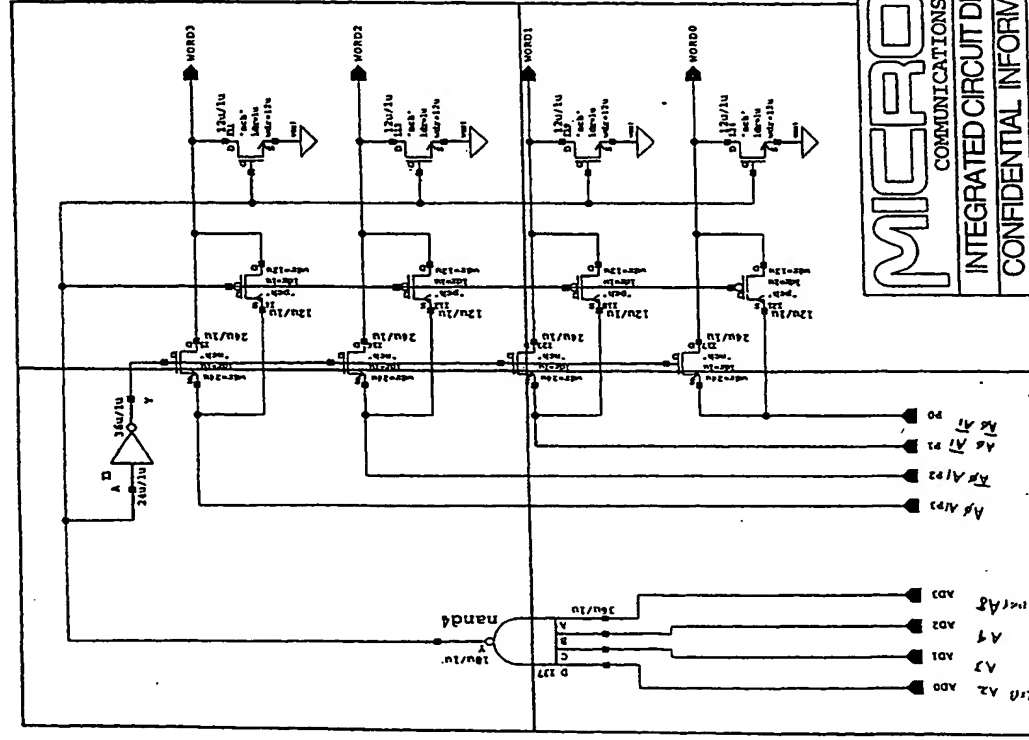
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

7.0307AA	7.0307AB
7.0307BA	7.0307BB

7.0307

Fig. 7.0307



MICRON		PROJECT: L03	DESIGNED: Rotzoll
COMMUNICATIONS, INC.		TITLE: RAM Word Line Decoder	
INTEGRATED CIRCUIT DESIGN		DATE: 103reva/ramwdec	REV: -
CONFIDENTIAL INFORMATION		DATE: Sep 29 15:41:08 1993	REV: A

MI40-030

7.0308AA	7.0308AB
7.0308BA	7.0308BB

7.0308

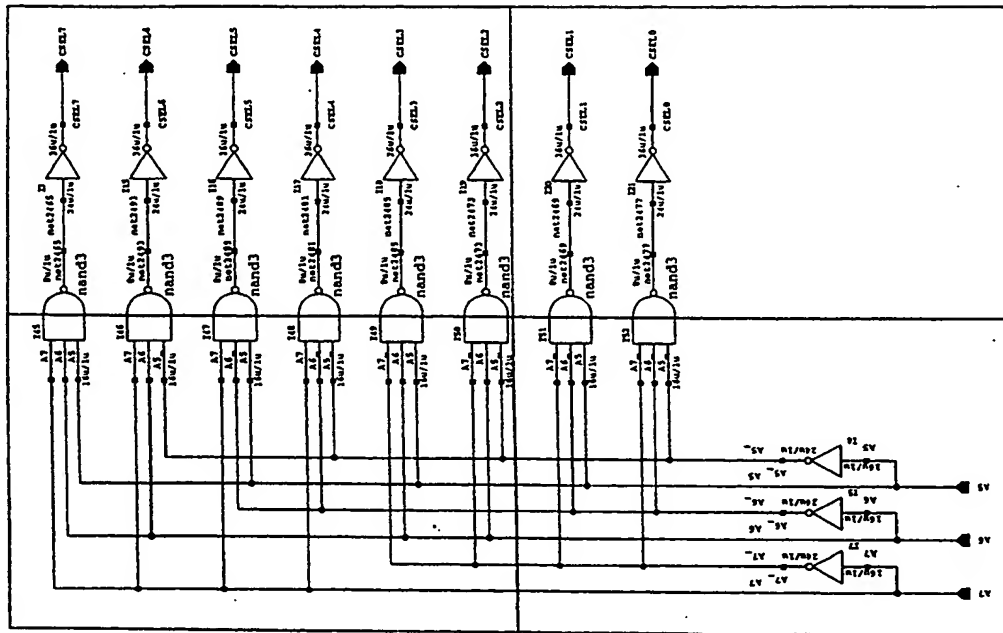


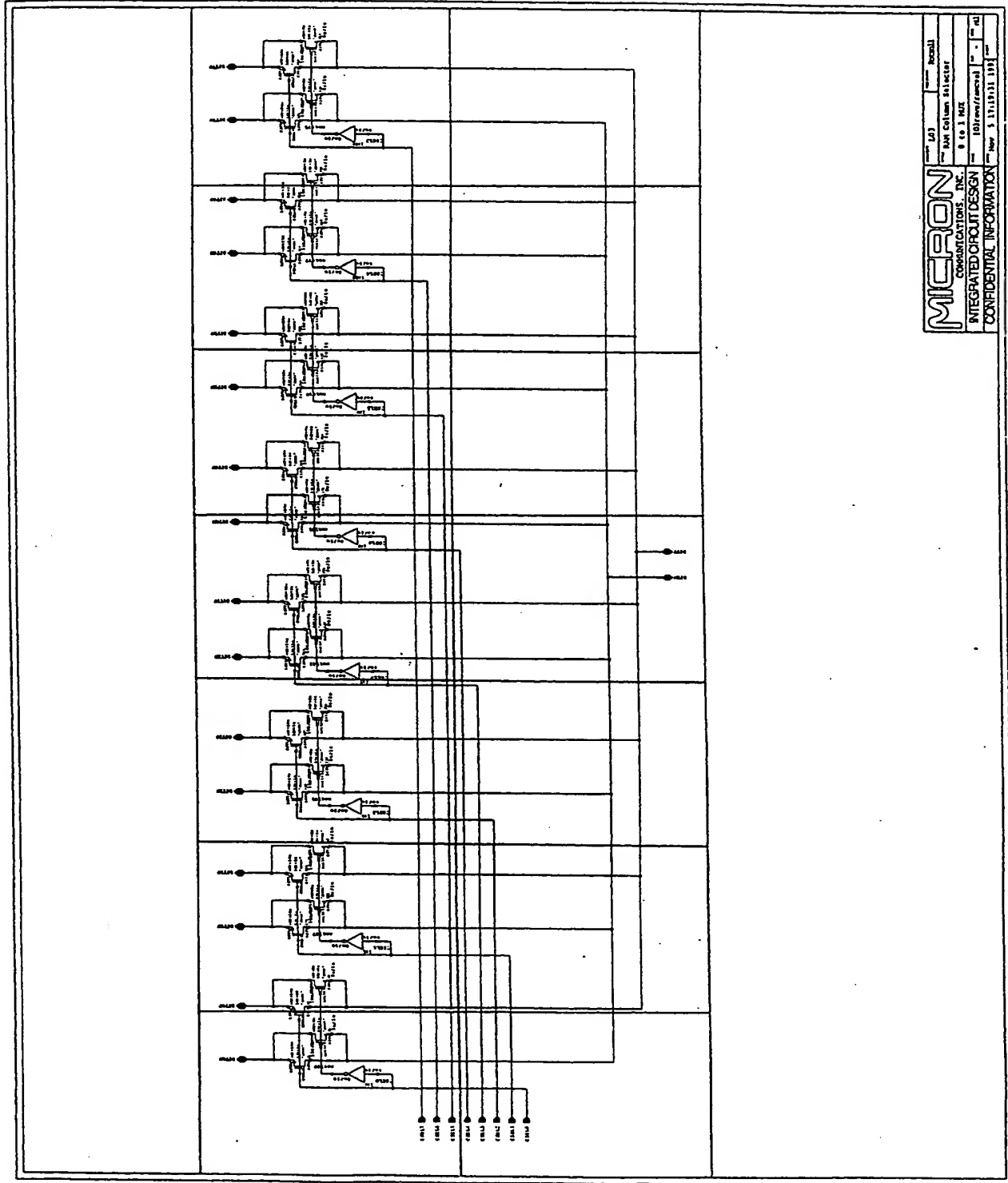
FIG. 7.0308

MICRON		PRODUCT L03	REVISION: Rev2011
COMMUNICATIONS, INC.		RAM Column Select Decode	
INTEGRATED CIRCUIT DESIGN		3 to 8	REV. 111
CONFIDENTIAL INFORMATION		103rev0/rancdec	REV. 111
		Nov 5 17:21:07 1993	REV. 111

7.0309AA	7.0309AB	7.0309AC	7.0309AD	7.0309AE	7.0309AF	7.0309AG
7.0309BA	7.0309BB	7.0309BC	7.0309BD	7.0309BE	7.0309BF	7.0309BG

EE 7.0309

Fig. 7.0309



MICRON	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part No.	7.0309
Rev.	1
Date	11/10/71
By	11/10/71
Check	11/10/71
Appr.	11/10/71
Design	11/10/71
Test	11/10/71
Prod.	11/10/71
Dist.	11/10/71
Recd.	11/10/71
Shipped	11/10/71
Returned	11/10/71
Disposed	11/10/71
Other	11/10/71

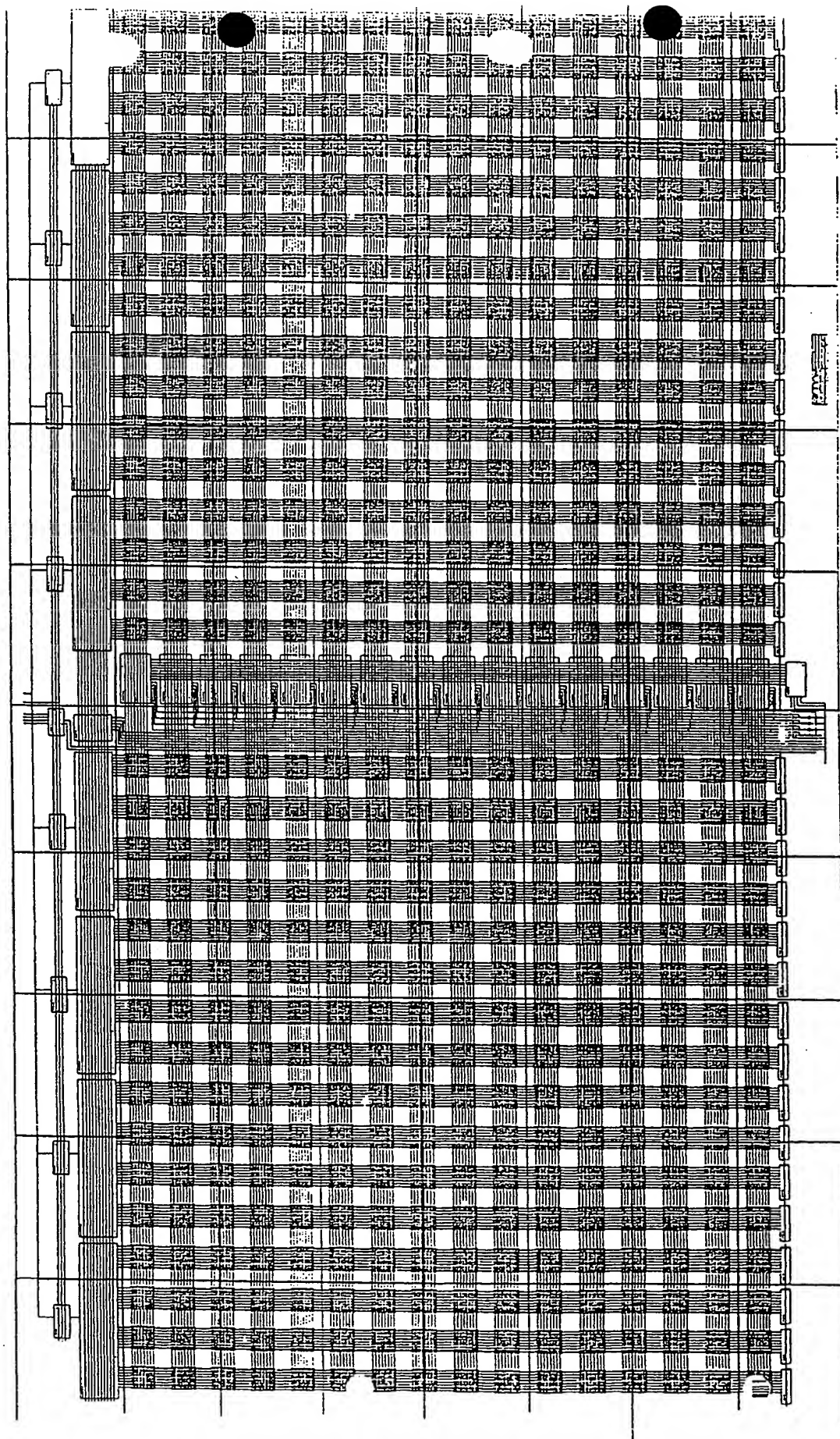
7.0310AA	7.0310AB
7.0310BA	7.0310BB

WILEY-LISS

7.04AA	7.04AB	7.04AC	7.04AD	7.04AE	7.04AF	7.04AG	7.04AH	7.04AI	7.04AJ
7.04BA	7.04BB	7.04BC	7.04BD	7.04BE	7.04BF	7.04BG	7.04BH	7.04BI	7.04BJ
7.04CA	7.04CB	7.04CC	7.04CD	7.04CE	7.04CF	7.04CG	7.04CH	7.04CI	7.04CJ
7.04DA	7.04DB	7.04DC	7.04DD	7.04DE	7.04DF	7.04DG	7.04DH	7.04DI	7.04DJ
7.04EA	7.04EB	7.04EC	7.04ED	7.04EE	7.04EF	7.04EG	7.04EH	7.04EI	7.04EJ
7.04FA	7.04FB	7.04FC	7.04FD	7.04FE	7.04FF	7.04FG	7.04FH	7.04FI	7.04FJ
7.04GA	7.04GB	7.04GC	7.04GD	7.04GE	7.04GF	7.04GG	7.04GH	7.04GI	7.04GJ
7.04HA	7.04HB	7.04HC	7.04HD	7.04HE	7.04HF	7.04HG	7.04HH	7.04HI	7.04HJ

7.0400-030

FIG. 7.04

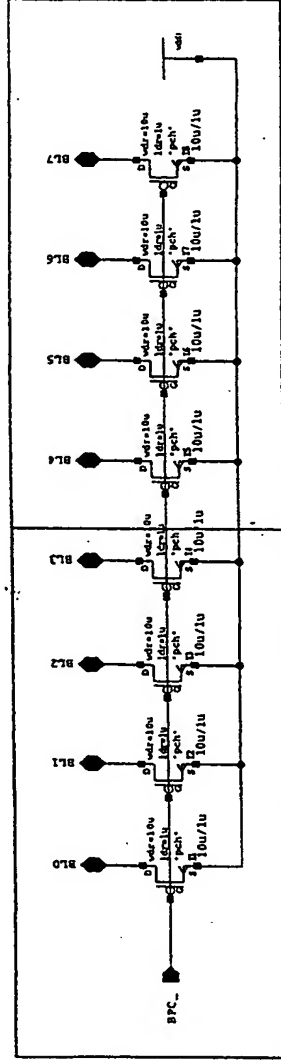


7.0401AA	7.0401AB
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EX-7.0401

7.0402AA	7.0402AB
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EE 7.0402





COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

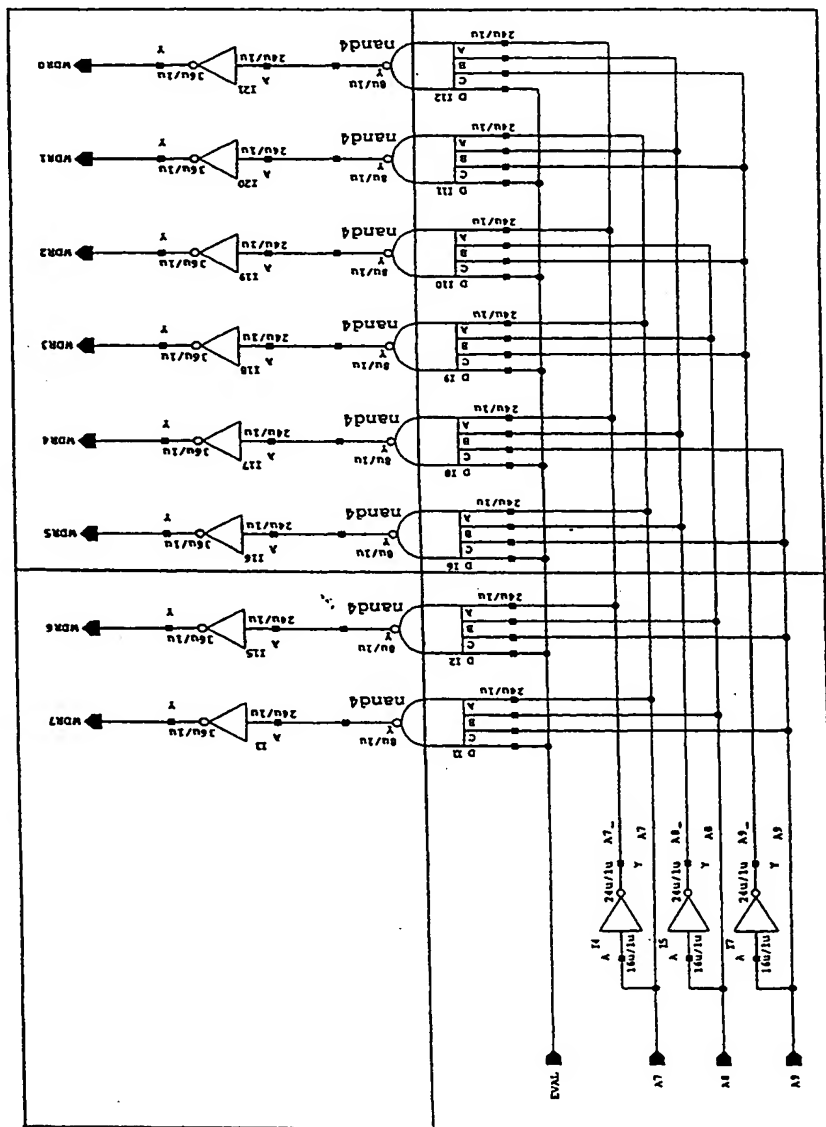
PROJECT: L03	DESIGNER: Rotzoll
TITLE: ROM Bit Line Precharge	
NAME: 103reva/rompch	REV: -
DATE: Oct 7 18:09:48 1993	SIZE: A
	SHEET: 1

FIG. 7.0402

7.0403AA	7.0403AB
7.0403BA	7.0403BB

7.0403

Fig. 7.0903



MICRON		PROJECT: L03	DESIGNER: Rotzoll	
COMMUNICATIONS, INC.		TITLE: ROM Word Line Driver		
INTEGRATED CIRCUIT DESIGN		DATE: 103revA/romwdr	REV: -	SHEET: A
CONFIDENTIAL INFORMATION		DATE: Oct 7 18:11:34 1993		

	7.0404AB	7.0404AC
7.0404BA	7.0404BB	7.0404BC
	7.0404CB	7.0404CC
	7.0404DB	7.0404DC

II II II 7.040404

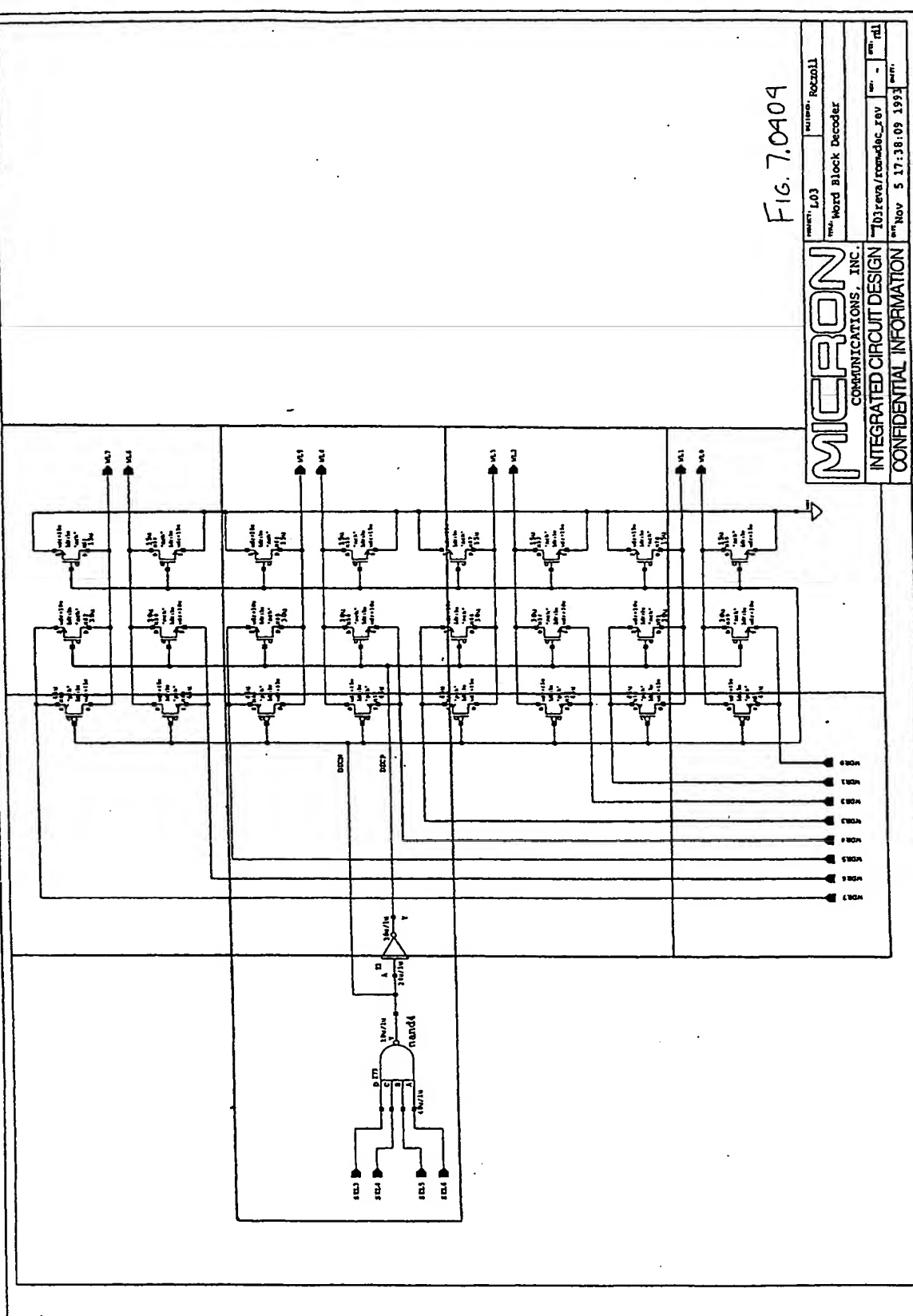


FIG. 7.0404

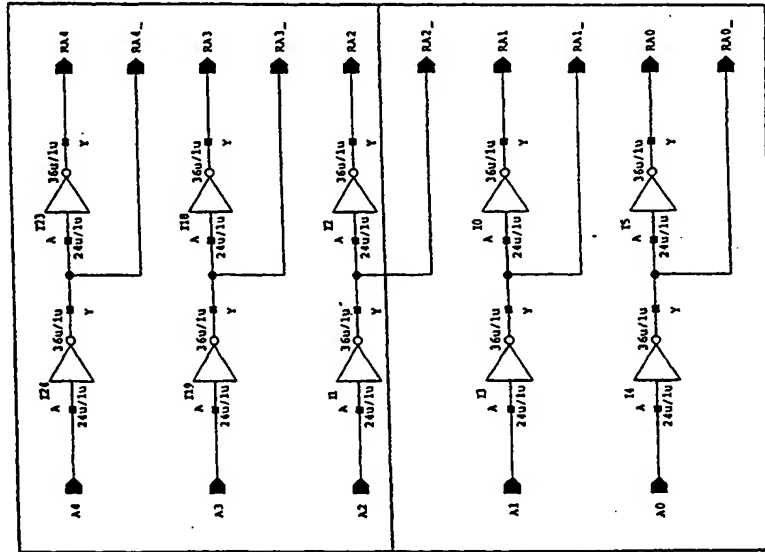
MICRON COMMUNICATIONS, INC. INTEGRATED CIRCUIT DESIGN	PROJECT: L03	REVISION: Rev2011
	NEW Word Block Decoder	
	DATE: 10/11/93	BY: rll
CONFIDENTIAL INFORMATION		

7.0405AA

7.0405BA

ISS 7.0405

Fig. 7.0405



<div>MICRON</div> <div>COMMUNICATIONS, INC.</div>				PROJECT: L03	DESIGNER: Rotzoll
				TITLE: ROM Bit Line Address Driver	
				NUMBER: 103reva/rombldr	REV: - CODE: A
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION				DATE: Oct 7 12:08:42 1993	
				SHEET: 1	

MICRON
COMMUNICATIONS, INC.

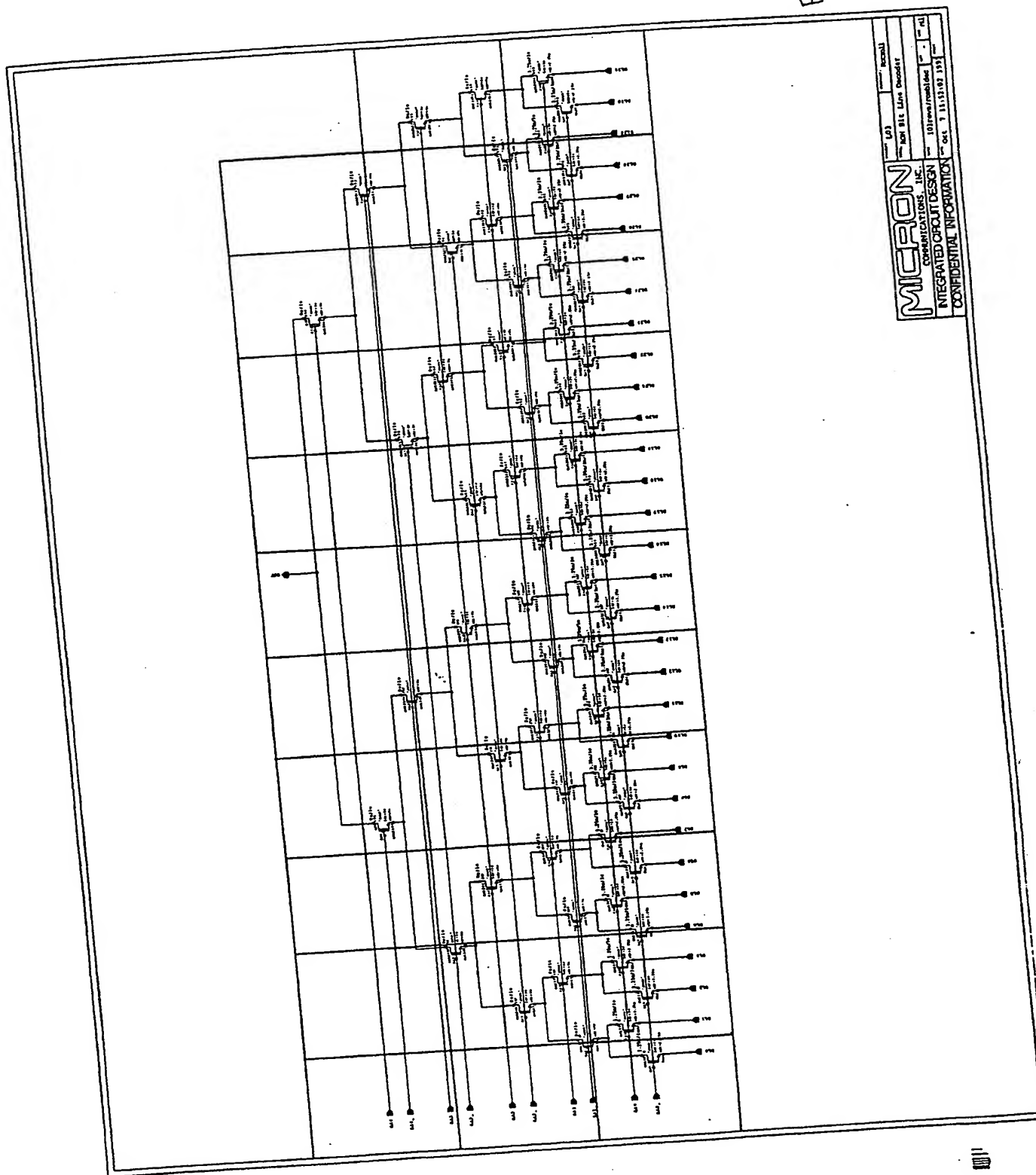
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

7.0406AA	7.0406AB	7.0406AC	7.0406AD	7.0406AE	7.0406AF	7.0406AG	7.0406AH	7.0406AI	7.0406AJ	
7.0406BA	7.0406BB	7.0406BC	7.0406BD	7.0406BE	7.0406BF	7.0406BG	7.0406BH	7.0406BI	7.0406BJ	7.0406BK
7.0406CA	7.0406CB	7.0406CC	7.0406CD	7.0406CE	7.0406CF	7.0406CG	7.0406CH	7.0406CI	7.0406CJ	7.0406CK

ILH 037 7.0406

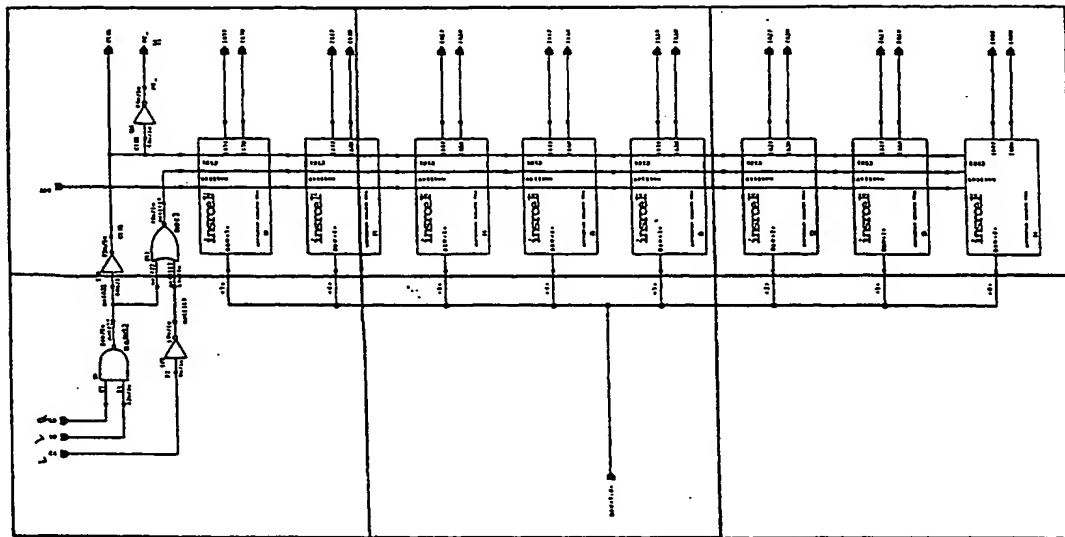
FIG. 7.0406



7.05AA	7.05AB
7.05BA	7.05BB
7.05CA	7.05CB

Fig 7.05

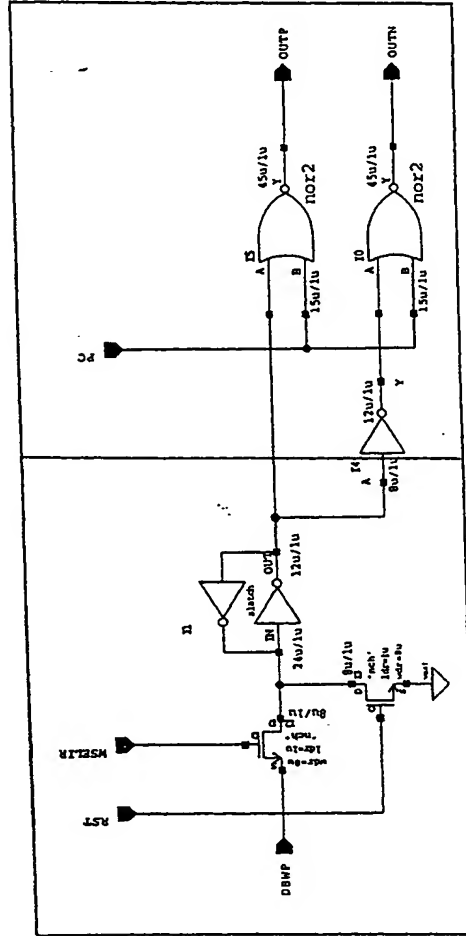
Fig. 7.05



7.0501AA	7.0501AB
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EX 7.0501

Fig. 7.0501



PROJECT: L03		DESIGNED: Rotzoll	
TITLE: Instruction Register Cell		REV: -	
NUMBER: 103reva/insrcel		DATE: Oct 5 20:12:49 1993	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
MICRON COMMUNICATIONS, INC.		REV: A	

7.06AA	7.06AB	7.06AC	7.06AD	7.06AE	7.06AF	7.06AG	7.06AH	7.06AI	7.06AJ	7.06AK	7.06AL	7.06AM	7.06AN
	7.06BB	7.06BC	7.06BD	7.06BE	7.06BF	7.06BG	7.06BH	7.06BI	7.06BJ	7.06BK	7.06BL	7.06BM	7.06BN
7.06CA	7.06CB	7.06CC	7.06CD	7.06CE	7.06CF	7.06CG	7.06CH	7.06CI	7.06CJ	7.06CK	7.06CL	7.06CM	7.06CN

II II 7.006

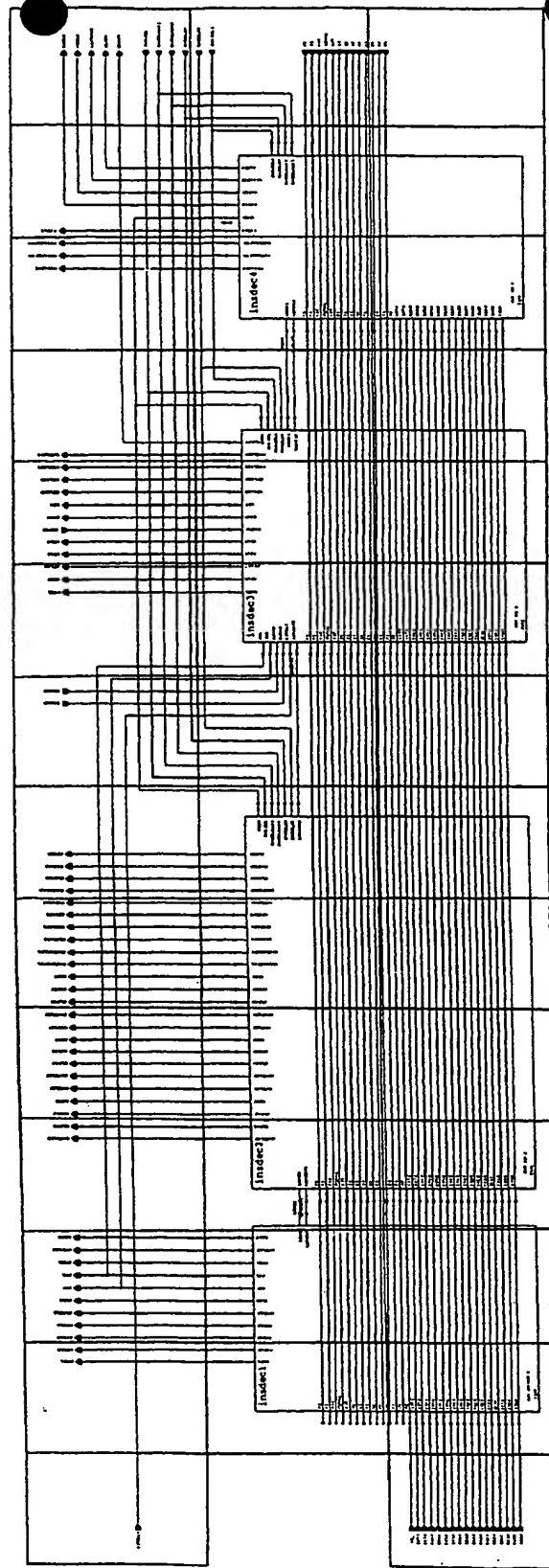
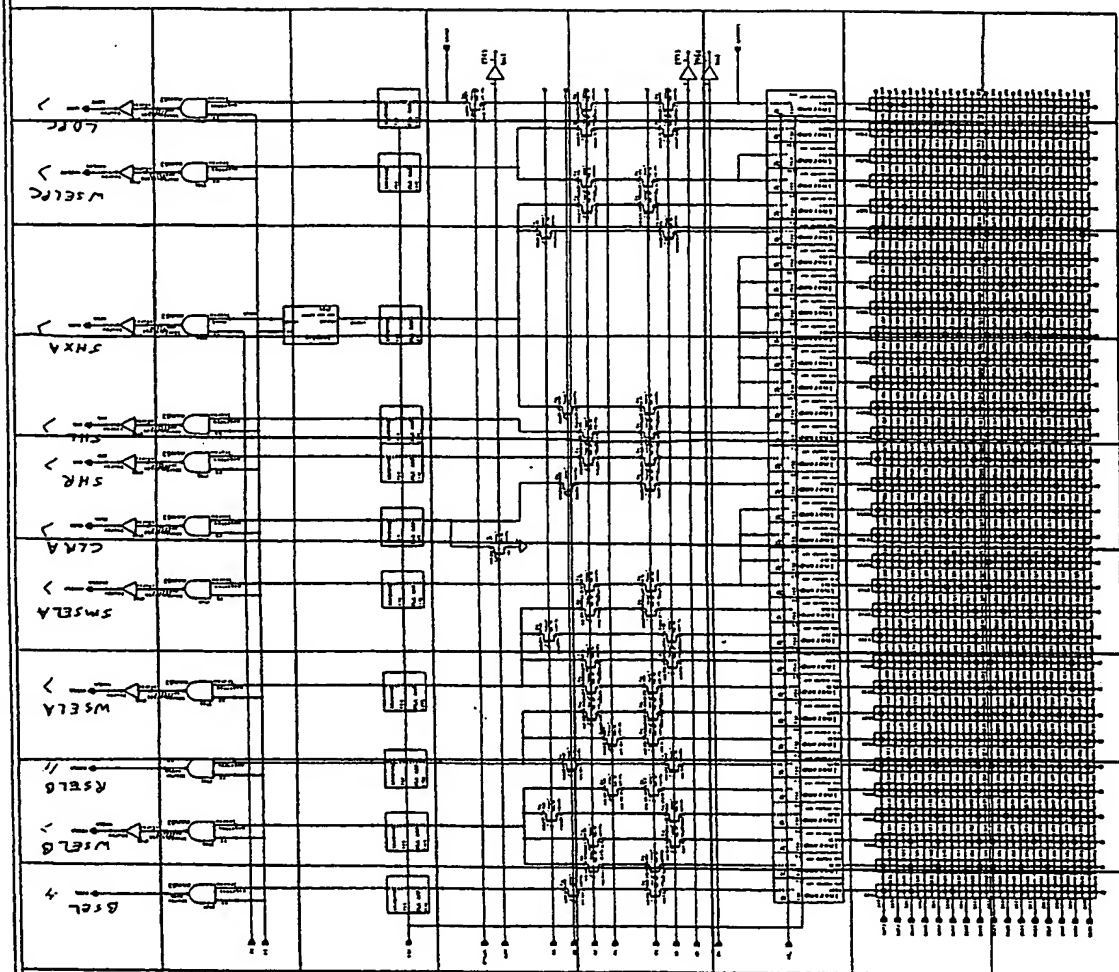


Fig. 7.06

7.0601AA	7.0601AB	7.0601AC	7.0601AD	7.0601AE	7.0601AF	7.0601AG	7.0601AH	7.0601AI
7.0601BA	7.0601BB	7.0601BC	7.0601BD	7.0601BE	7.0601BF	7.0601BG	7.0601BH	7.0601BI
7.0601CA	7.0601CB	7.0601CC	7.0601CD	7.0601CE	7.0601CF	7.0601CG	7.0601CH	7.0601CI
7.0601DA	7.0601DB	7.0601DC	7.0601DD	7.0601DE	7.0601DF	7.0601DG	7.0601DH	7.0601DI
7.0601EA	7.0601EB	7.0601EC	7.0601ED	7.0601EE	7.0601EF	7.0601EG	7.0601EH	7.0601EI
7.0601FA	7.0601FB	7.0601FC	7.0601FD	7.0601FE	7.0601FF	7.0601FG	7.0601FH	7.0601FI
7.0601GA	7.0601GB	7.0601GC	7.0601GD	7.0601GE	7.0601GF	7.0601GG	7.0601GH	7.0601GI
7.0601HA	7.0601HB	7.0601HC	7.0601HD	7.0601HE	7.0601HF	7.0601HG	7.0601HH	7.0601HI

II II II 7.0601 II

FIG. 7.0601



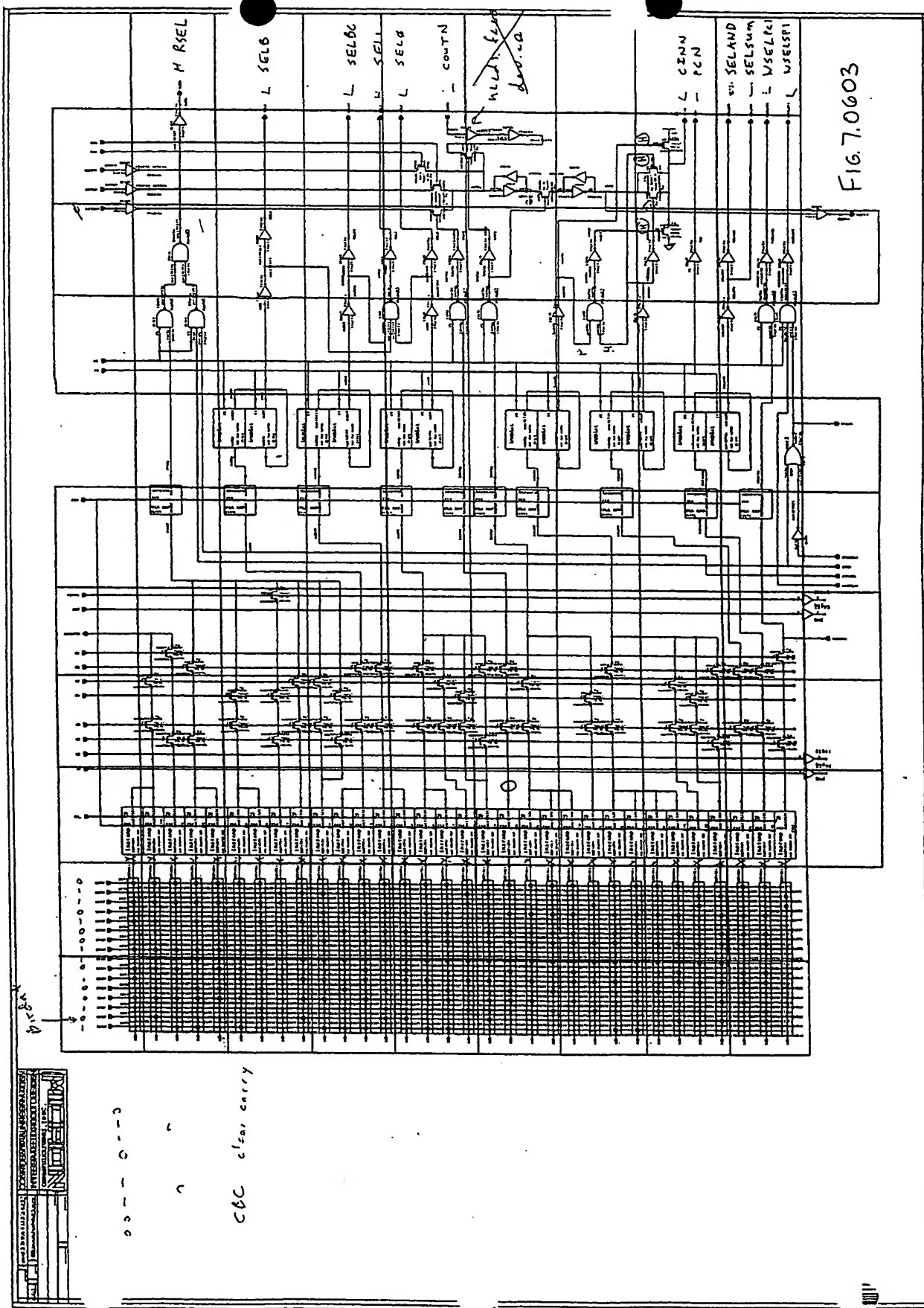
7.0602AA	7.0602AB	7.0602AC	7.0602AD	7.0602AE	7.0602AF	7.0602AG	7.0602AH
7.0602BA	7.0602BB	7.0602BC	7.0602BD	7.0602BE	7.0602BF	7.0602BG	7.0602BH
7.0602CA	7.0602CB	7.0602CC	7.0602CD	7.0602CE	7.0602CF	7.0602CG	7.0602CH
7.0602DA	7.0602DB	7.0602DC	7.0602DD	7.0602DE	7.0602DF	7.0602DG	7.0602DH
7.0602EA	7.0602EB	7.0602EC	7.0602ED	7.0602EE	7.0602EF	7.0602EG	7.0602EH
7.0602FA	7.0602FB	7.0602FC	7.0602FD	7.0602FE	7.0602FF	7.0602FG	7.0602FH
7.0602GA	7.0602GB	7.0602GC	7.0602GD	7.0602GE	7.0602GF	7.0602GG	7.0602GH
7.0602HA	7.0602HB	7.0602HC	7.0602HD	7.0602HE	7.0602HF	7.0602HG	7.0602HH
		7.0602IC	7.0602ID	7.0602IE	7.0602IF	7.0602IG	7.0602IH
		7.0602JC	7.0602JD	7.0602JE	7.0602JF	7.0602JG	7.0602JH

7.0602

Fig. 7.0602

7.0603AA	7.0603AB	7.0603AC	7.0603AD	7.0603AE	7.0603AF	7.0603AH	7.0603AI	7.0603AJ
7.0603BA	7.0603BB	7.0603BC	7.0603BD	7.0603BE	7.0603BF	7.0603BG	7.0603BI	7.0603BJ
7.0603CA	7.0603CB	7.0603CC	7.0603CD	7.0603CE	7.0603CF	7.0603CG	7.0603CI	7.0603CJ
7.0603DA	7.0603DB	7.0603DC	7.0603DD	7.0603DE	7.0603DF	7.0603DG	7.0603DI	7.0603DJ
7.0603EA	7.0603EB	7.0603EC	7.0603ED	7.0603EE	7.0603EF	7.0603EG	7.0603EI	7.0603EJ
7.0603FA	7.0603FB	7.0603FC	7.0603FD	7.0603FE	7.0603FF	7.0603FG	7.0603FI	7.0603FJ
7.0603GA	7.0603GB	7.0603GC	7.0603GD	7.0603GE	7.0603GF	7.0603GG	7.0603GI	7.0603GJ
7.0603HA	7.0603HB	7.0603HC	7.0603HD	7.0603HE	7.0603HF	7.0603HG	7.0603HI	7.0603HJ
7.0603IA	7.0603IB	7.0603IC	7.0603ID	7.0603IE	7.0603IF	7.0603IG	7.0603IH	7.0603IJ
		7.0603JC	7.0603JD	7.0603JE	7.0603JF	7.0603JG	7.0603JI	
								7.0603BK


 DEPARTMENT OF DEFENSE



7.0604AA	7.0604AB	7.0604AC	7.0604AD	7.0604AE	7.0604AF	7.0604AG	7.0604AH	7.0604AI
7.0604BA	7.0604BB	7.0604BC	7.0604BD	7.0604BE	7.0604BF	7.0604BG	7.0604BH	7.0604BI
7.0604CA	7.0604CB	7.0604CC	7.0604CD	7.0604CE	7.0604CF	7.0604CG	7.0604CH	7.0604CI
7.0604DA	7.0604DB	7.0604DC	7.0604DD	7.0604DE	7.0604DF	7.0604DG	7.0604DH	7.0604DI
7.0604EA	7.0604EB	7.0604EC	7.0604ED	7.0604EE	7.0604EF	7.0604EG	7.0604EH	7.0604EI
7.0604FA	7.0604FB	7.0604FC	7.0604FD	7.0604FE	7.0604FF	7.0604FG	7.0604FH	7.0604FI
7.0604GA	7.0604GB	7.0604GC	7.0604GD	7.0604GE	7.0604GF	7.0604GG	7.0604GH	7.0604GI
7.0604HA	7.0604HB	7.0604HC	7.0604HD	7.0604HE	7.0604HF	7.0604HG		
7.0604IA	7.0604IB	7.0604IC	7.0604ID	7.0604IE	7.0604IF	7.0604IG		
7.0604JA	7.0604JB	7.0604JC	7.0604JD	7.0604JE	7.0604JF	7.0604JG	7.0604JH	7.0604JI

[illegible]

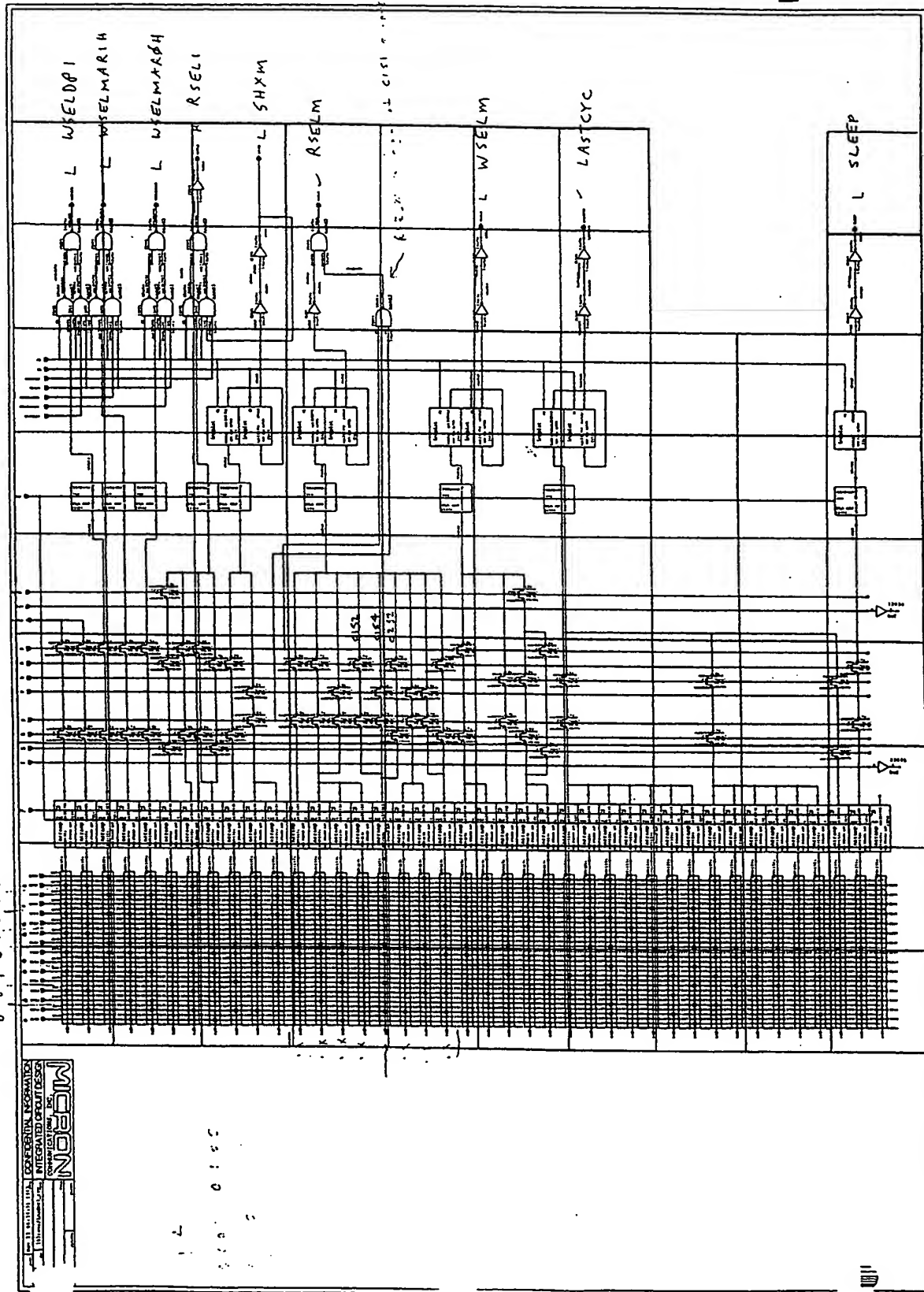


FIG. 7.06

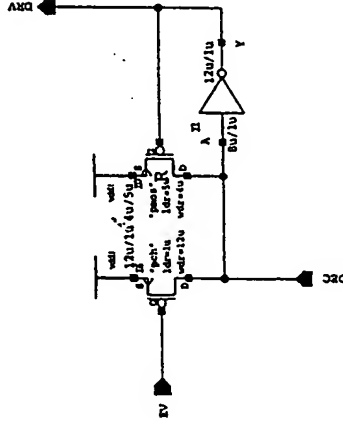
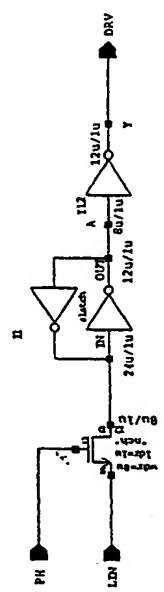


Fig. 7.060402

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Instruction Decoder PLA Amp	
INTEGRATED CIRCUIT DESIGN		MODEL: 103reva/inspamp	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 6 12:21:34 1993	SIZE: A

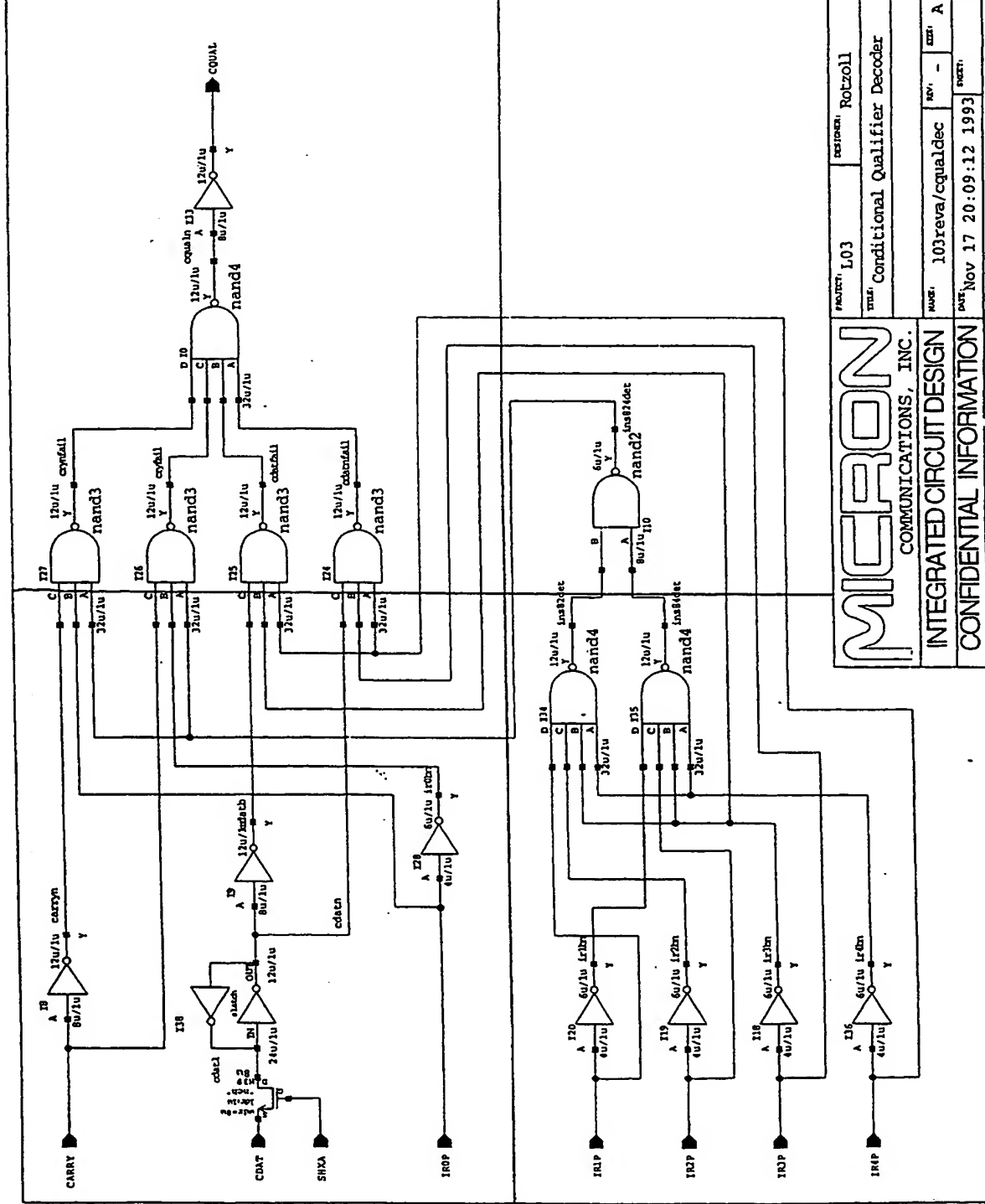


MICRON		PROJECT: L03	DESIGNED: Rotzoll
COMMUNICATIONS, INC.		TITLE: Instruction Decoder PLA Latch	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/insplat	REV: -
CONFIDENTIAL INFORMATION		DATE: Sep 29 16:10:56 1993	POST: A

7.07AA	7.07AB
7.07BA	7.07BB

EX-107

Fig. 7.07



MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Conditional Qualifier Decoder	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/cqualdec	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 17 20:09:12 1993	DESIGN: A

7.08AA

7.08BA

7.08CA

7.08 7.08B

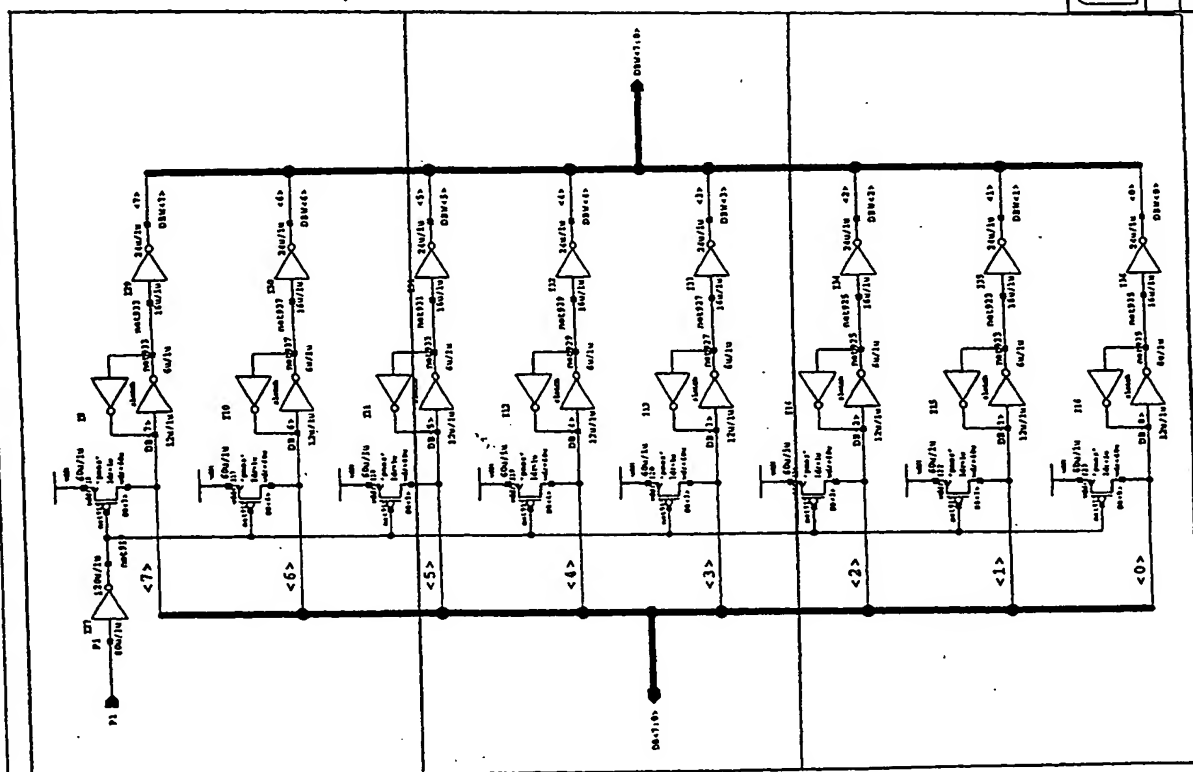


Fig. 7.08

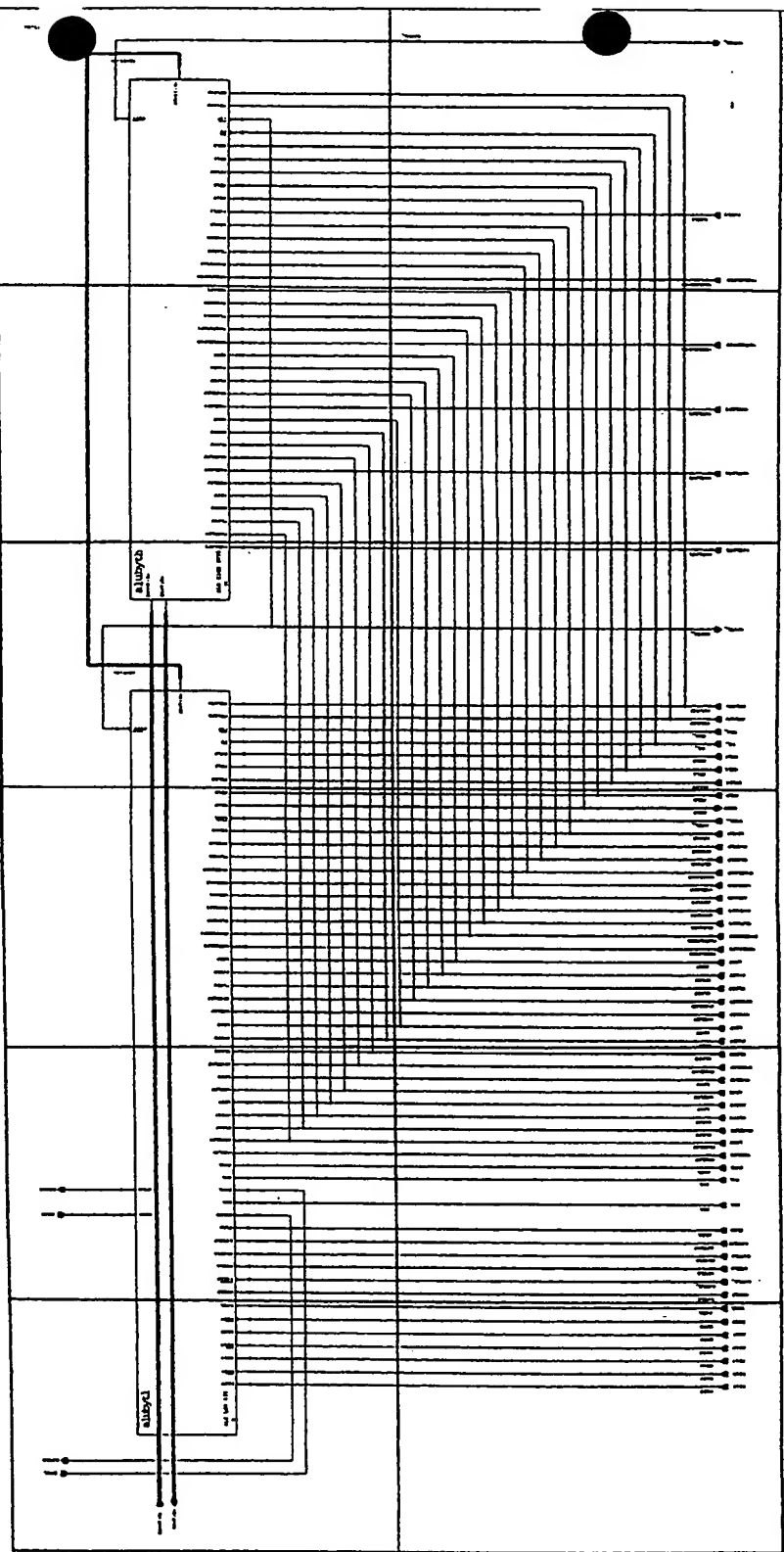
MICRON		PRODUCT: L03	REVISION: Rev 2.0
COMMUNICATIONS, INC.		TYPE: Databus Latch/Precharge	
INTEGRATED CIRCUIT DESIGN		DATE: 10/27/83	BY: J. J. J.
CONFIDENTIAL INFORMATION		DATE: Oct 1 14:51:49 1993	REV: 1.0

7.09AA	7.09AB	7.09AC	7.09AD	7.09AE	7.09AF
7.09BA	7.09BB	7.09BC	7.09BD	7.09BE	7.09BF

EX-7.09

Fig. 7.09

MICRON
CORPORATION
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION



7.0901AA	7.0901AB	7.0901AC	7.0901AD	7.0901AE
7.0901BA	7.0901BB	7.0901BC	7.0901BD	7.0901BE
7.0901CA	7.0901CB	7.0901CC	7.0901CD	7.0901CE

И. И. Ермаков

Fig. 7.0901

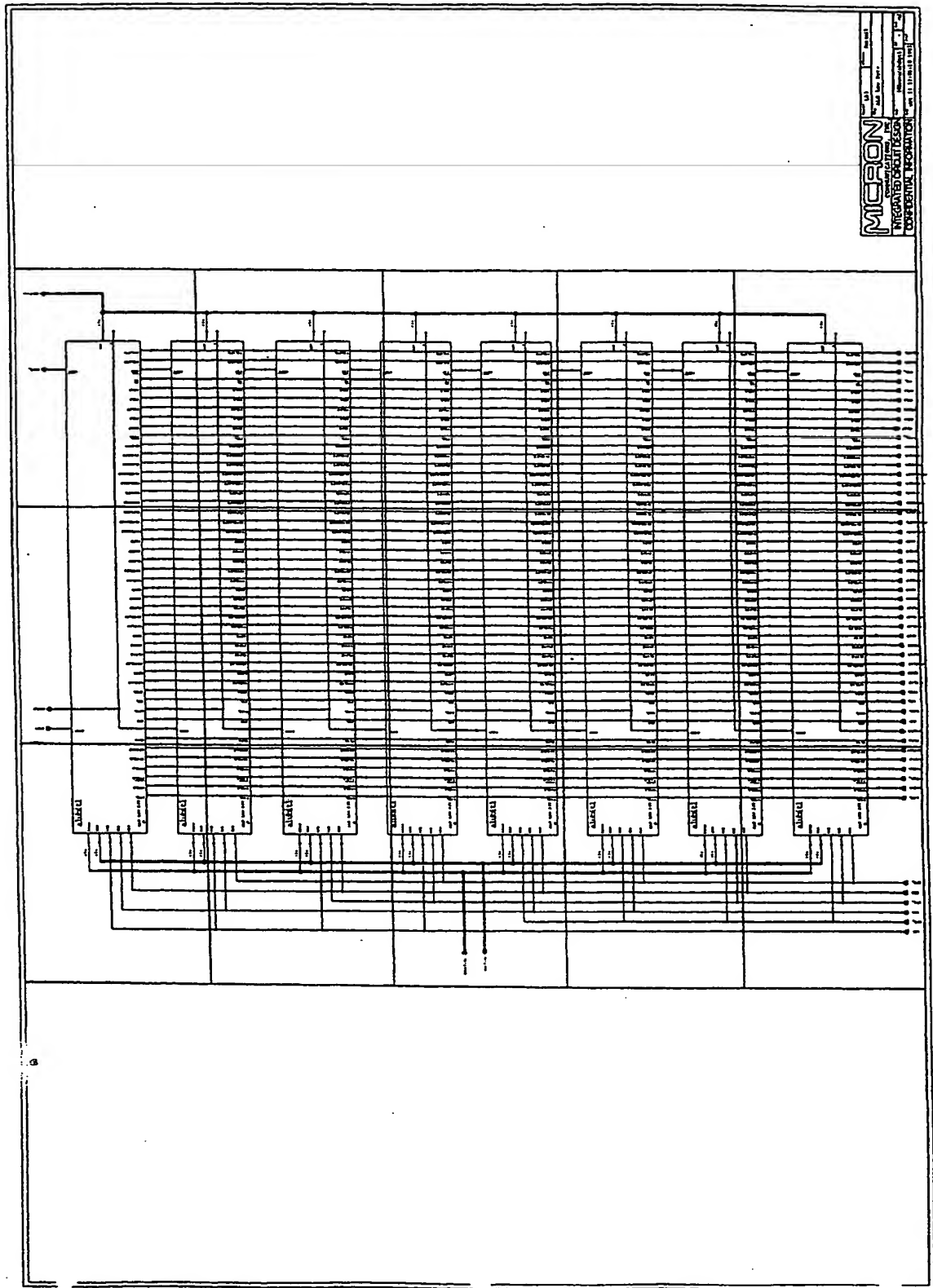
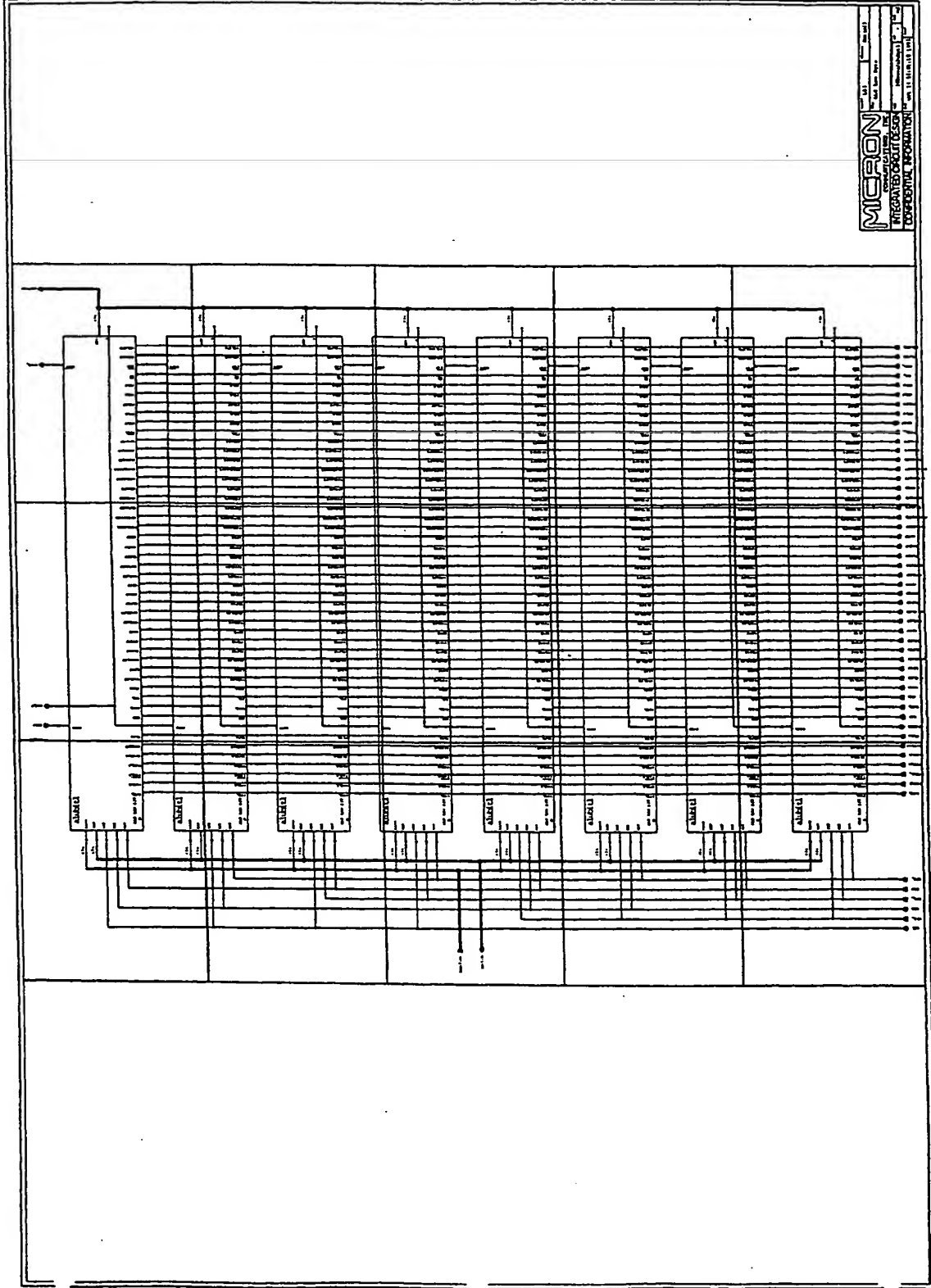


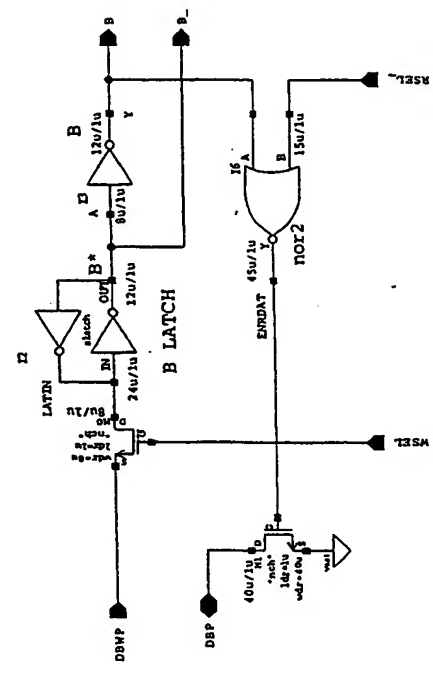
FIG. 7.0901



7.090101AA	7.090101AB	7.090101AC	7.090101AD
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II II II II II II II II

Fig. 7.09010102



MICRON		COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
PROJECT: L03		DESIGNER: Rotzoll	
TITLE: ALU B Register Cell			
NAME: 103reva/alubcell		REV: -	
DATE: Oct 1 15:32:35 1993		PAGE: A	

7.09010103AB

7.09010103AA

EX-11 7.09010103

FOOTNOTES:

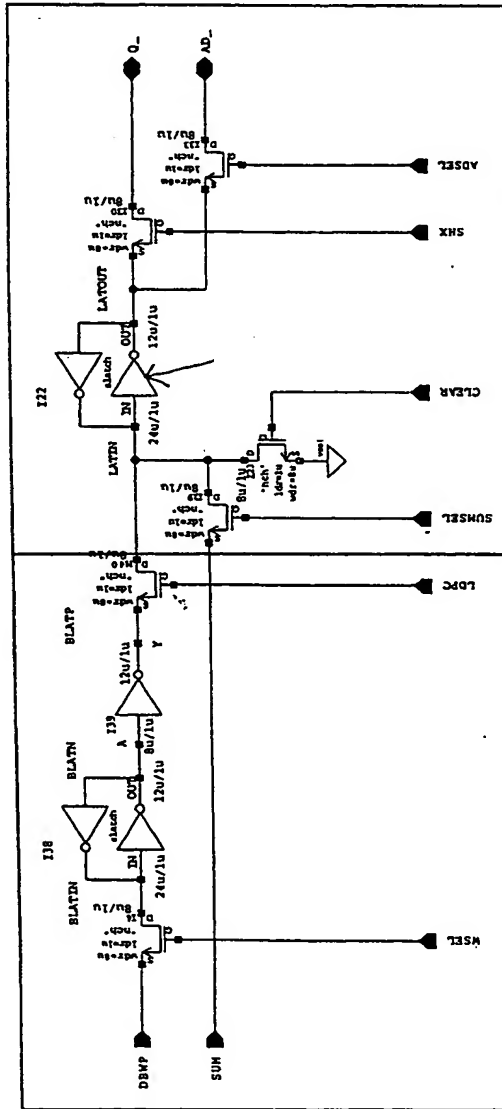
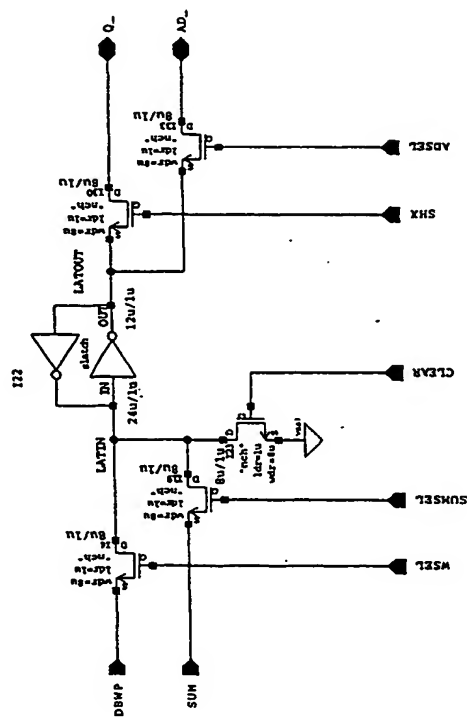


Fig. 7.09010104

<div>MICRON</div> <div>COMMUNICATIONS, INC.</div>				PROJECT: L03		DESIGN: Rotzoll			
				TITLE: ALU Register Cell					
				NAME: 103revA/alupc		REV: -		EDES: A	
				DATE: Oct 1 15:45:48 1993		PAGE: 1		SHEET: 1	
INTEGRATED CIRCUIT DESIGN									
CONFIDENTIAL INFORMATION									



MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

PROJECT: L03	DESIGNER: Rotzoll
TITLE: ALU Register Cell	
NUMBER: 101revc/alurcell	REV: -
DATE: Oct 1 15:51:03 1993	USER: A

Fig. 7.09010105

7.09010106AA	7.09010106AB
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EX 7.09010106

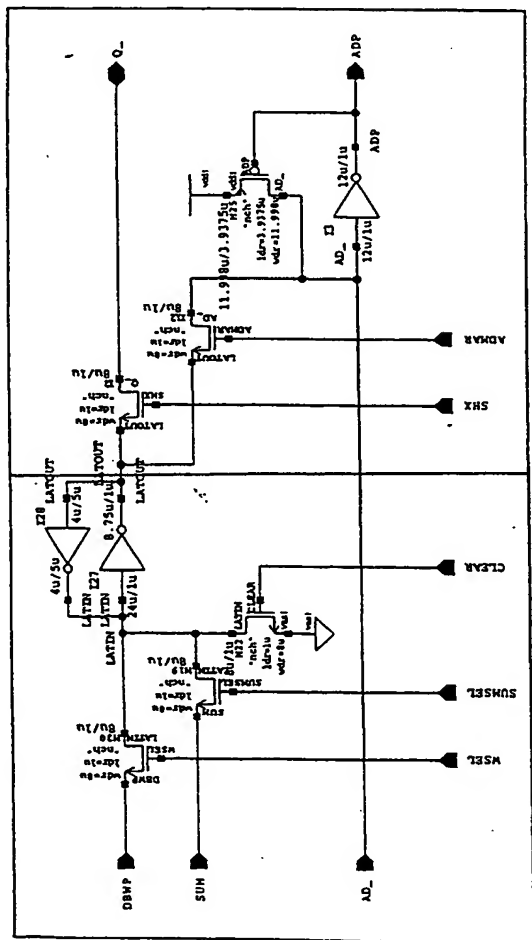


FIG. 7.09010106

MICRON		PROJECT: L03		DESIGNER: JOTCOOLE	
COMMUNICATIONS, INC.					
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION					
NAME:		103revA/alumar		REV: B8	
DATE:		Jan 4 10:27:28 1996		SHEET:	

MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

B8: added pch feedback device

7.09010108AA	7.09010108AB	7.09010108AC
7.09010108BA	7.09010108BB	7.09010108BC

SECRET

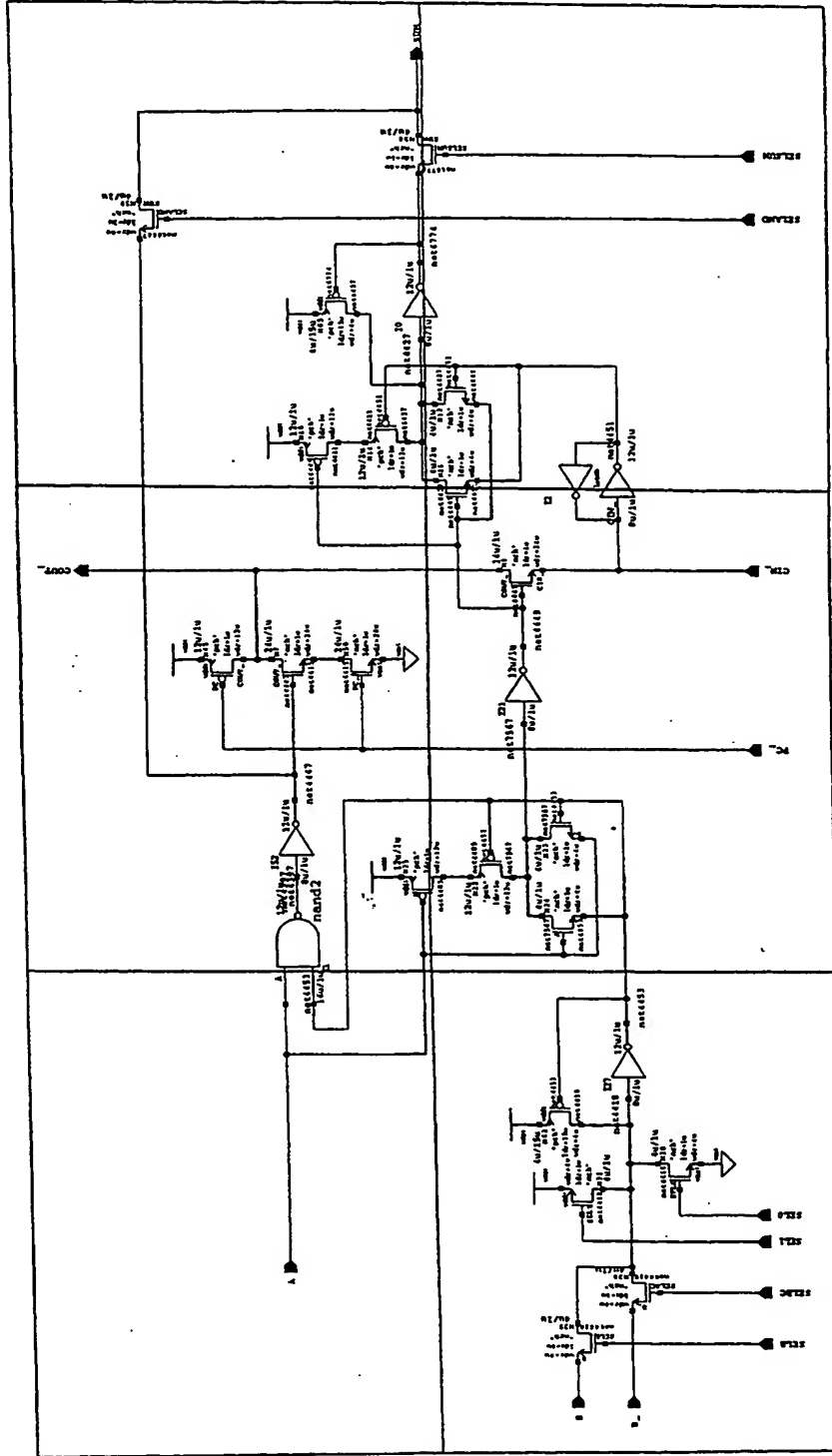


Fig. 7.09010108

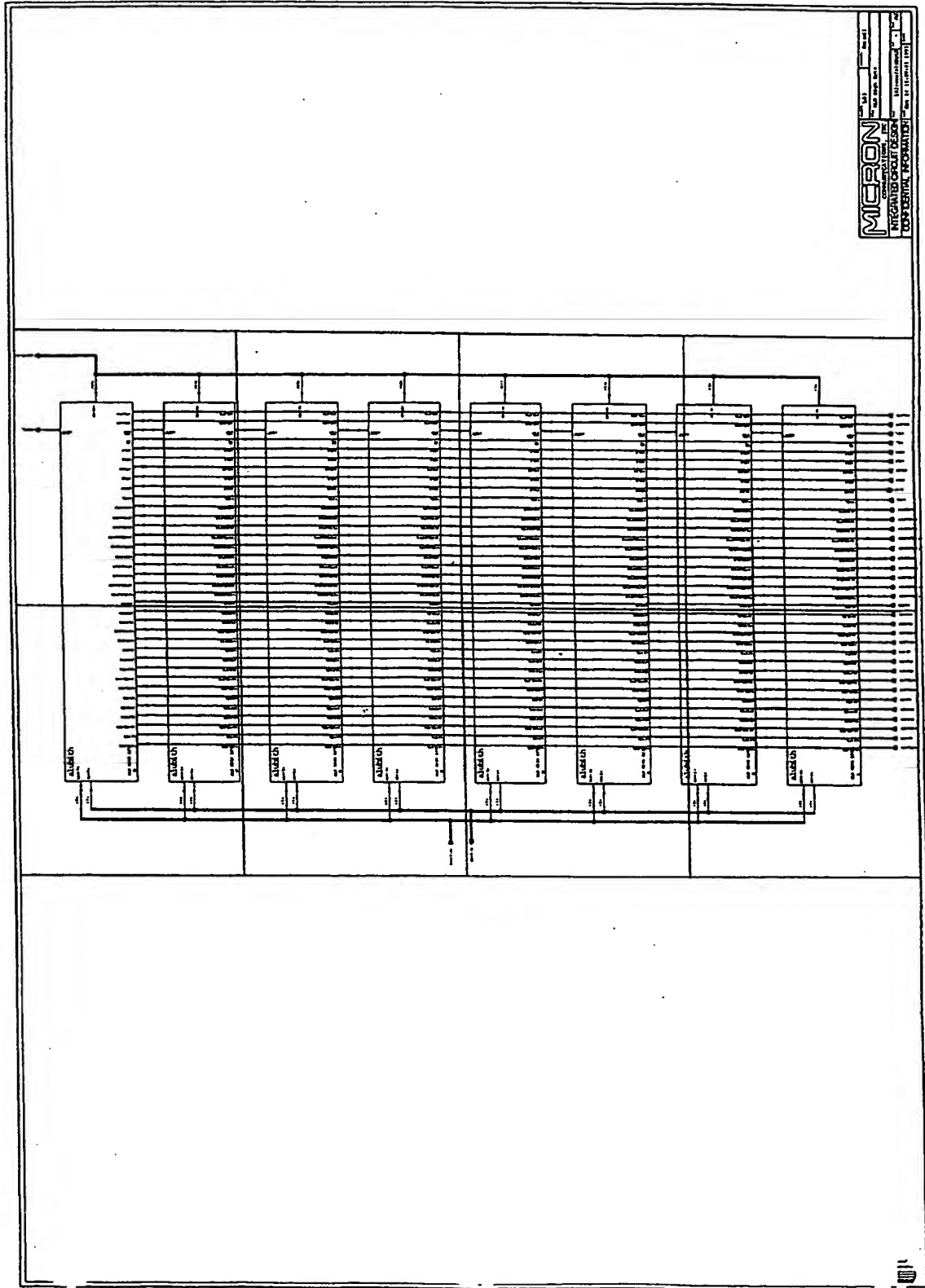
B5: move feedback device from I21 to I27

MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		FILE: ALU Addr	
INTEGRATED CIRCUIT DESIGN		DATE: 103revA/aluadd	REV: B5
CONFIDENTIAL INFORMATION		DATE: Sep 16 15:48:21 1995	USER: rll

7.0902AA	7.0902AB	7.0902AC	7.0902AD
7.0902BA	7.0902BB	7.0902BC	7.0902BD

EX 1.0902

Fig. 7.0902



7.090201AA	7.090201AB	7.090201AC
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EX-7.090201

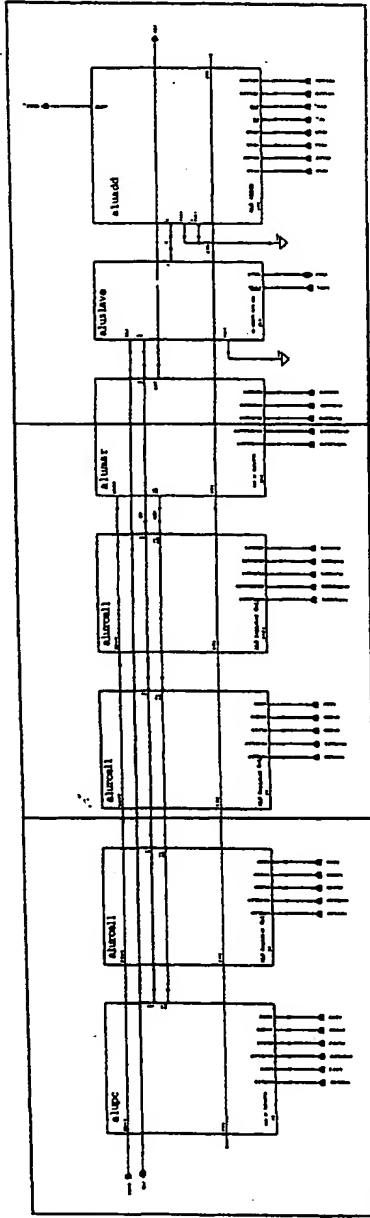
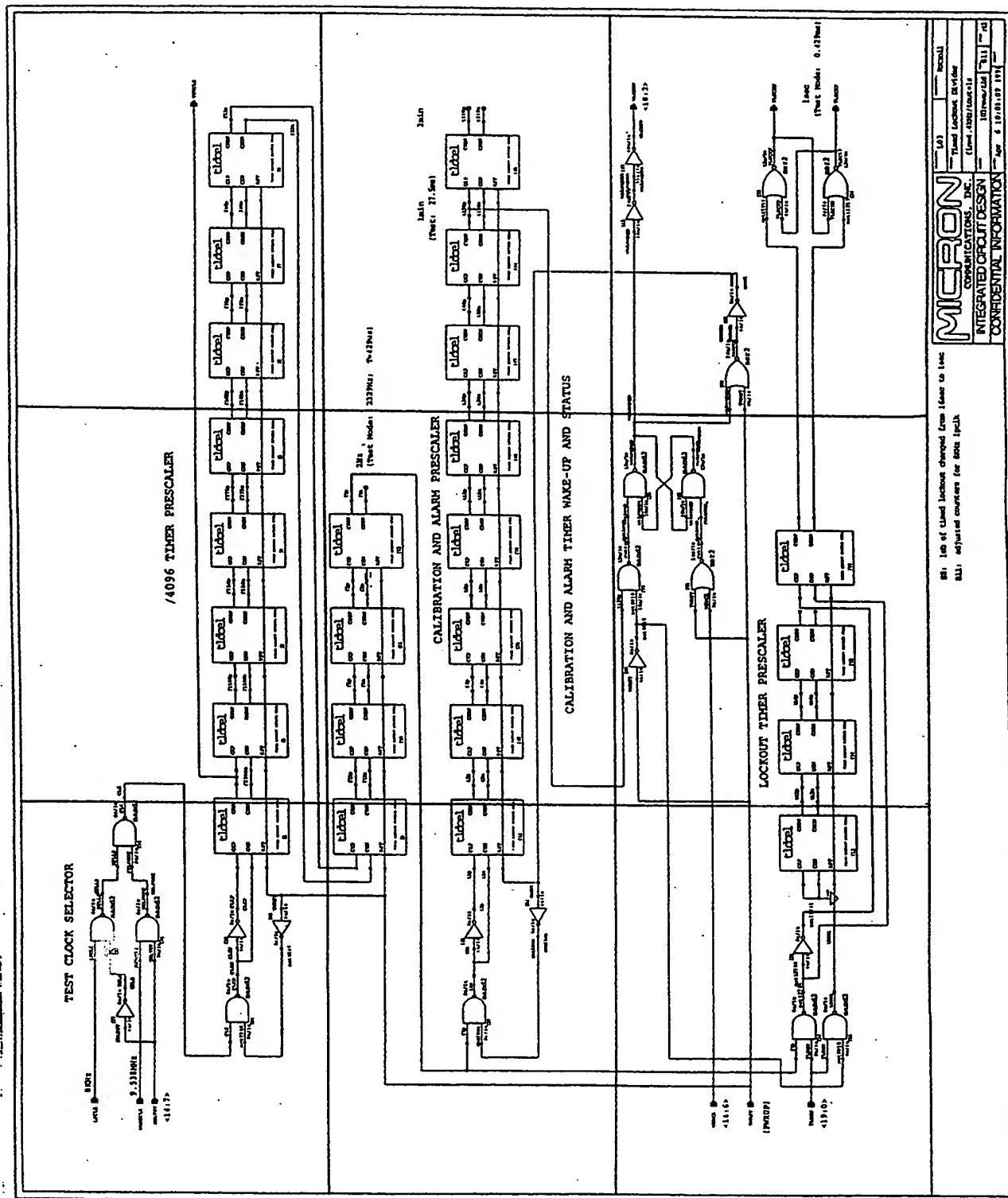


FIG. 7.090201

7.10AA	7.10AB	7.10AC
7.10BA	7.10BB	7.10BC
7.10CA	7.10CB	7.10CC

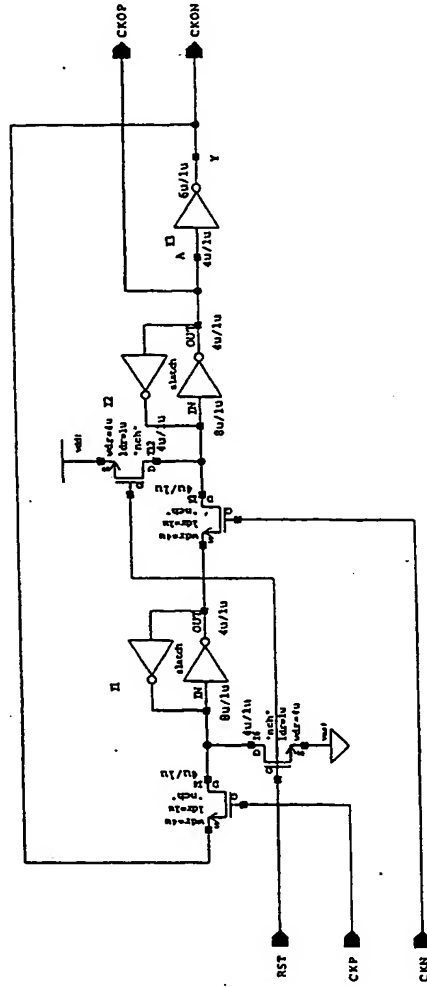
II II II III III

FIG. 7.10



MICRON
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

81: 1st of 1st Lockout changed from 100 to 100
 81: 1st of 1st Lockout changed from 100 to 100



12/29/92

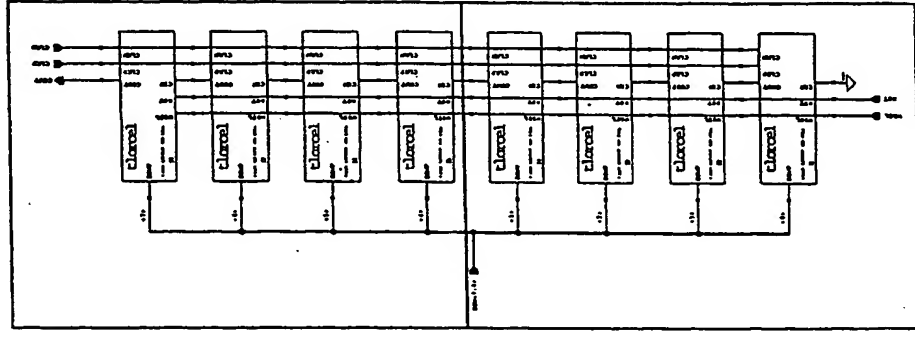
MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Timed Lockout Divider Cell	
INTEGRATED CIRCUIT DESIGN		NUMBER: 103revA/tl&el	REV: A
CONFIDENTIAL INFORMATION		DATE: Sep 22 15:26:56 1994	SHEET: 1

Fig. 7.1001

7.11AA	7.11AB
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II. II. II. II

FIG. 7.11



7.1101AA	7.1101AB	7.1101AC
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IL IL IL IL IL IL IL IL

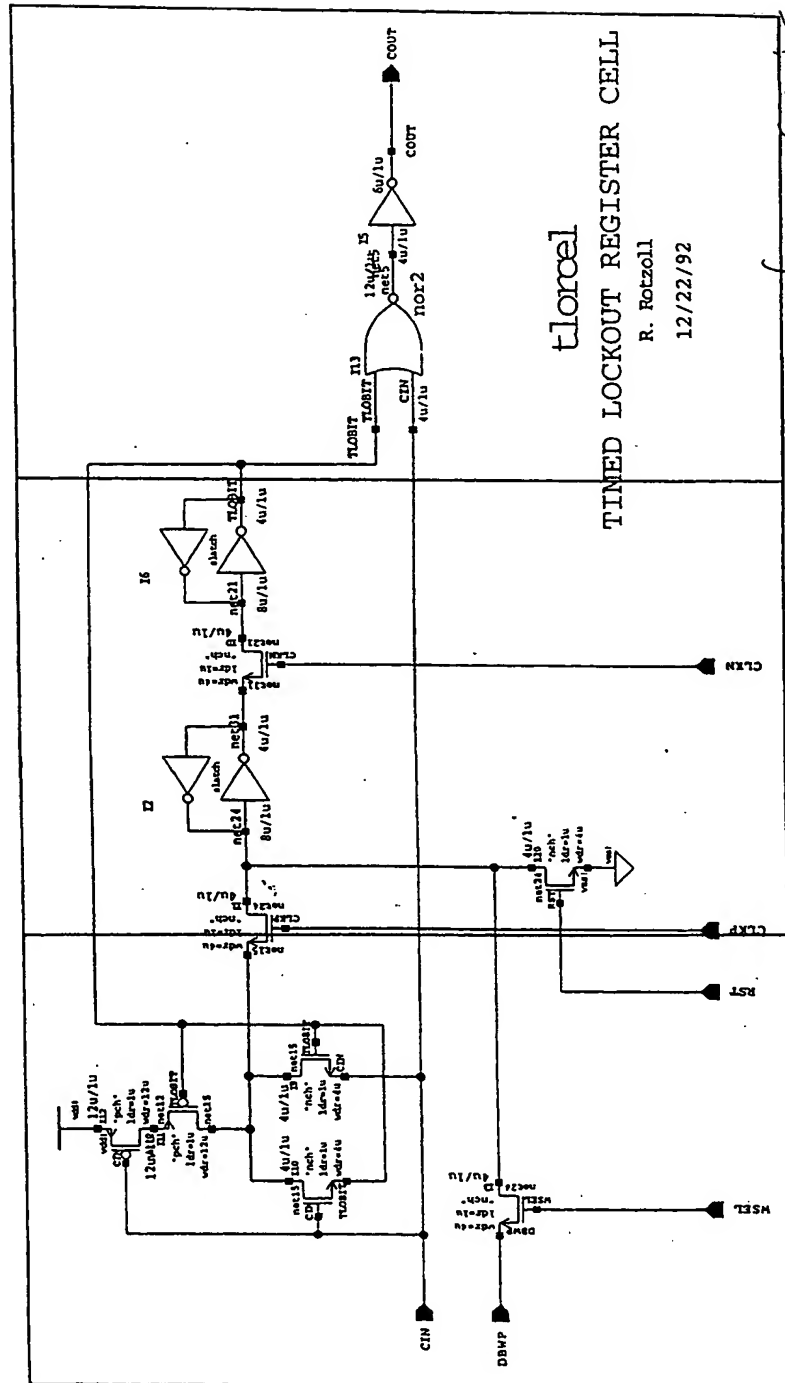


FIG. 7.1101

7.12AA	7.12AB	7.12AC
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Exhibit 10.12

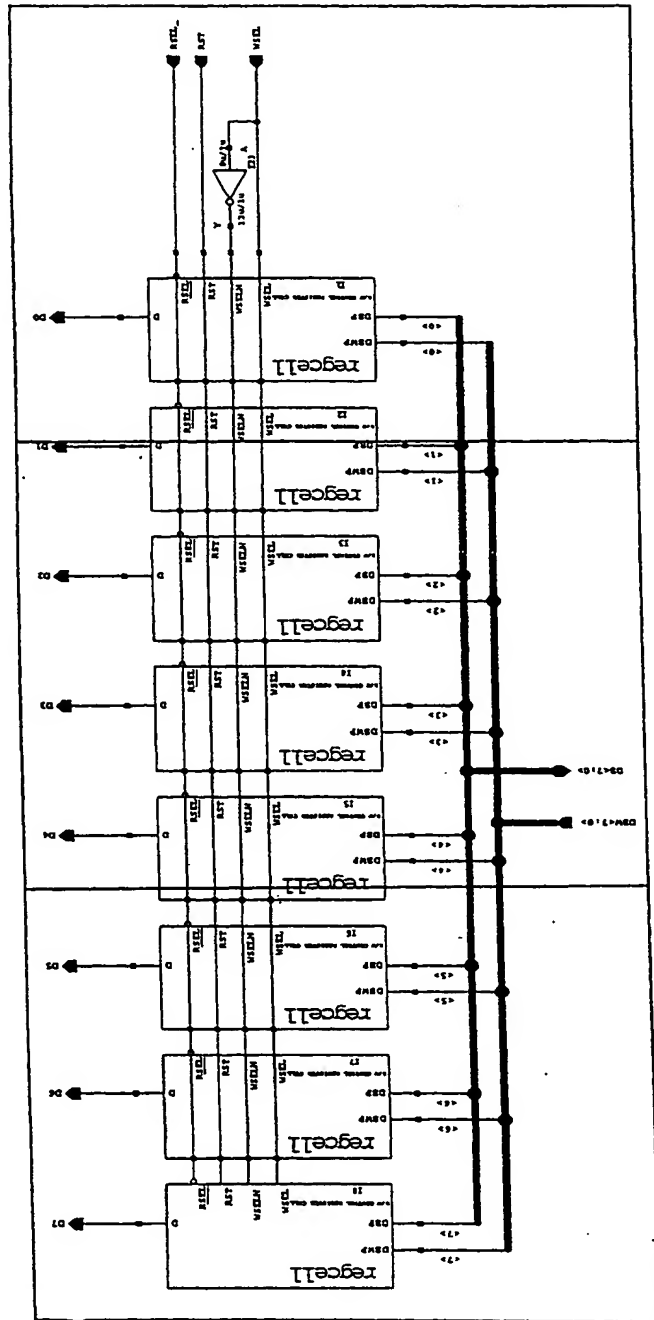
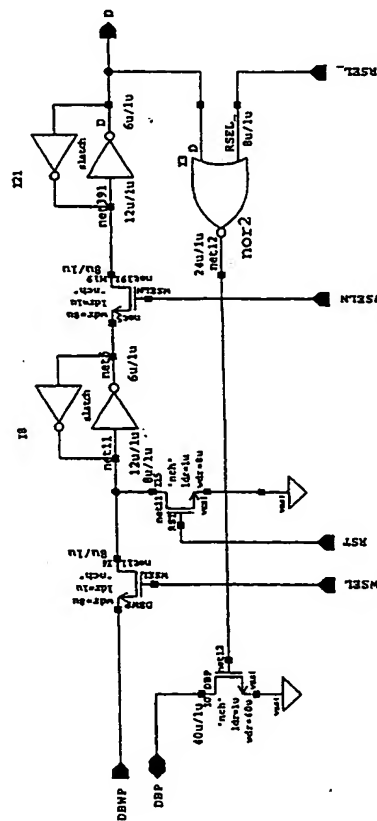


Fig. 7.12

MICRON		PROJECT: L03	REVISION: R02
COMMUNICATIONS, INC.		R/W Control Register	
INTEGRATED CIRCUIT DESIGN		DATE: 10/12/93	BY: RL
CONFIDENTIAL INFORMATION		DATE: Nov 12 09:44:40 1993	BY: RL

Fig. 7.1201



MICRON		PROJECT: L03	DESIGN: Rotzoll
COMMUNICATIONS, INC.		TITLE: R/W Control Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/regcell	REV: - A
CONFIDENTIAL INFORMATION		DATE: Nov 12 09:41:36 1993	DEPT:

7.13AA

7.13BA

7.13

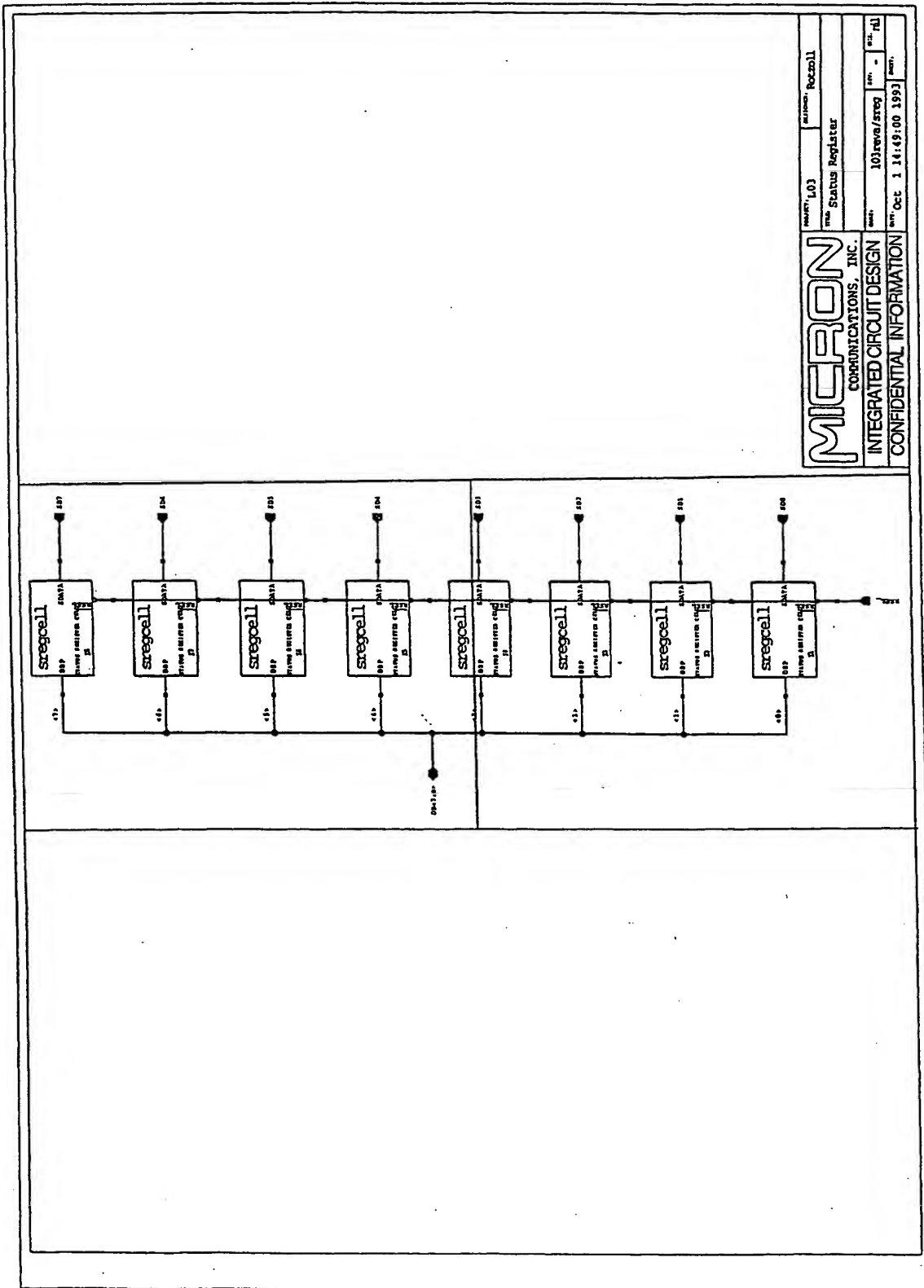
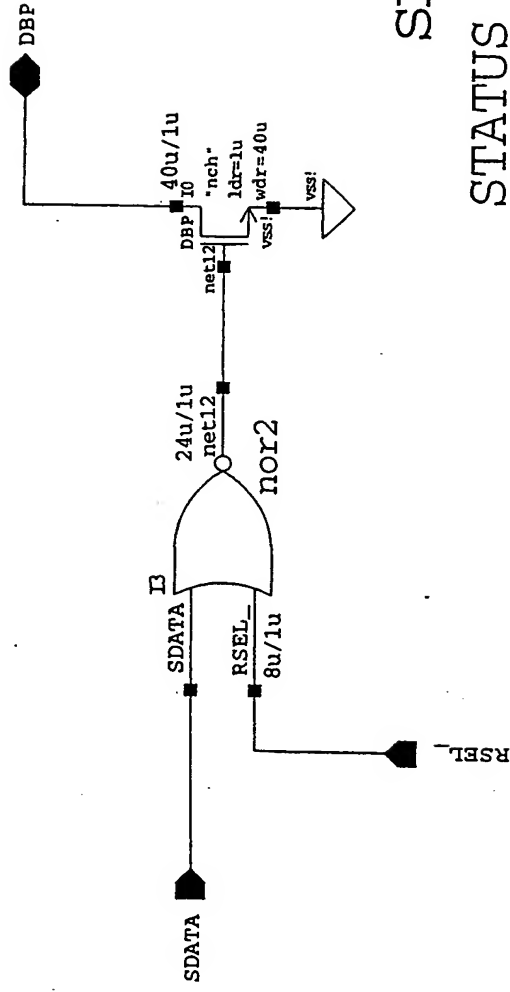


FIG. 7.13



sregcell

STATUS REGISTER CELL

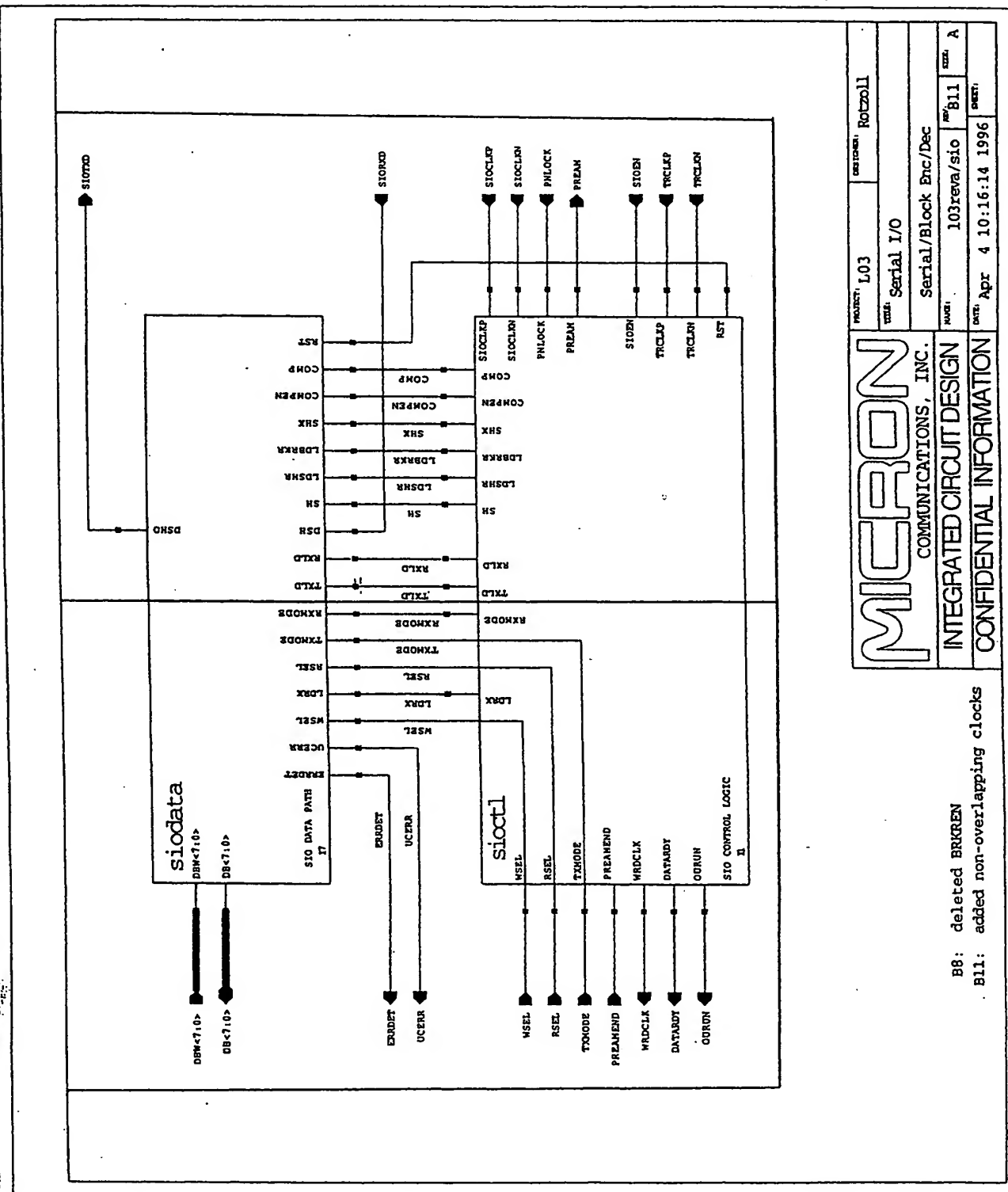
R. Rotzoll

12/8/92

FIG. 7.1301

7.14AB	7.14AA
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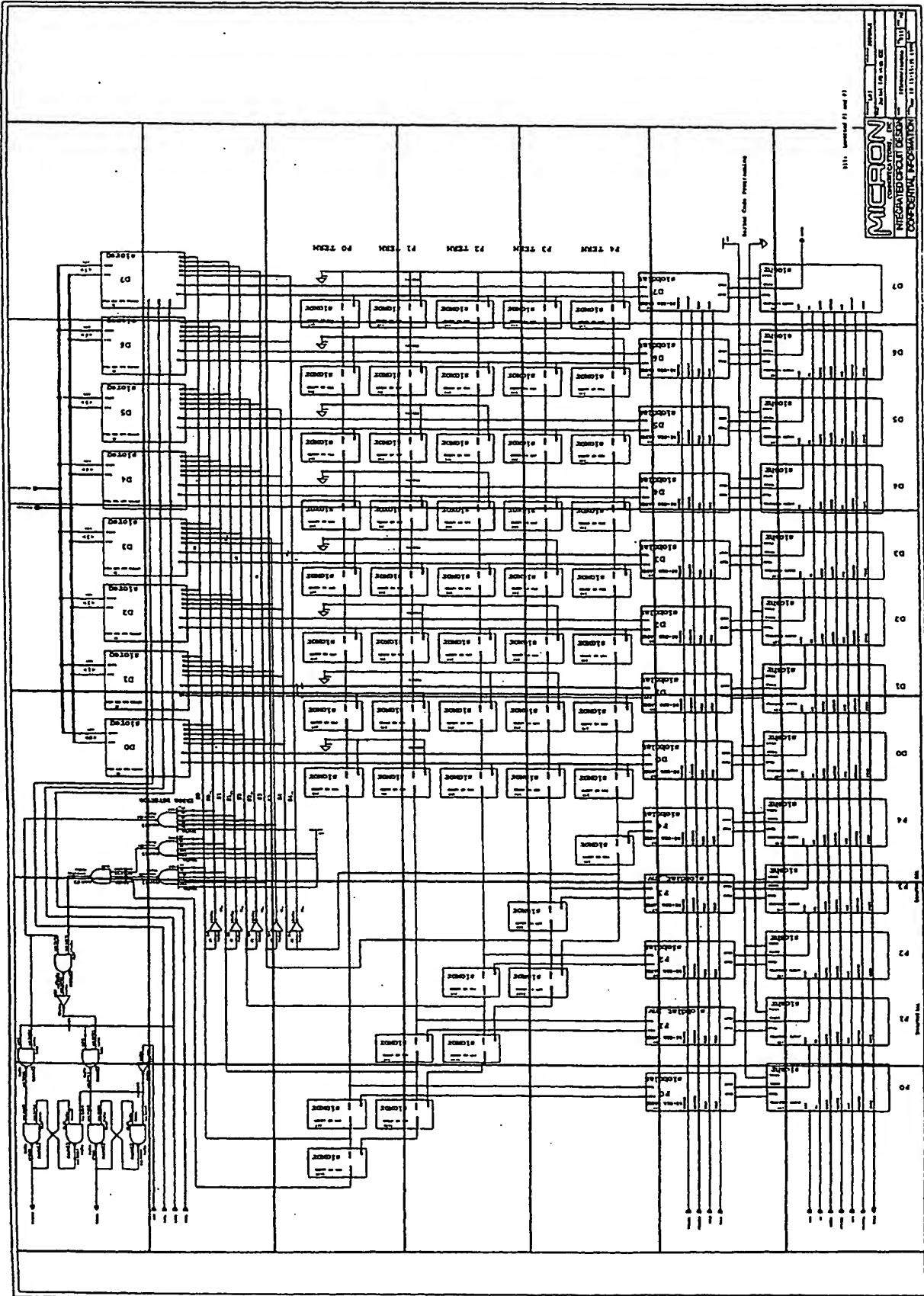
Exhibit 7.14



7.1401AA	7.1401AB	7.1401AC	7.1401AD	7.1401AE	7.1401AF
7.1401BA	7.1401BB	7.1401BC	7.1401BD	7.1401BE	7.1401BF
7.1401CA	7.1401CB	7.1401CC	7.1401CD	7.1401CE	7.1401CF
7.1401DA	7.1401DB	7.1401DC	7.1401DD	7.1401DE	7.1401DF
7.1401EA	7.1401EB	7.1401EC	7.1401ED	7.1401EE	7.1401EF
7.1401FA	7.1401FB	7.1401FC	7.1401FD	7.1401FE	7.1401FF
7.1401GA	7.1401GB	7.1401GC	7.1401GD	7.1401GE	7.1401GF

II II II II II II

Fig. 7/1401



7.140101AA	7.140101AB
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II II II II II II II

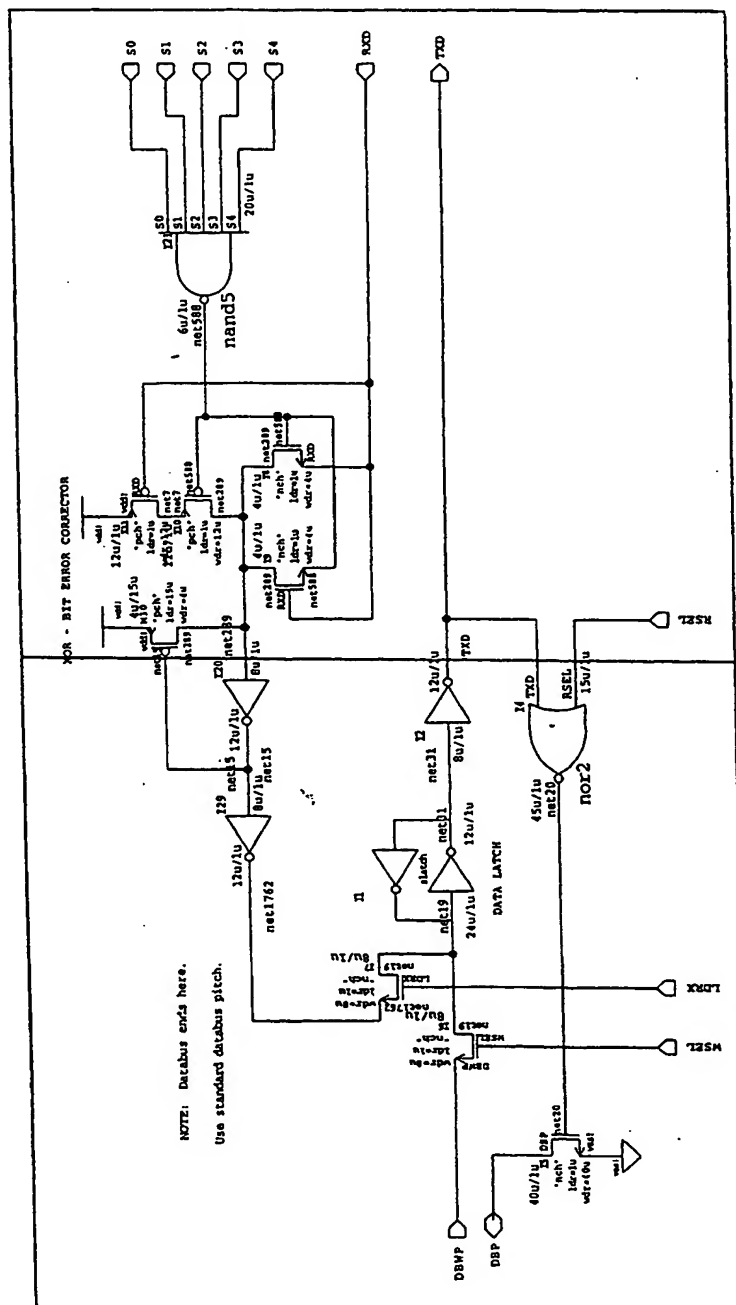
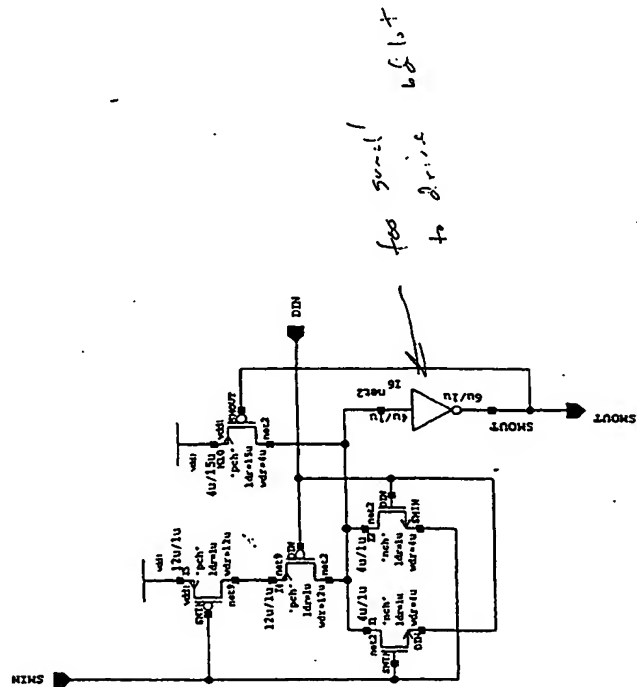


FIG. 7.140101

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Serial I/O Register Call	
INTEGRATED CIRCUIT DESIGN		DATE: Sep 1 18:03:52 1994	PROJECT: 103reva/sioreg
CONFIDENTIAL INFORMATION		REV: B1	SIZE: A



for signal
to drive
66 bit

Fig. 7.190102

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: SIO XOR	
INTEGRATED CIRCUIT DESIGN		NAME: 103revA/sioxor	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 1 18:07:22 1994	SHEET: A

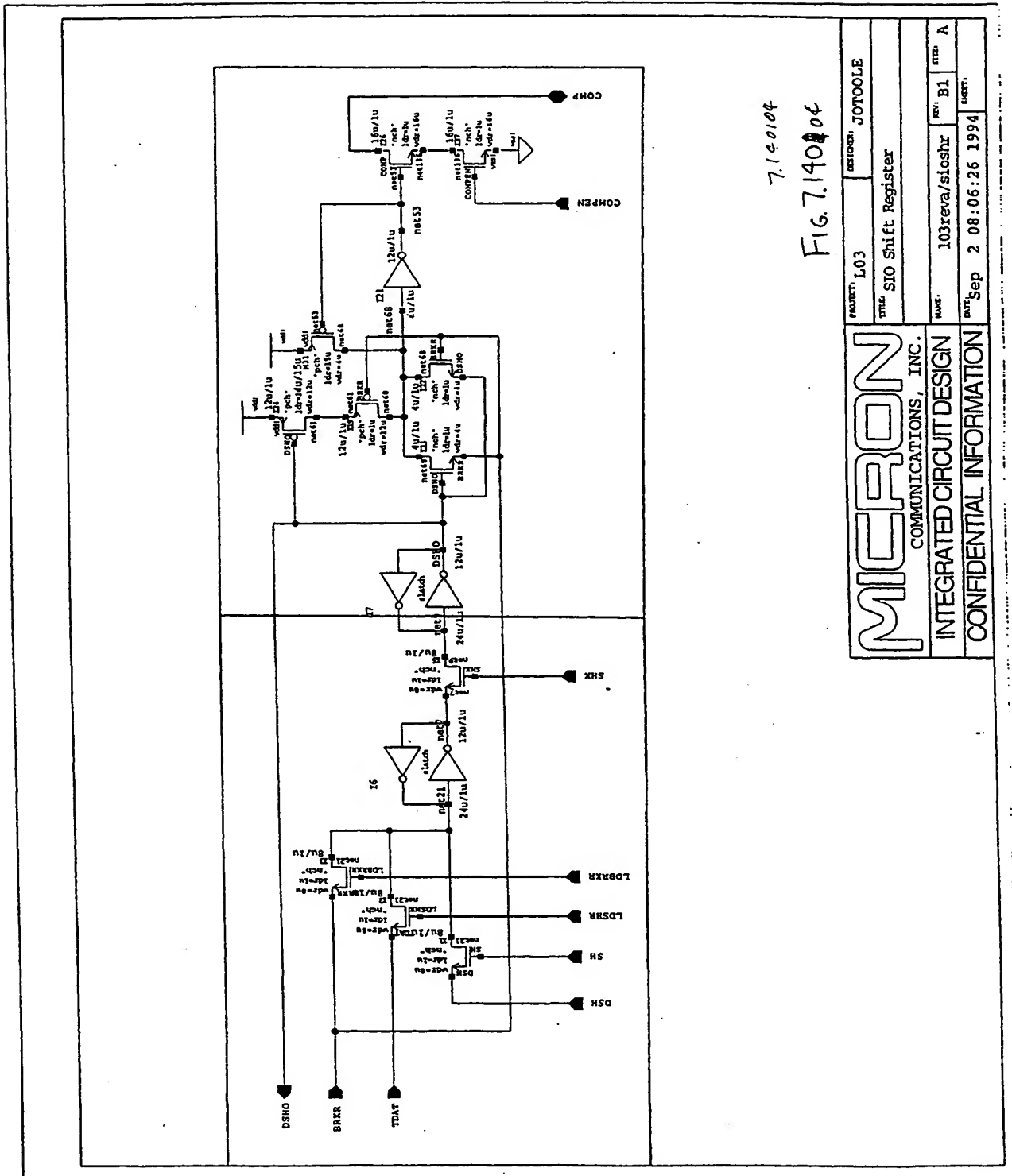
7.140103AA	7.140103AB
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7.140103AA

ЕВРОПЕЙСКИЙ

7.140104AA	7.140104AB
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SECRET 7.140104



7.140104
Fig. 7.140104

MICRON		PRODUCT	L03	DESIGNER	JOTOOLE
COMMUNICATIONS, INC.		TITLE	SIO Shift Register		
INTEGRATED CIRCUIT DESIGN		NAME	103reva/sioshr	REV	B1
CONFIDENTIAL INFORMATION		DATE	Sep 2 08:06:26 1994	EXT	A

7.140105AB	7.140105AA
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EX-1101105

7.1402AC	7.1402AD	7.1402AE	7.1402AF	7.1402AG	7.1402AH	7.1402AI
7.1402BA	7.1402BB	7.1402BC	7.1402BD	7.1402BE	7.1402BF	7.1402BI
7.1402CA	7.1402CB	7.1402CC	7.1402CD	7.1402CE	7.1402CF	7.1402CI
7.1402DA	7.1402DB	7.1402DC	7.1402DD	7.1402DE	7.1402DF	7.1402DI
7.1402EA	7.1402EB	7.1402EC	7.1402ED	7.1402EE	7.1402EF	7.1402EI

2007.11.12

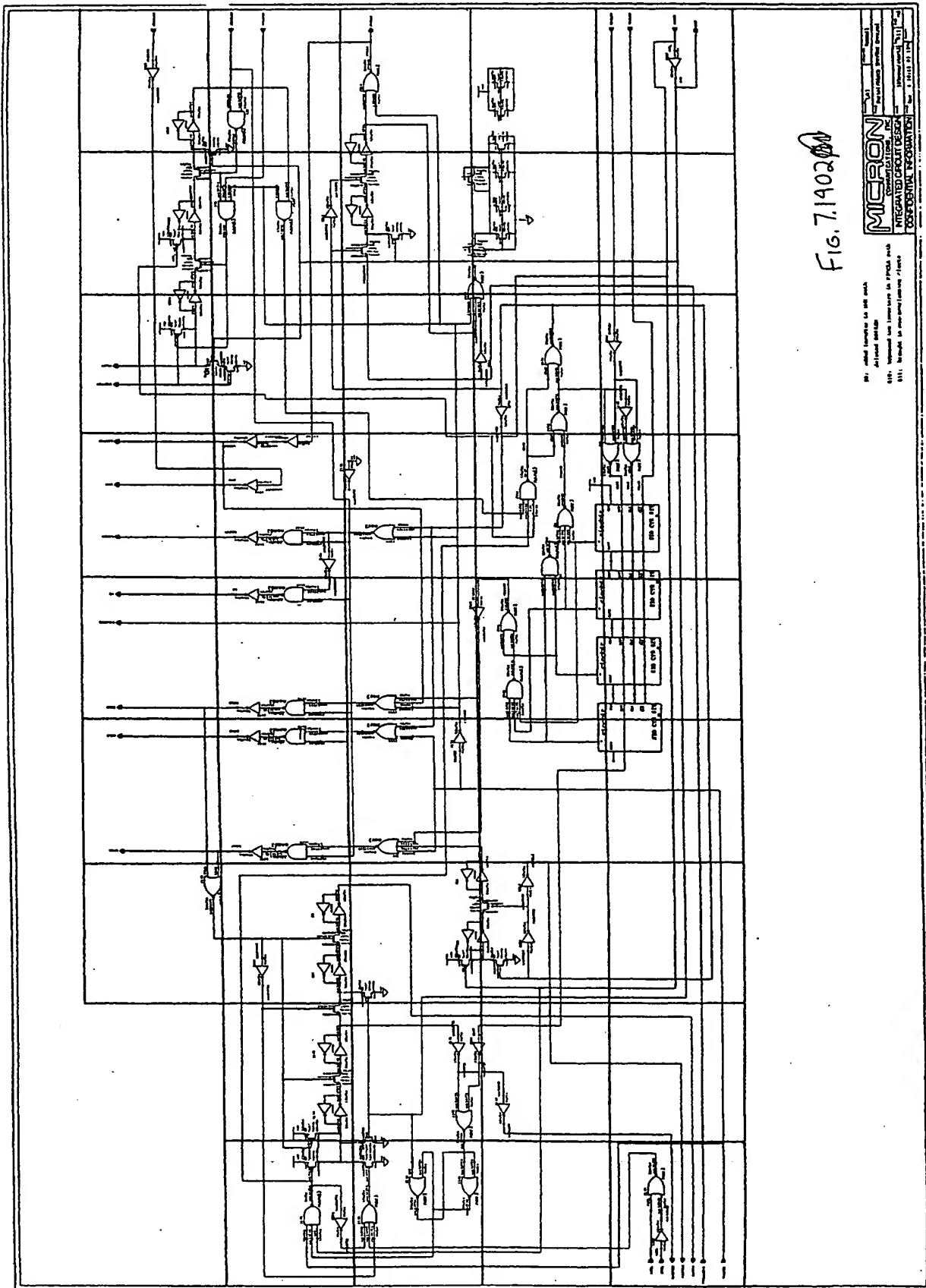


Fig. 7.1402

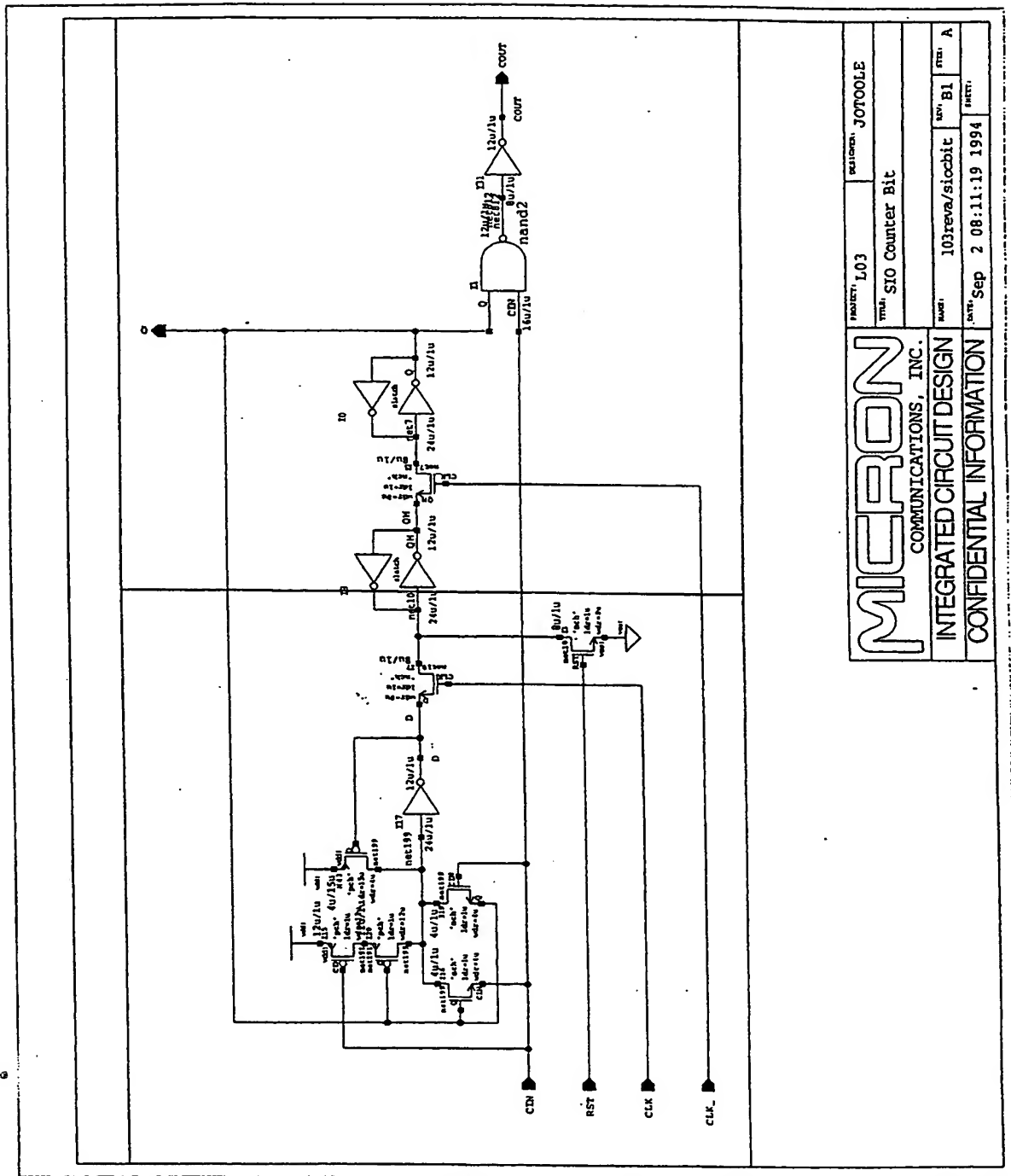
801, 802, 803, 804, 805, 806, 807, 808, 809, 810, 811, 812, 813, 814, 815, 816, 817, 818, 819, 820, 821, 822, 823, 824, 825, 826, 827, 828, 829, 830, 831, 832, 833, 834, 835, 836, 837, 838, 839, 840, 841, 842, 843, 844, 845, 846, 847, 848, 849, 850, 851, 852, 853, 854, 855, 856, 857, 858, 859, 860, 861, 862, 863, 864, 865, 866, 867, 868, 869, 870, 871, 872, 873, 874, 875, 876, 877, 878, 879, 880, 881, 882, 883, 884, 885, 886, 887, 888, 889, 890, 891, 892, 893, 894, 895, 896, 897, 898, 899, 900, 901, 902, 903, 904, 905, 906, 907, 908, 909, 910, 911, 912, 913, 914, 915, 916, 917, 918, 919, 920, 921, 922, 923, 924, 925, 926, 927, 928, 929, 930, 931, 932, 933, 934, 935, 936, 937, 938, 939, 940, 941, 942, 943, 944, 945, 946, 947, 948, 949, 950, 951, 952, 953, 954, 955, 956, 957, 958, 959, 960, 961, 962, 963, 964, 965, 966, 967, 968, 969, 970, 971, 972, 973, 974, 975, 976, 977, 978, 979, 980, 981, 982, 983, 984, 985, 986, 987, 988, 989, 990, 991, 992, 993, 994, 995, 996, 997, 998, 999, 1000

7.140201AA	7.140201AB
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7.140201AA

THE END

201
Fig. 7.140

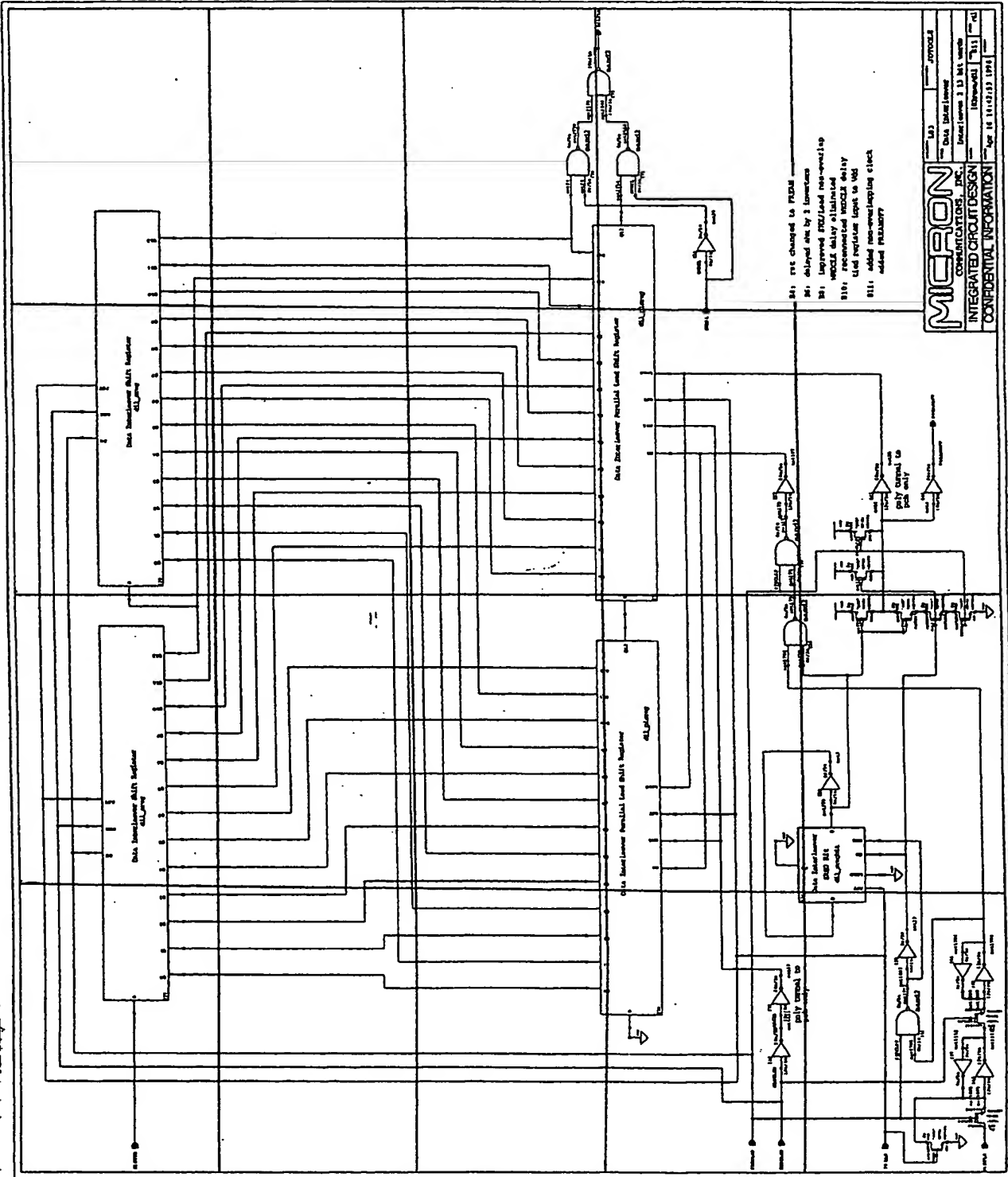


MICRON		PROJECT: L03	REVISION: J07000LE
COMMUNICATIONS, INC.		TITLE: SIO Counter Bit	
INTEGRATED CIRCUIT DESIGN		DATE: 103rev/sicbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 2 08:11:19 1994	REV: A

7.15AA	7.15AB	7.15AC	7.15AD
7.15BA	7.15BB	7.15BC	7.15BD
7.15CA	7.15CB	7.15CC	7.15CD
7.15DA	7.15DB	7.15DC	7.15DD
7.15EA	7.15EB	7.15EC	

II II II II II

Fig. 7.15



MICRON
 CORPORATION, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

7.1501AA

7.1501BA

7.1501CA

Fig 7.1501

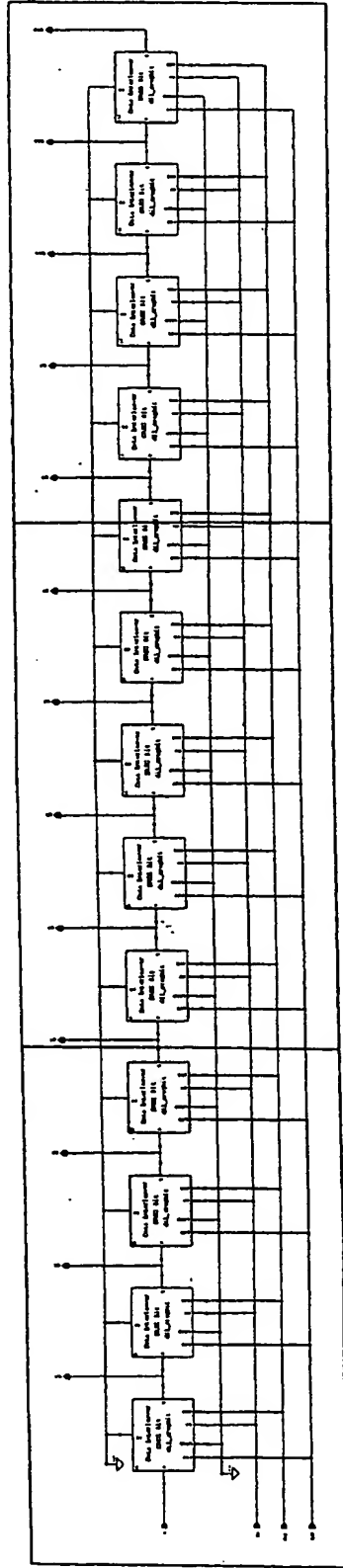


Fig. 7.1501

7.1502AA

7.1502BA

7.1502CA

Fig 7.1502

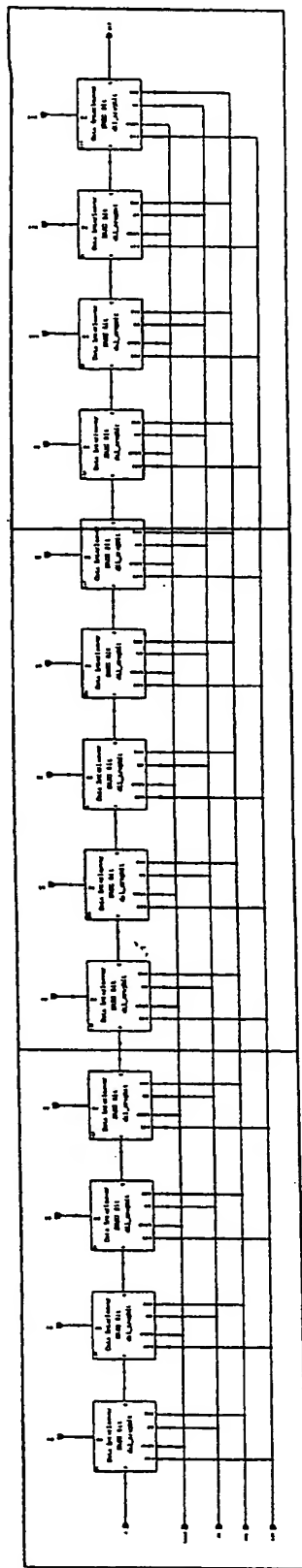
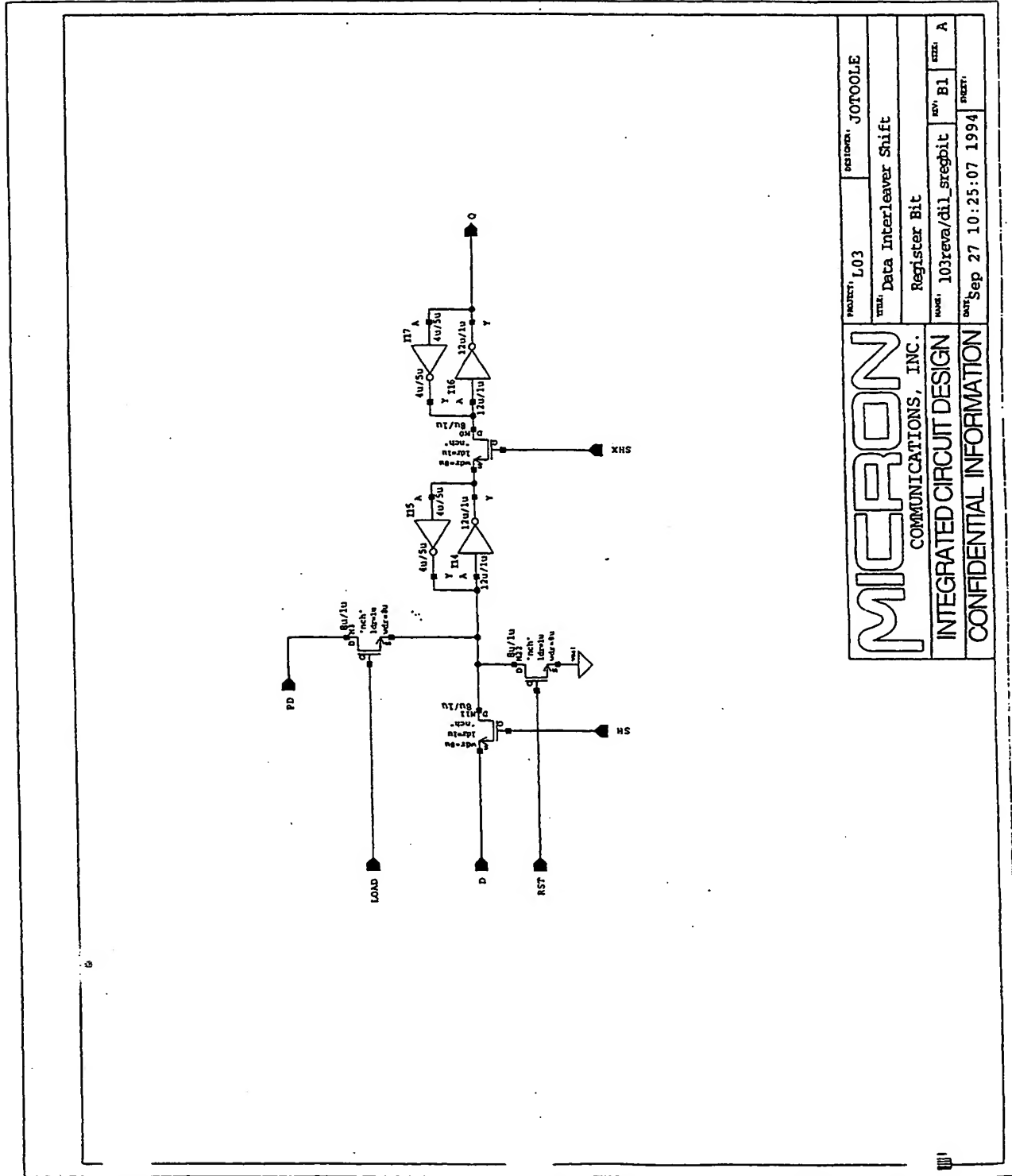


Fig. 7.1502

Fig. 7/50201

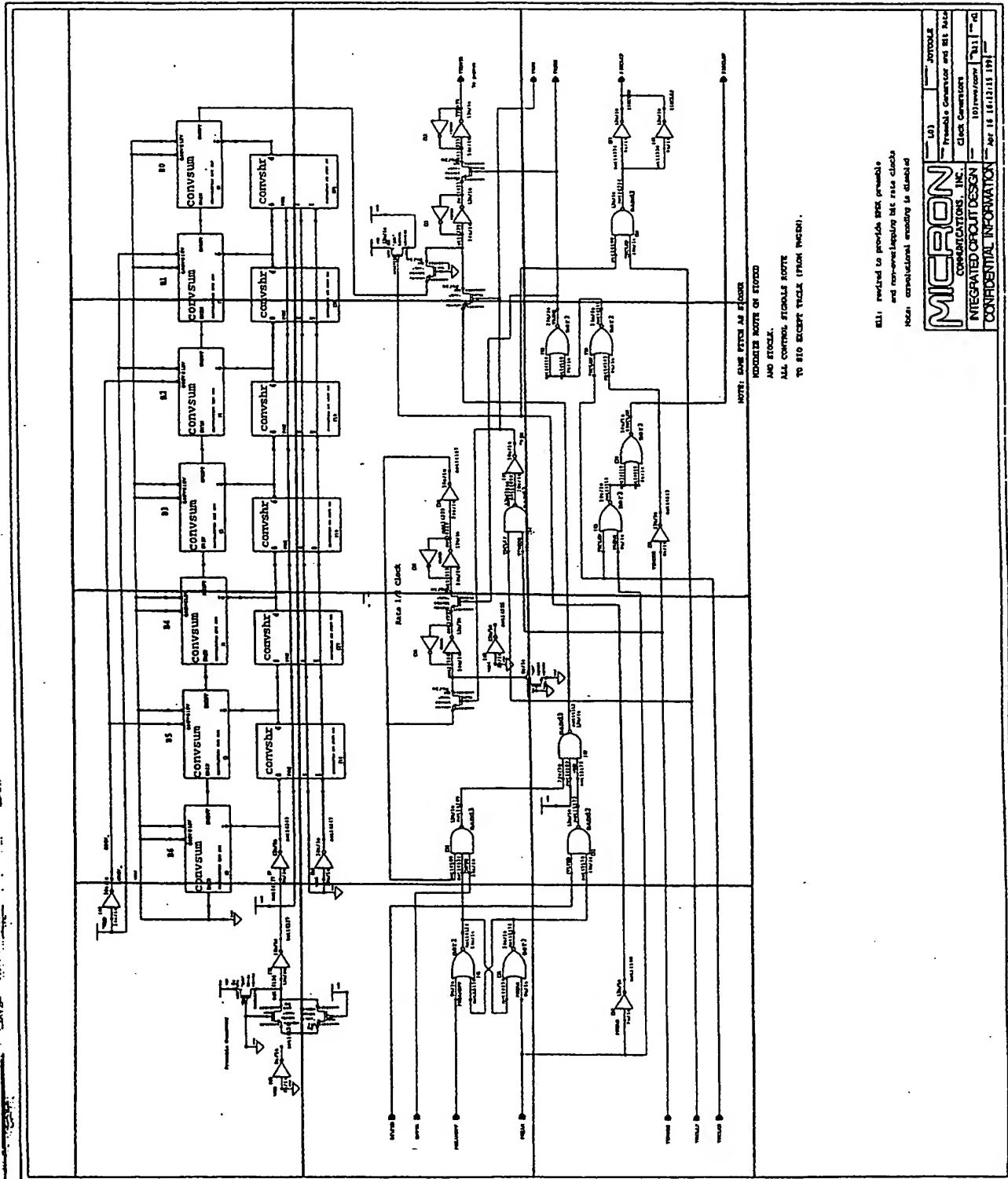


MICRON		PROJECT: L03	REVISION: J07001E
COMMUNICATIONS, INC.		DATA	Data Interleaver Shift
INTEGRATED CIRCUIT DESIGN		REGISTER BIT	
CONFIDENTIAL INFORMATION		MADE: 103reva/dil_sregbit	REV: B1
		DATE: Sep 27 10:25:07 1994	EXCEL: A

7.16AA	7.16AB	7.16AC	7.16AD
7.16BA	7.16BB	7.16BC	7.16BD
7.16CA	7.16CB	7.16CC	7.16CD

IL 101

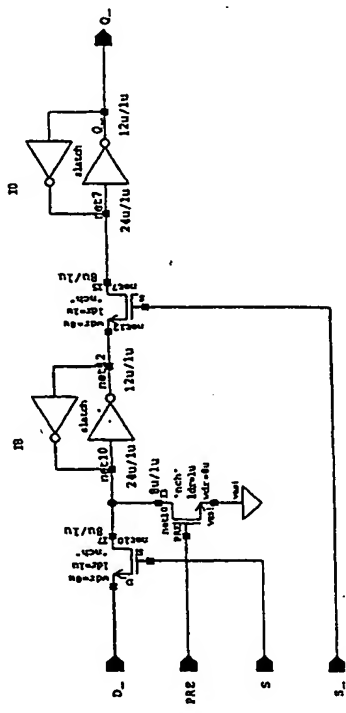
Fig. 7.16



ELL: revised to provide SPK preamble
and non-overlapping bit rate clock
Note: convolutional encoding is disabled

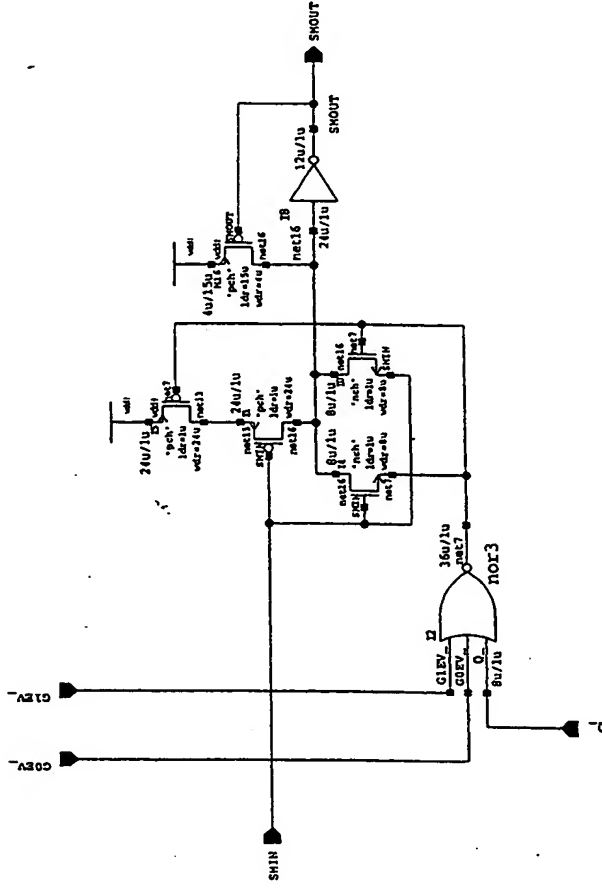
MICRON		U3	INTCLOCK
COMMUNICATIONS, INC.		Preamble Generator and Bit Rate	
INTEGRATED CIRCUIT DESIGN		Clock Generators	
CONFIDENTIAL INFORMATION		10/19/80/100	
		Rev. 16, 10/12/13, 1991	

Fig. 7.1601



MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Convolutional Encoder Shift	
INTEGRATED CIRCUIT DESIGN		Register Cell	
CONFIDENTIAL INFORMATION		DATE: 103revA/convshr	REV: A
		DATE: Sep 2 10:34:27 1994	SHEET: 1

Fig. 7.1602



PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: Convolutional Encoder Summer			
MAKER: 103reva/convsum	REV: B1	SIZE: A	
DATE: Sep 2 10:32:17 1994		PROJECT:	

MICRON
COMMUNICATIONS, INC.

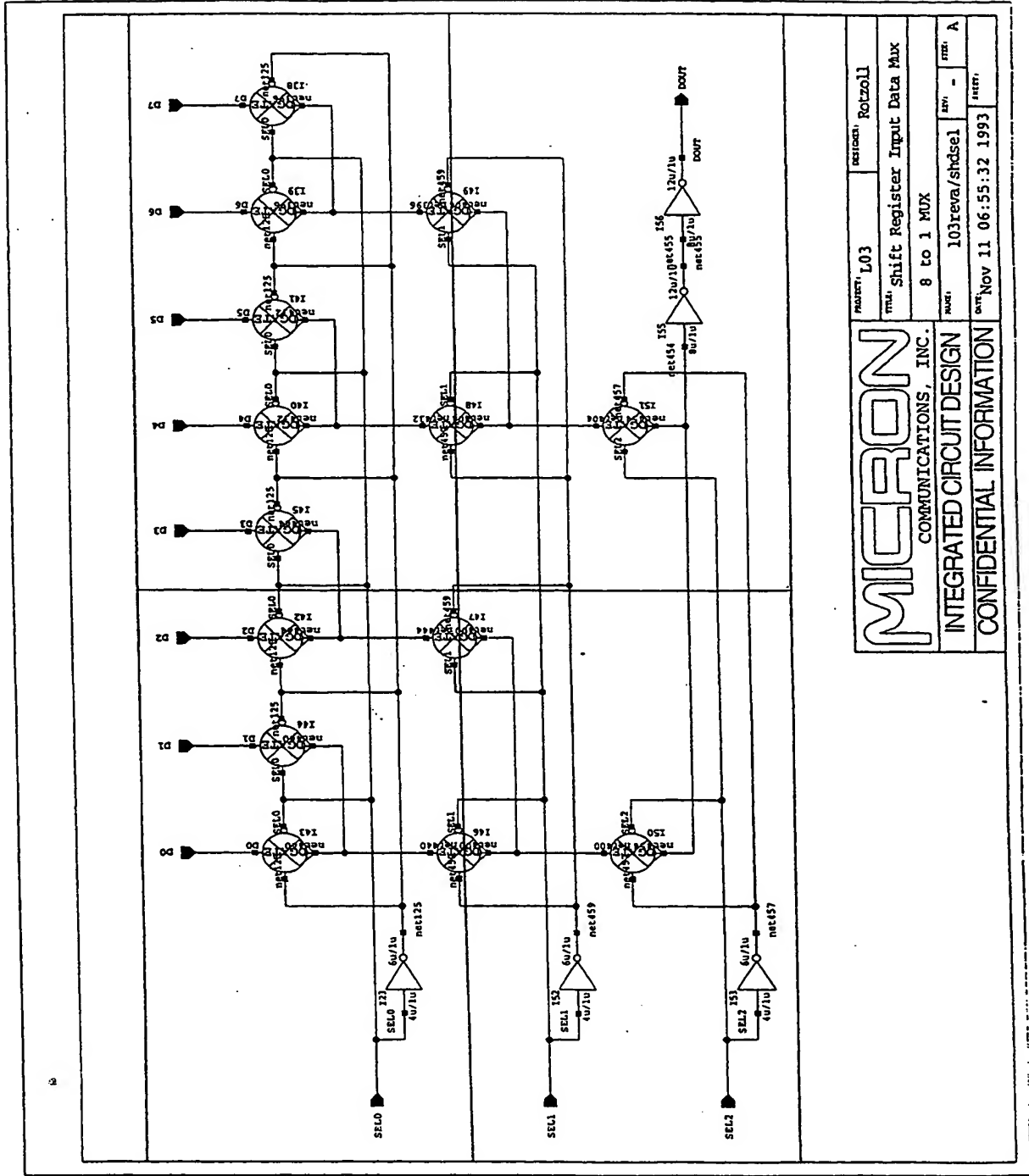
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

7.17AB	7.17BB
7.17AA	7.17BA

Ex 11.11

Fig. 7.17



MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Shift Register Input Data Mux	
INTEGRATED CIRCUIT DESIGN		8 to 1 MUX	
CONFIDENTIAL INFORMATION		NAME: 103revA/shdse1	REV: -
		DATE: Nov 11 06:55:32 1993	ITEM: A
		PART:	

7.18AA	7.18AB	7.18AC
7.18BA	7.18BB	7.18BC
7.18CA	7.18CB	7.18CC

И. П. И.

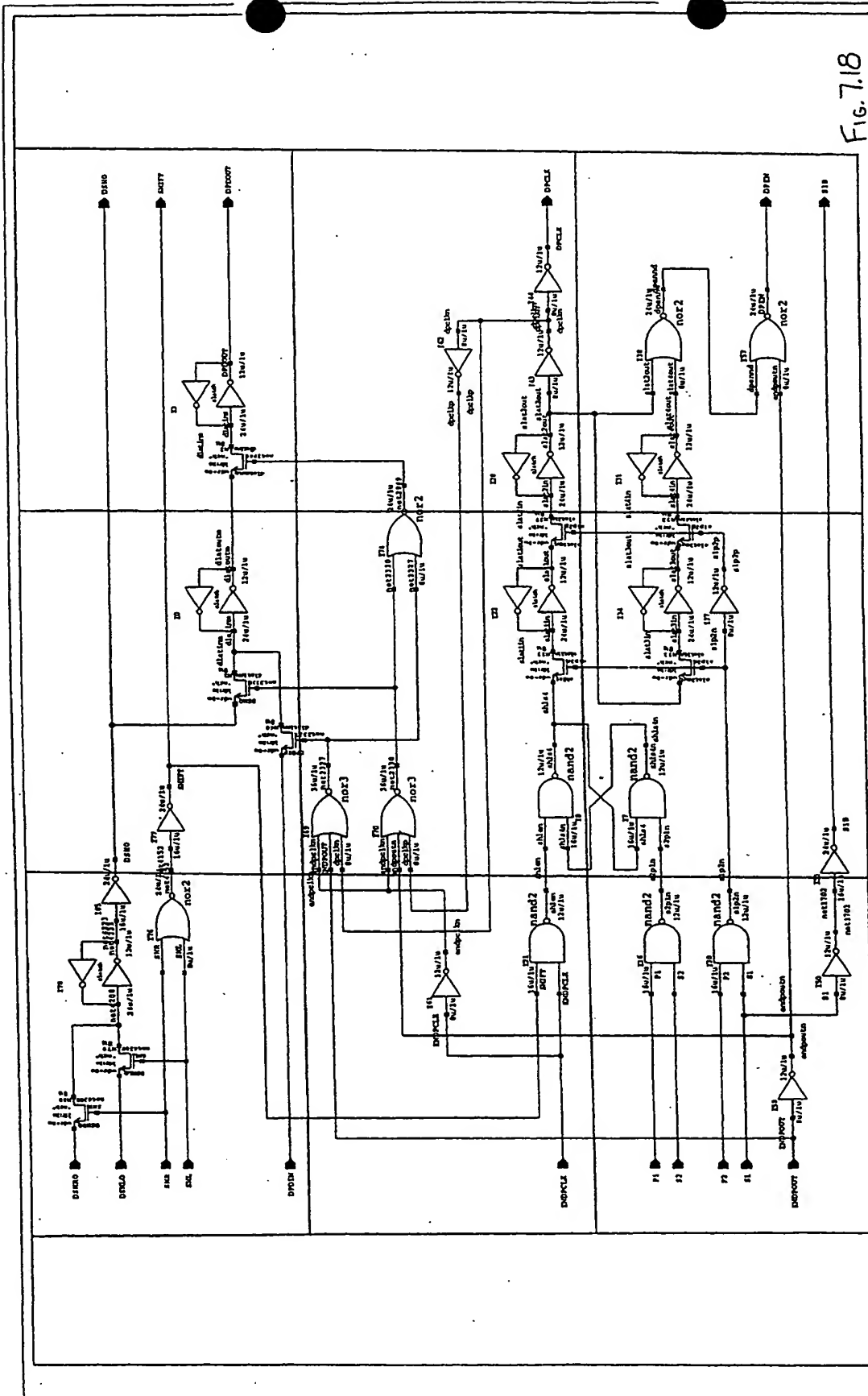


Fig. 7.18

MICRON	PRODUCT	L03	REVISED	Rev 2.01
	NAME	Digital Port Output Controller		
	DATE	10/19/90	DESIGNED BY	RL
	DATE	11/12/93	REVISION	1



COMMUNICATIONS, INC.

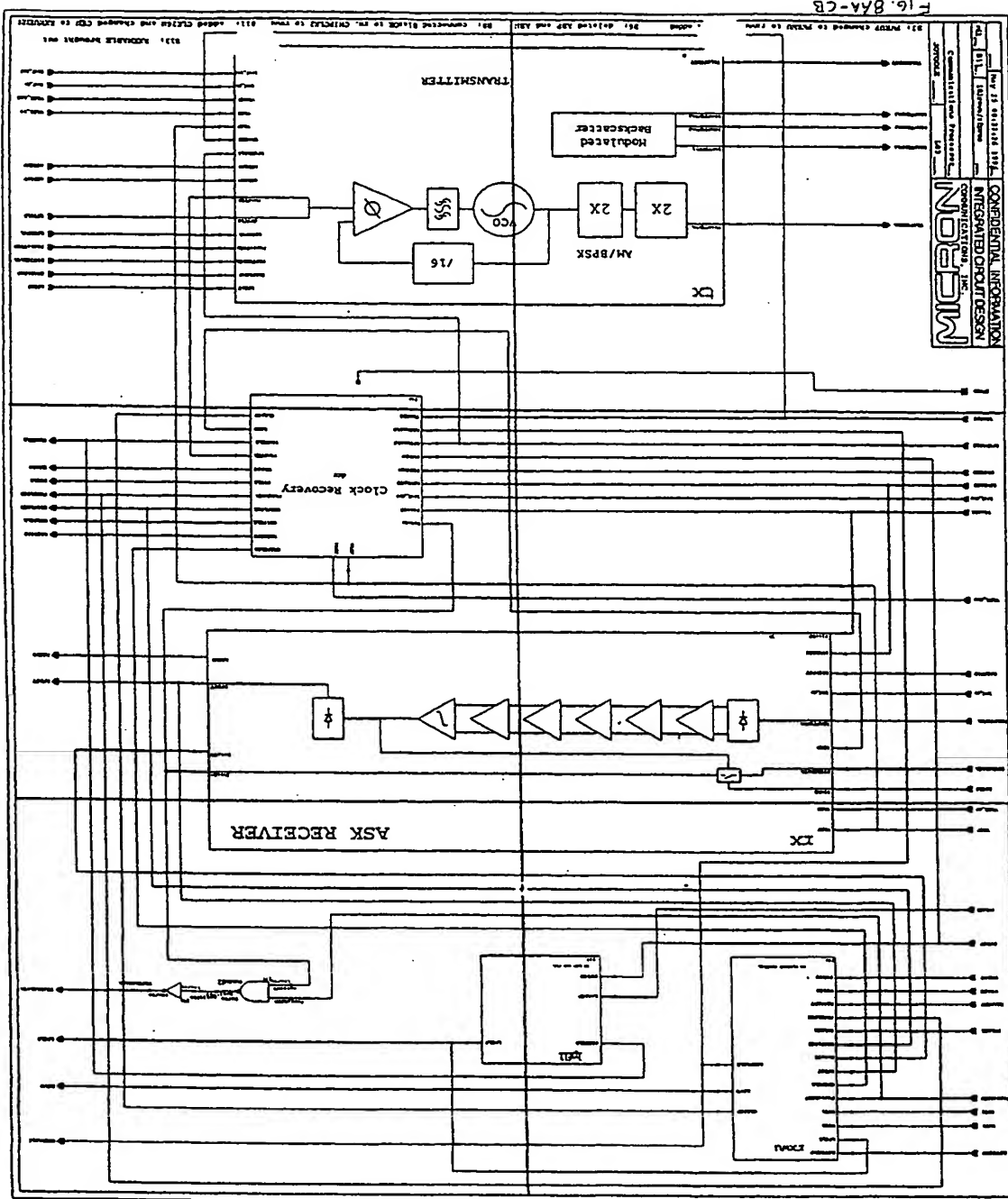
INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

Nov 12 10:05:40 1993

8AA	8AB
8BA	8BB
8CA	8CB

IL II  



8.01AA	8.01AB	8.01AC	8.01AD	8.01AE
8.01BA	8.01BB	8.01BC	8.01BD	8.01BE
8.01CA	8.01CB	8.01CC	8.01CD	8.01CE
8.01DA	8.01DB	8.01DC	8.01DD	8.01DE

II II II II II II

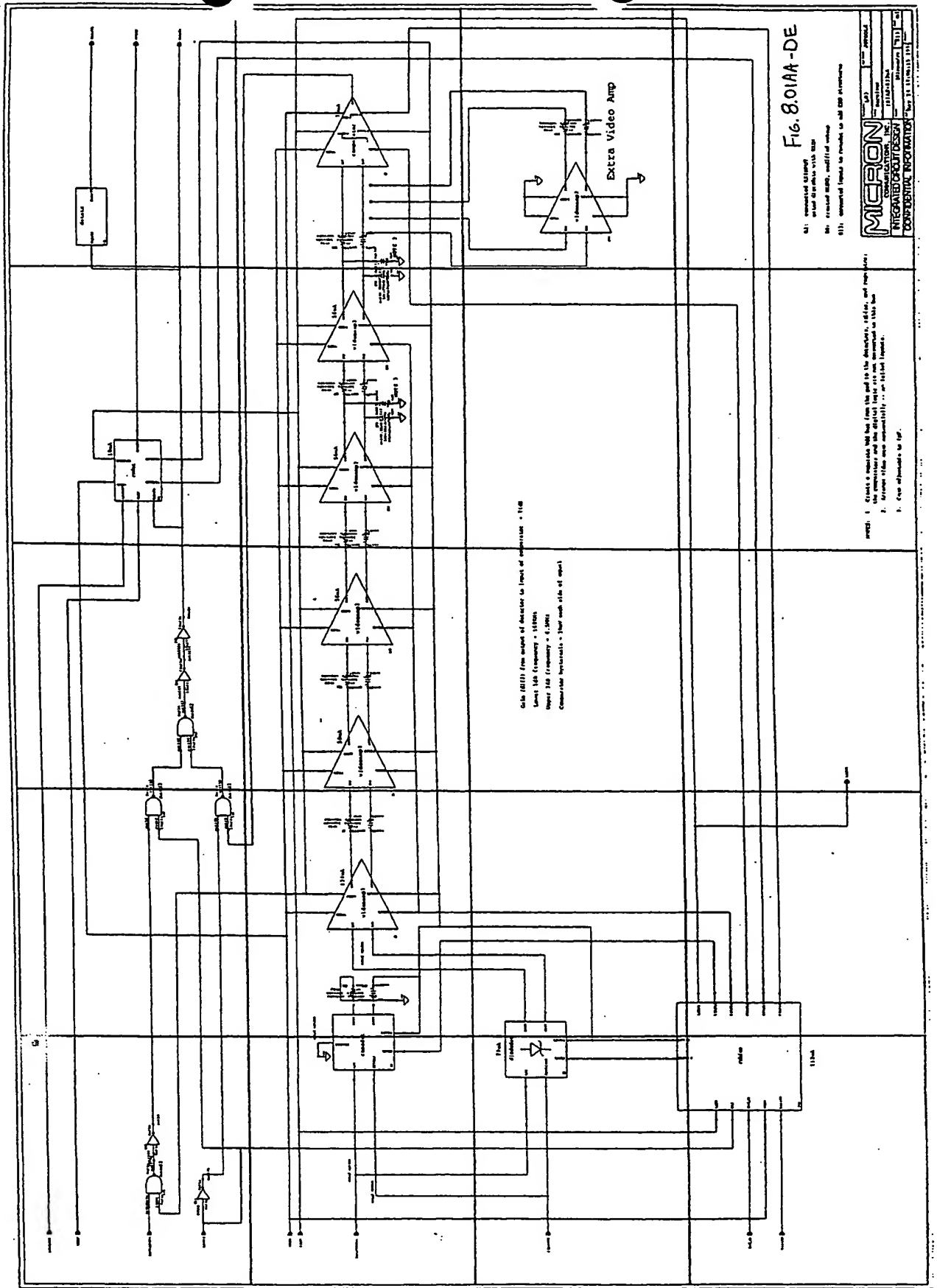


Fig. 8.01AA-DE

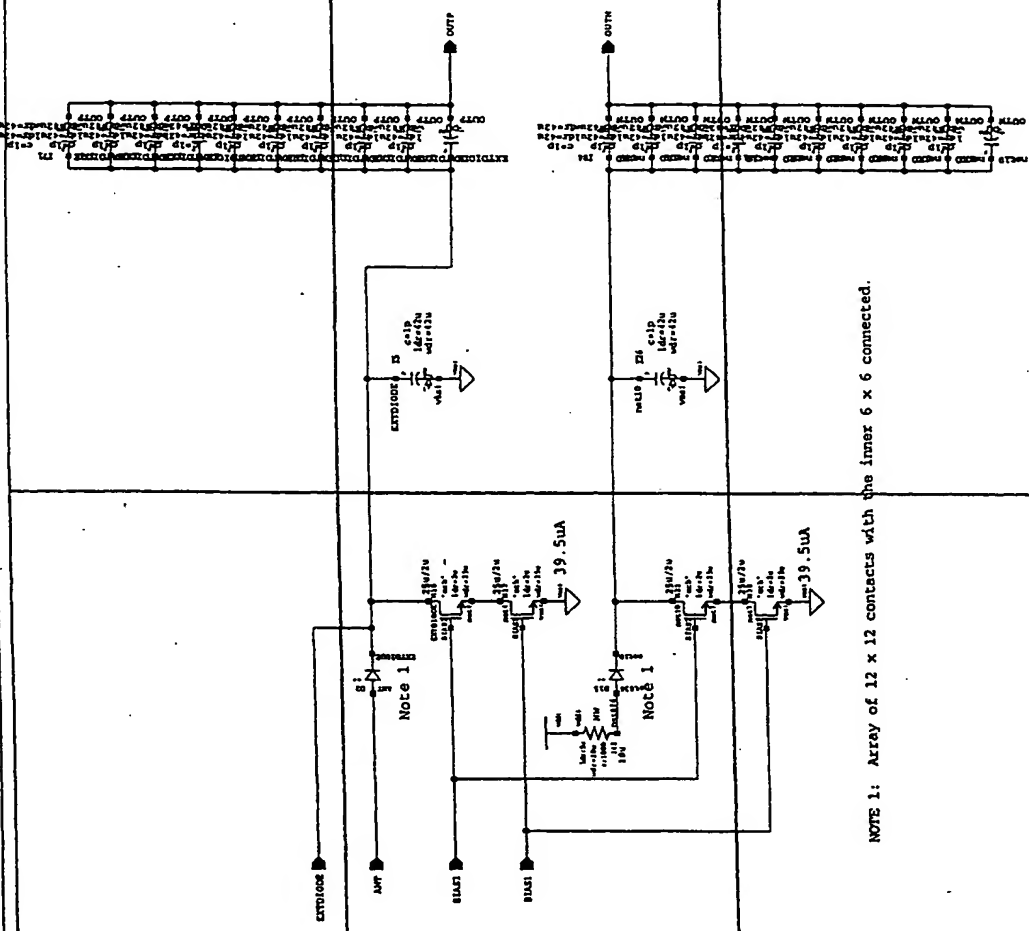
- (1) removed output
- (2) added output with 10V
- (3) removed output, modified output
- (4) removed output to provide to all 10V

NOTE: 1. Circuit is modified to be from the input to the detector, video, and output.
2. The detector and the output are not connected to the 10V.
3. The output is not connected to the 10V.
4. The output is not connected to the 10V.

MICRON
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

8.0101AA	8.0101AB
8.0101BA	8.0101BB
8.0101CA	8.0101CB

II II II II II II

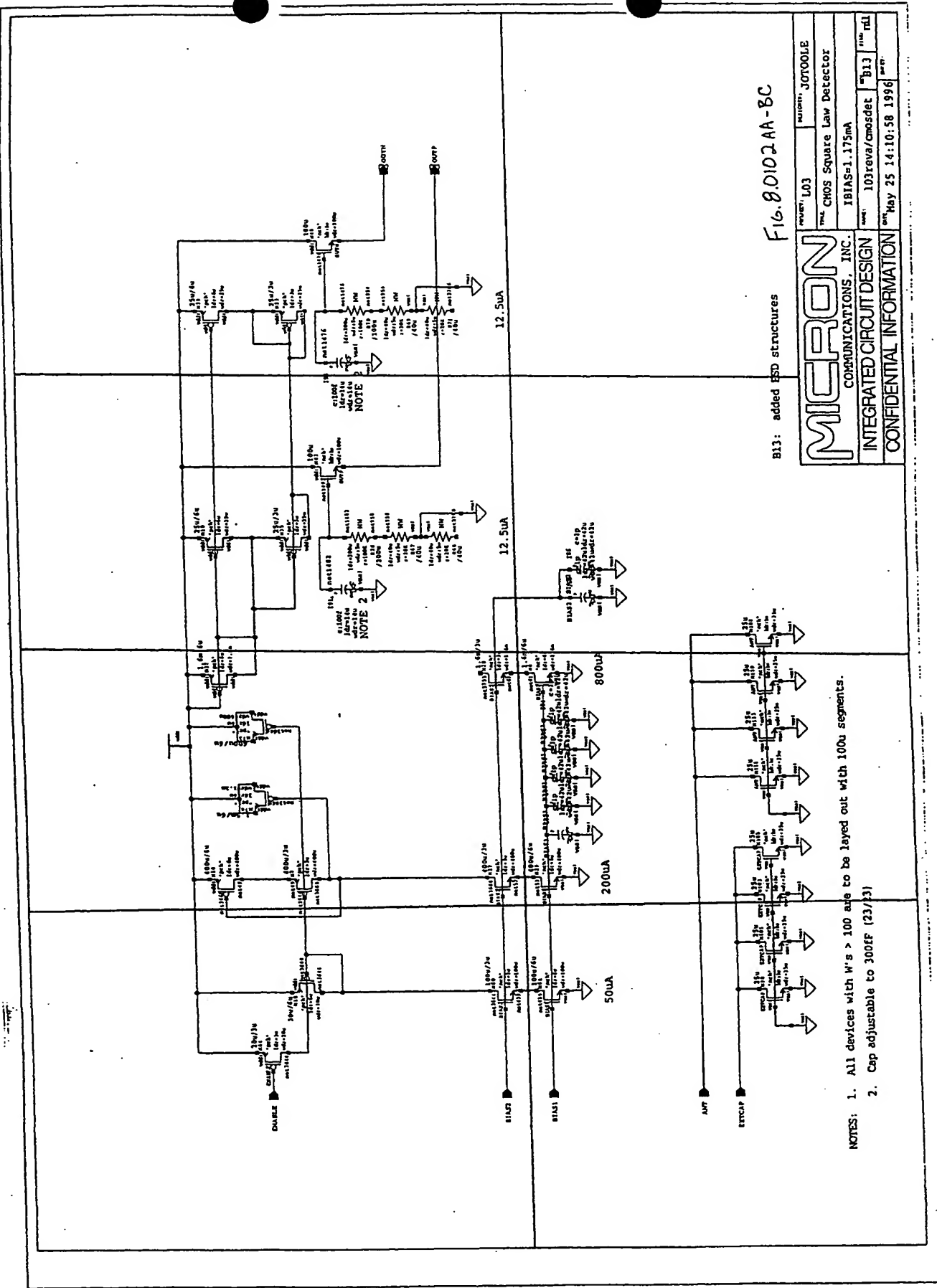


NOTE 1: Array of 12 x 12 contacts with the inner 6 x 6 connected.

Fig. 8.0101AA-C8

MICRON		PROPERTY L03	DATE 10/10/96	JOT000LE
COMMUNICATIONS, INC.		Schottky Diode Detector		
INTEGRATED CIRCUIT DESIGN		IBIAS=79uA		
CONFIDENTIAL INFORMATION		B13		
		May 24 13:54:28 1996		

- B2: connected EXTIOIDE line
- B6: schottky array changed to 6x6
- rf cap reduced to 1pF
- B8: increased Cc to 10pF; increased Crf to 1pF
- B11: added 1K resistor in series with dummy diode for ESD



B13: added BSD structures FIG. 8.0102 AA-8C

MICRON		REVISION: L03	DATE: J0700LE
COMMUNICATIONS, INC.		TYPE: CMOS Square Law Detector	
INTEGRATED CIRCUIT DESIGN		IBIAS=1.175mA	
CONFIDENTIAL INFORMATION		DATE: 103reva/cmosdet	B13
		DATE: May 25 14:10:58 1996	REV:

NOTES: 1. All devices with W's > 100 are to be laid out with 100u segments.
2. Cap adjustable to 300fF (23/f3)

8.0103AA	8.0103AB	8.0103AC	8.0103AD	8.0103AE	8.0103AF
8.0103BA	8.0103BB	8.0103BC	8.0103BD	8.0103BE	8.0103BF
8.0103CA	8.0103CB	8.0103CC	8.0103CD	8.0103CE	8.0103CF

SECRET

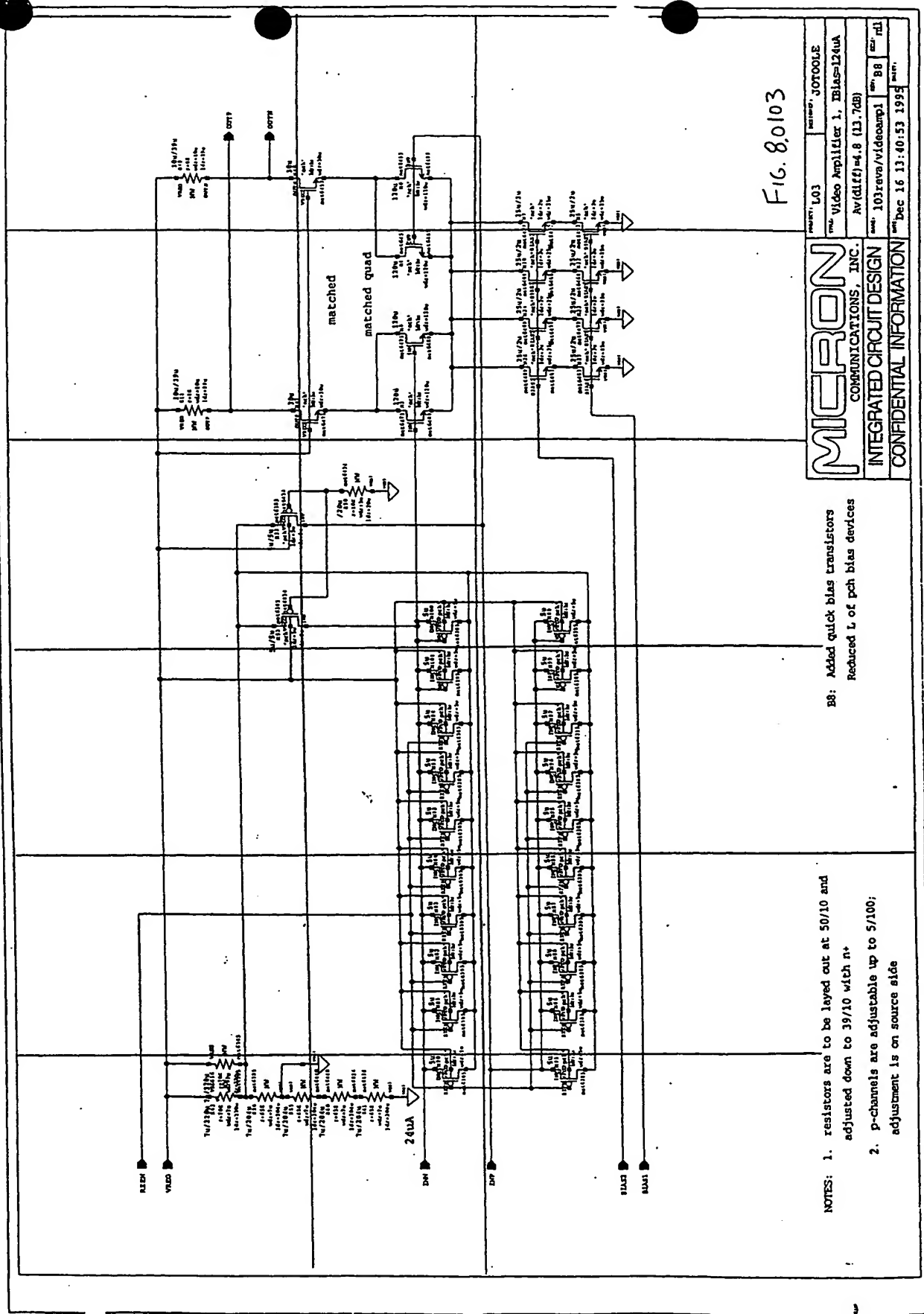


FIG. 8.0103

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03	DESIGN: J07000LE
Title: Video Amplifier 1, Bias=124uA	
Av(dB)=4.8 (13.7dB)	
Rev: 103revA/Videoamp1	Rev: 88
Dec 16 13:40:53 1995	

88: Added quick bias transistors
Reduced I_b of pch bias devices

- NOTES:
1. resistors are to be layed out at 50/10 and adjusted down to 39/10 with n+
 2. p-channels are adjustable up to 5/100; adjustment is on source side

8.0104AA	8.0104AB	8.0104AC
8.0104BA	8.0104BB	8.0104BC

[illegible]

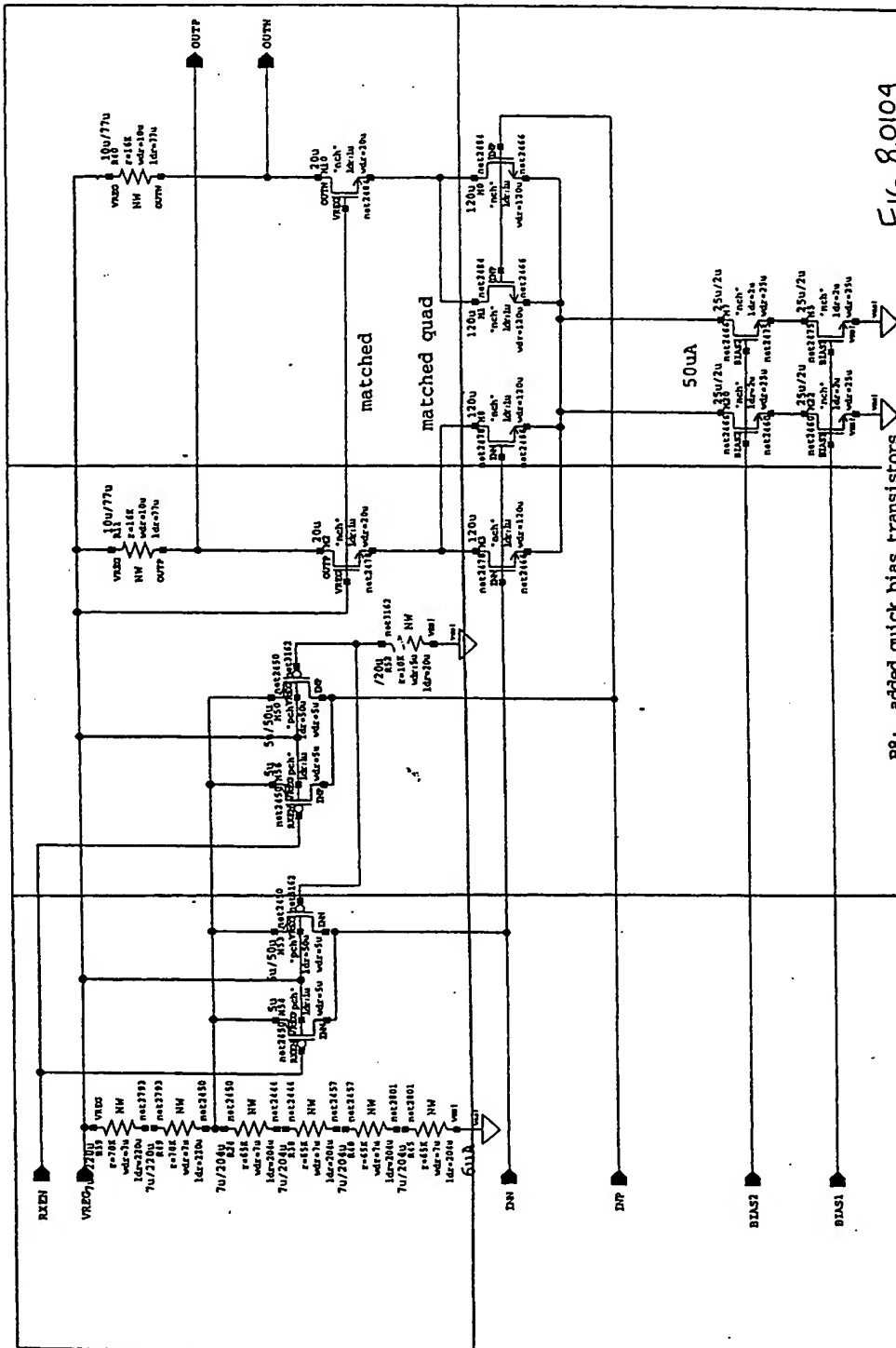


FIG 8.0104

B8: added quick bias transistors

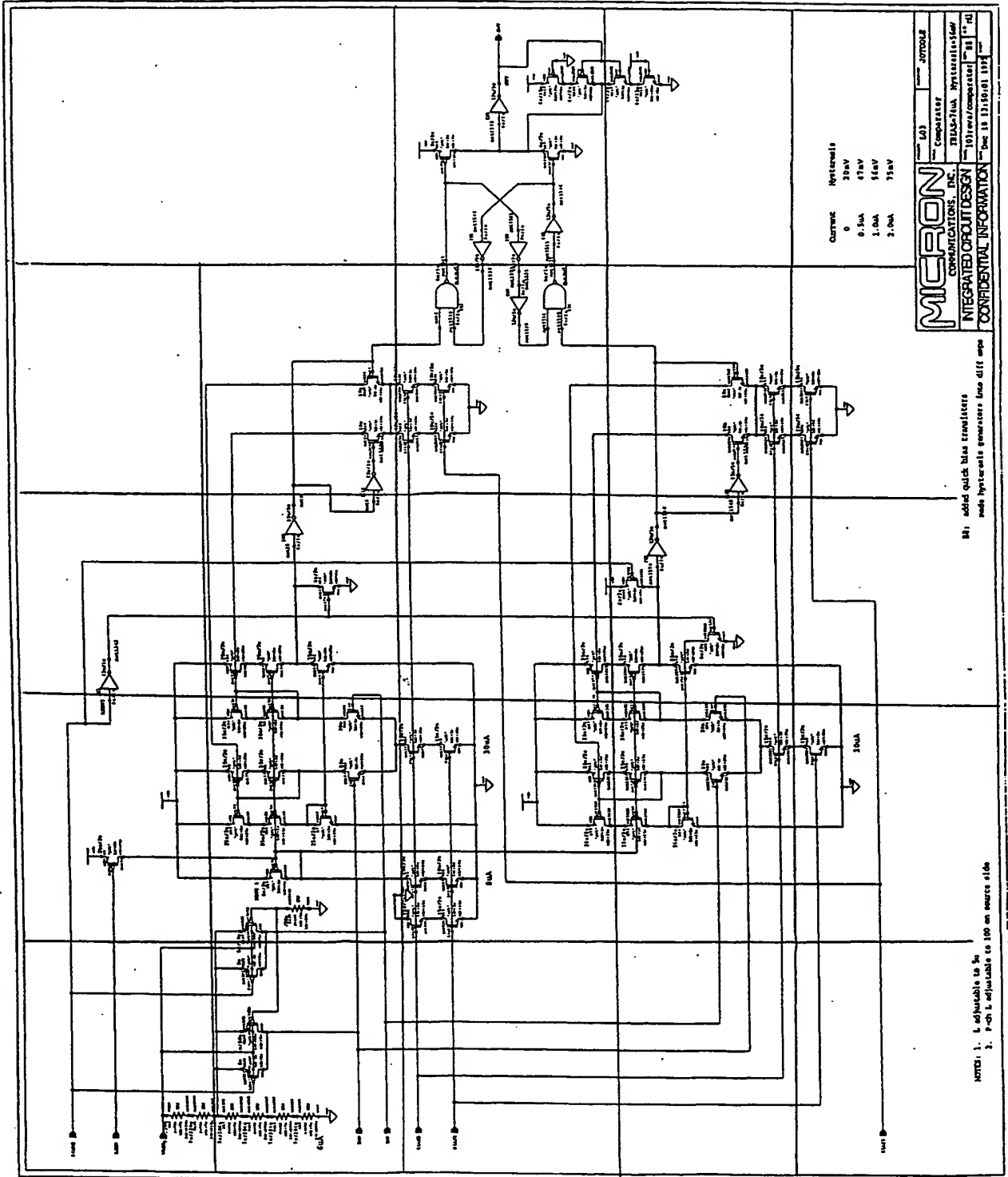
- NOTES:
1. resistors are to be layed out at 100/10 and adjusted down to 77/10 with n+
 2. p-channels are adjustable up to 5/100; adjustment is on the source side.

MICRON		DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		DATE: L03
INTEGRATED CIRCUIT DESIGN		TITLE: Video Amplifier 2, IBias=56uA
CONFIDENTIAL INFORMATION		AV(diff)=5.6 (15dB)
		MODE: 103reva/videoamp2
		REV: B8
		DATE: Dec 16 13:42:25 1995

8.0105AA	8.0105AB	8.0105AC	8.0105AD	
8.0105BA	8.0105BB	8.0105BC	8.0105BD	
8.0105CA	8.0105CB	8.0105CC	8.0105CD	8.0105CE
8.0105DA	8.0105DB	8.0105DC	8.0105DD	8.0105DE
8.0105EA	8.0105EB	8.0105EC	8.0105ED	

EEB.01.005

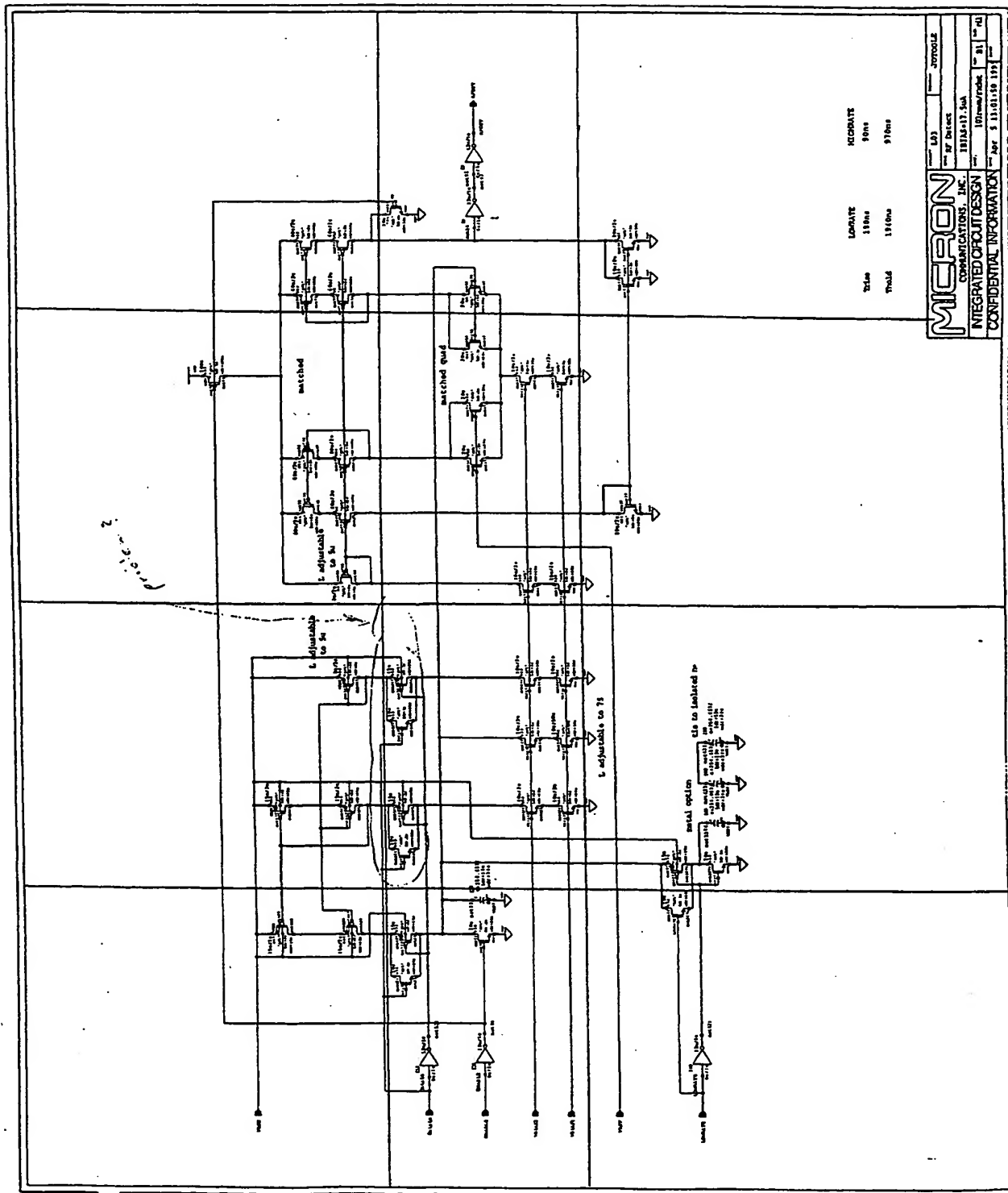
Fig. 8.0105



8.0106AA	8.0106AB	8.0106AC	8.0106AD
8.0106BA	8.0106BB	8.0106BC	8.0106BD
8.0106CA	8.0106CB	8.0106CC	8.0106CD

BB.0106

Fig. 8.0106

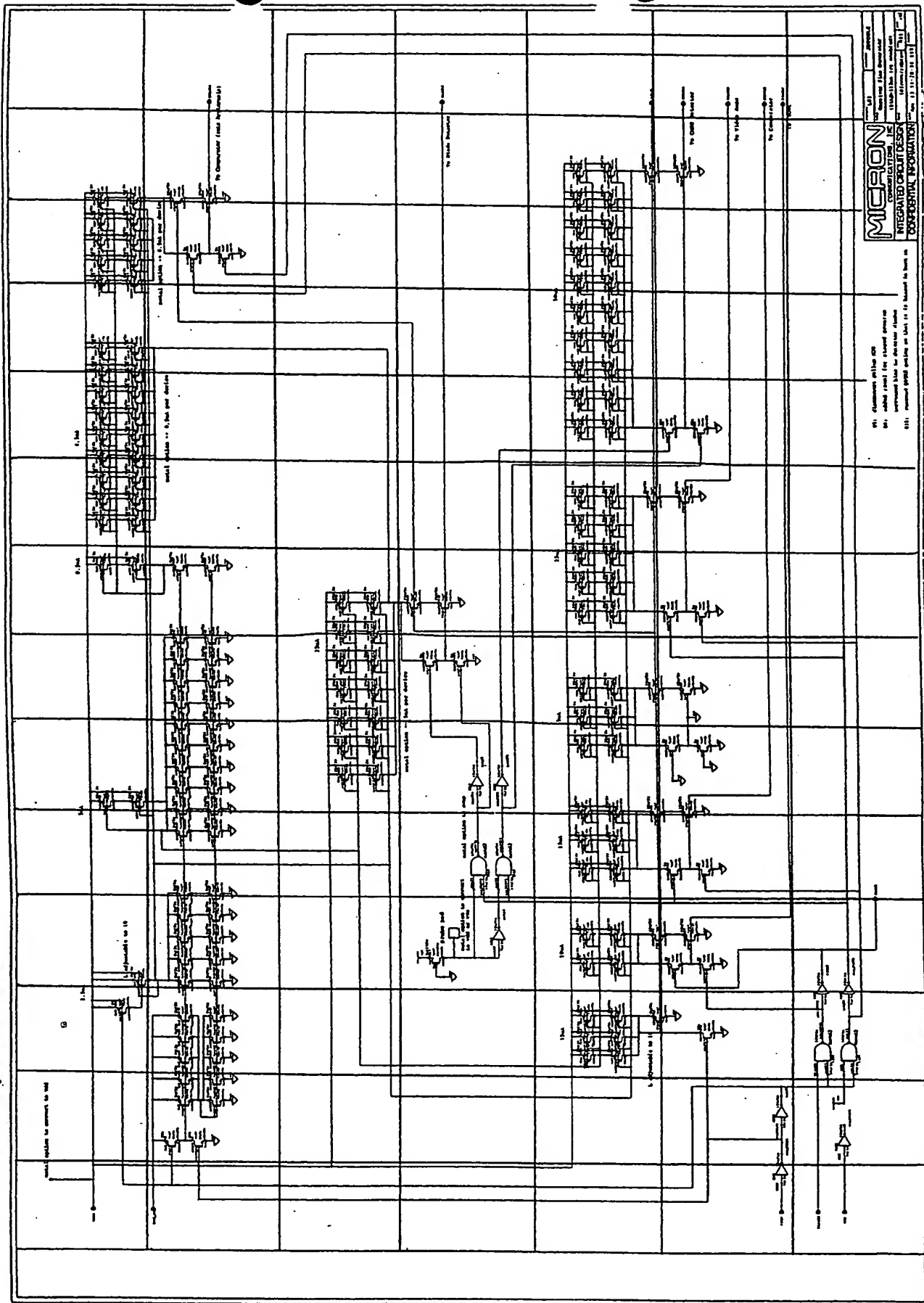


MICRON		L33	JURYVILLE
INTEGRATED CIRCUIT DESIGN		1813A417 Sub	
CONFIDENTIAL INFORMATION		10/10/1975	21
		Apr 1 11:01:18 1975	20

LOCATE
1813A417
1813A417 Sub

8.0107AA	8.0107AB	8.0107AC	8.0107AD	8.0107AE	8.0107AF	8.0107AG	8.0107AH	8.0107AI	8.0107AJ	8.0107AK	8.0107AL	8.0107AM	
8.0107BA	8.0107BB	8.0107BC	8.0107BD	8.0107BE	8.0107BF	8.0107BG	8.0107BH	8.0107BI	8.0107BJ	8.0701BK	8.0107BL	8.0107BM	8.0107BN
8.0107CA	8.0107CB	8.0107CC	8.0107CD	8.0107CE	8.0107CF	8.0107CG	8.0107CH	8.0107CI	8.0107CJ	8.0107CK	8.0107CL	8.0107CM	8.0107CN
8.0107DA	8.0107DB	8.0107DC	8.0107DD	8.0107DE	8.0107DF	8.0107DG	8.0107DH	8.0107DI	8.0107DJ	8.0107DK	8.0107DL	8.0107DM	8.0107DN
8.0107EA	8.0107EB	8.0107EC	8.0107ED	8.0107EE	8.0107EF	8.0107EG	8.0107EH	8.0107EI	8.0107EJ	8.0107EK	8.0107EL	8.0107EM	8.0107EN
8.0107FA	8.0107FB	8.0107FC	8.0107FD	8.0107FE	8.0107FF	8.0107FG	8.0107FH	8.0107FI	8.0107FJ	8.0107FK	8.0107FL	8.0107FM	8.0107FN
8.0107GA	8.0107GB	8.0107GC	8.0107GD	8.0107GE	8.0107GF	8.0107GG	8.0107GH	8.0107GI	8.0107GJ	8.0107GK	8.0107GL	8.0107GM	8.0107GN


 DEPARTMENT OF DEFENSE



10. Customer's Name: IBM
11. Project Name: Project 622
12. Project Number: 622-1000
13. Project Date: 11/17/73
14. Project Location: 11/17/73
15. Project Status: 11/17/73
16. Project Manager: 11/17/73
17. Project Engineer: 11/17/73
18. Project Designer: 11/17/73
19. Project Checker: 11/17/73
20. Project Approver: 11/17/73
21. Project Reviewer: 11/17/73
22. Project Auditor: 11/17/73
23. Project Validator: 11/17/73
24. Project Verifier: 11/17/73
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94. Project Validator: 11/17/73
95. Project Validator: 11/17/73
96. Project Validator: 11/17/73
97. Project Validator: 11/17/73
98. Project Validator: 11/17/73
99. Project Validator: 11/17/73
100. Project Validator: 11/17/73

Fig. 1.0107

8.0108AC	8.0108AB	8.0108AA
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EE 88.0108

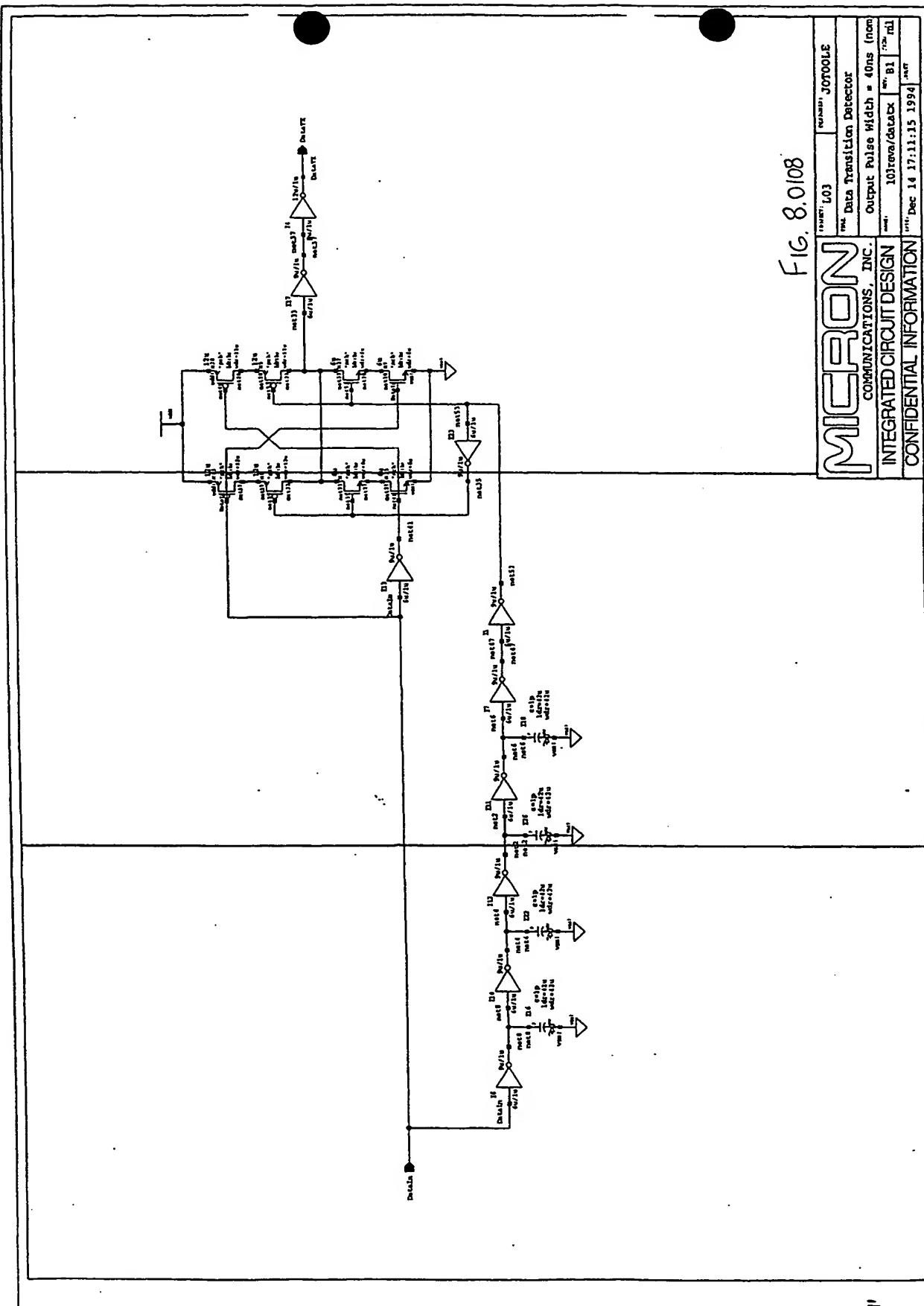


Fig. 8.0108

MICRON		ITEM#	J0700LE
COMMUNICATIONS, INC.		NAME	Data Transition Detector
INTEGRATED CIRCUIT DESIGN		Output Pulse Width = 40ns (nom)	
CONFIDENTIAL INFORMATION		103revs/datasheet	Rev. B1
		DATE	17:11:15 1994

8.02AA	8.02AB	8.02AC
8.02BA	8.02BB	8.02BC

EEB.002

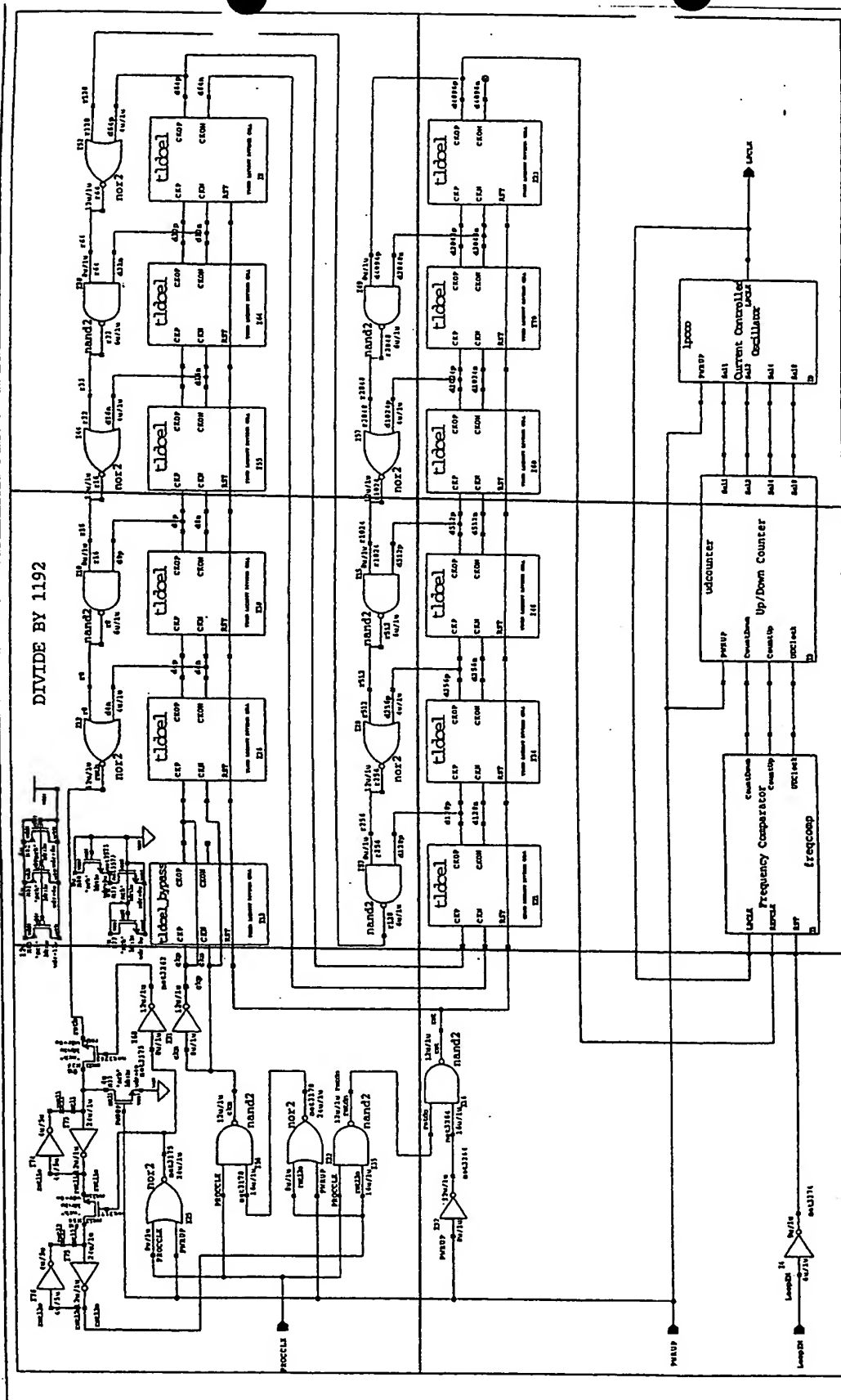


Fig. 8.02

MICRON		PROPERTY 103	REVISED	J0700LE
COMMUNICATIONS, INC.		Low Power PLL (1ab-55A typ)		
INTEGRATED CIRCUIT DESIGN		f _{osc} 9.5375MHz / f _{out} 8000Hz		
CONFIDENTIAL INFORMATION		101rev/0221		
		Mar 26 16:14:18 1996		

B10: first divider stage bypassed

8.0201AB

8.0201AA

II II III III III III III III

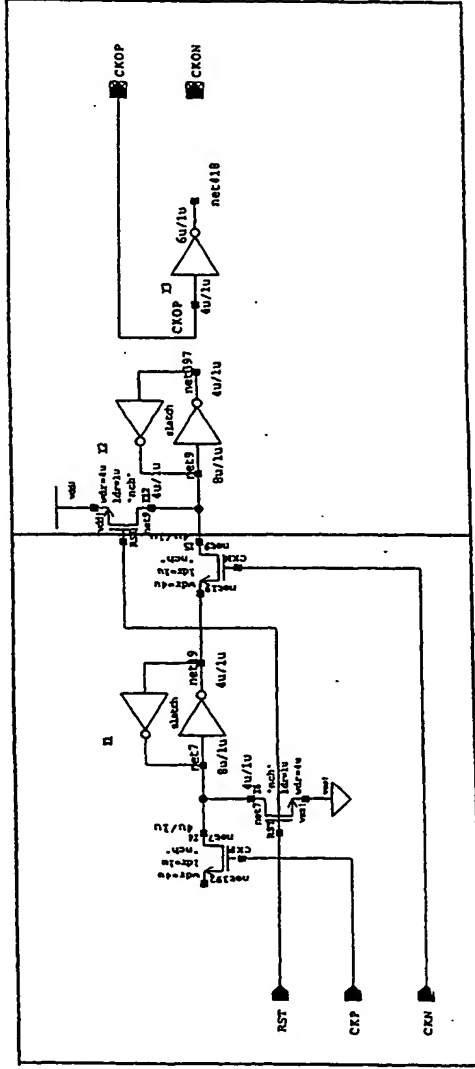


Fig. 8.0201

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Titled Lockout Divider Cell	
INTEGRATED CIRCUIT DESIGN		REV: J03rev0/cldcel_bypass	REV: B10
CONFIDENTIAL INFORMATION		DATE: Mar 26 13:54:47 1996	DATE: A

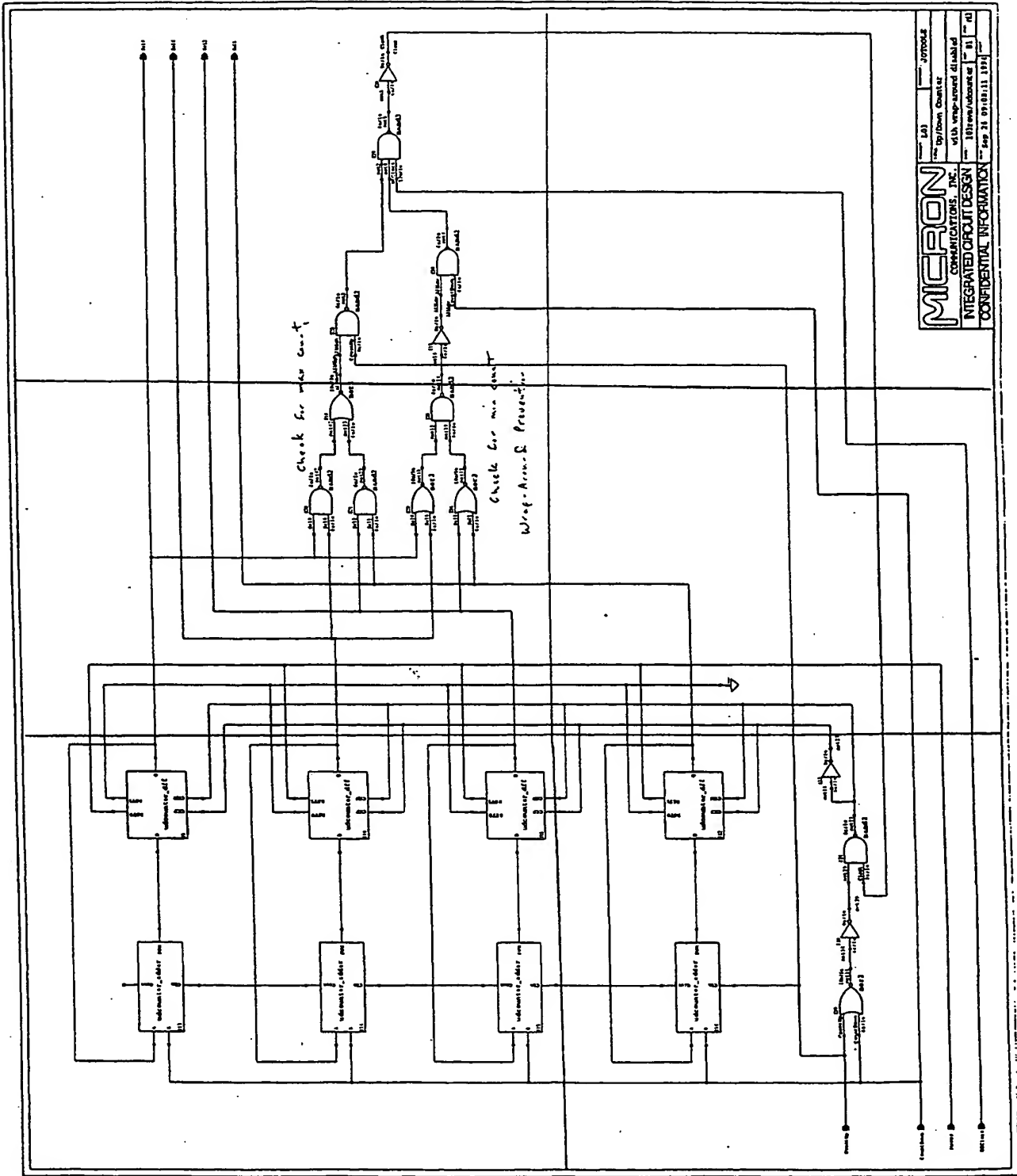
B10: new cell to bypass 1st counter stage

8.0202AA	8.0202AB	8.0202AC	8.0202AD
8.0202BA	8.0202BB	8.0202BC	8.0202BD
8.0202CA	8.0202CB	8.0202CC	8.0202CD

II II II II II II II II

8.0203AA	8.0203AB	8.0203AC
8.0203BA	8.0203BB	8.0203BC

EE BB BB BB

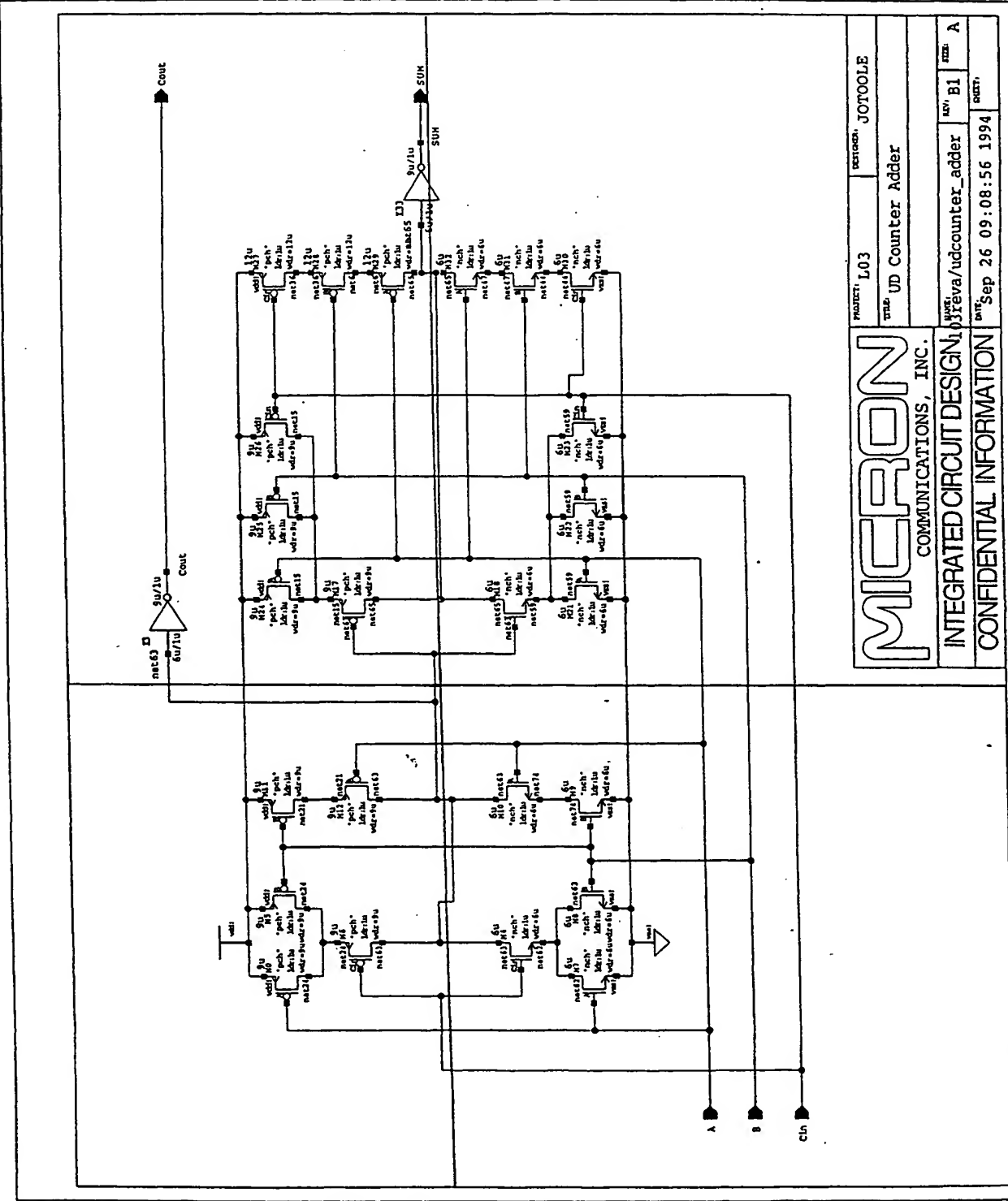


MICRON
 CORPORATION, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

FIG. 80203

8.020301AA	8.020301AB
8.020301BA	8.020301BB

II II 8.020301



MICRON		PROJECT: L03		DESIGNED: JOTOOLE	
		TITLE: UD Counter Address			
		USER: jfreva/udcounter_addr		REV: B1	
		DATE: Sep 26 09:08:56 1994		PAGE: A	

MICRON
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

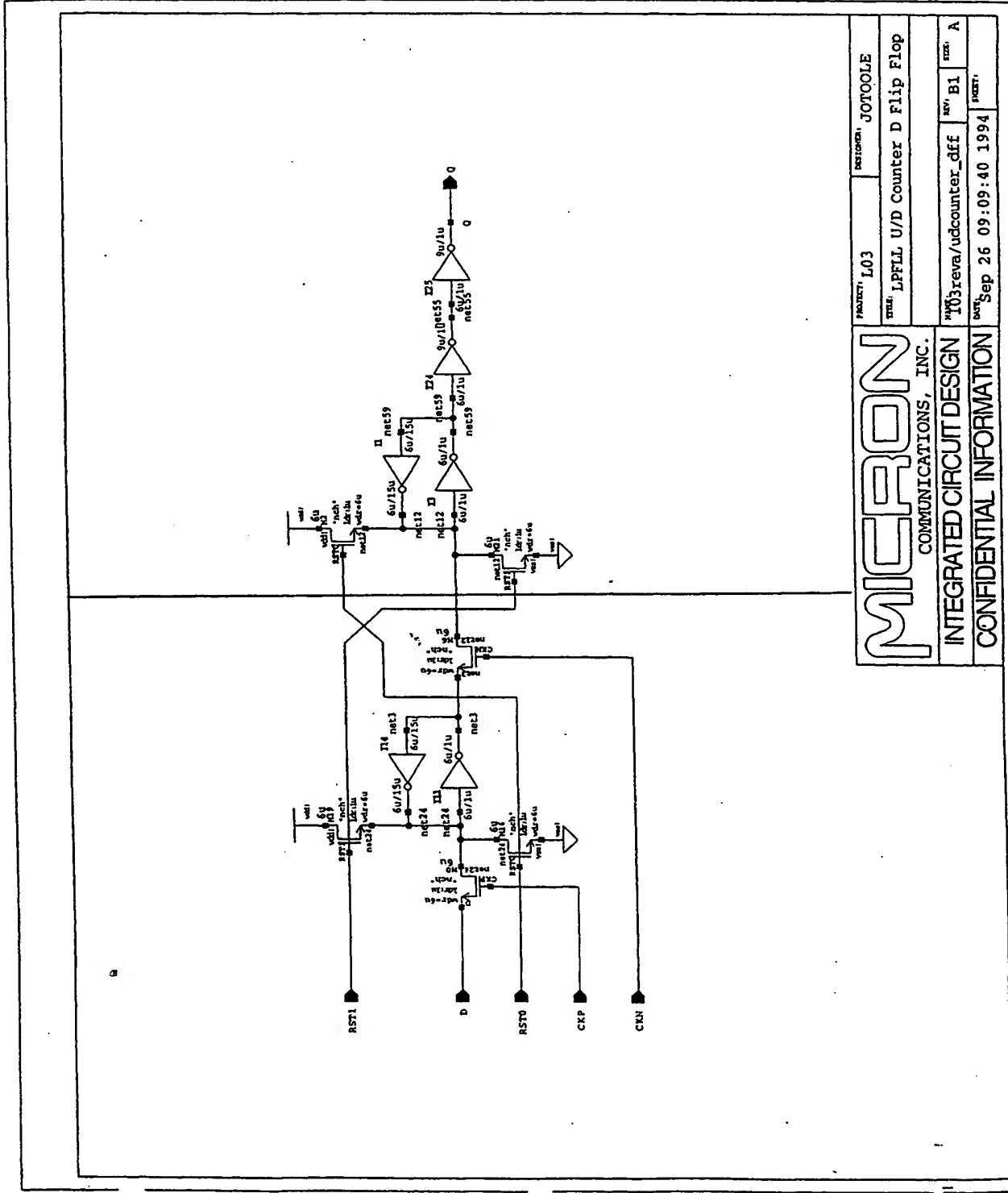
CONFIDENTIAL INFORMATION

Fig. 8.020501

8.020302AB

8.020302AA

8.020302

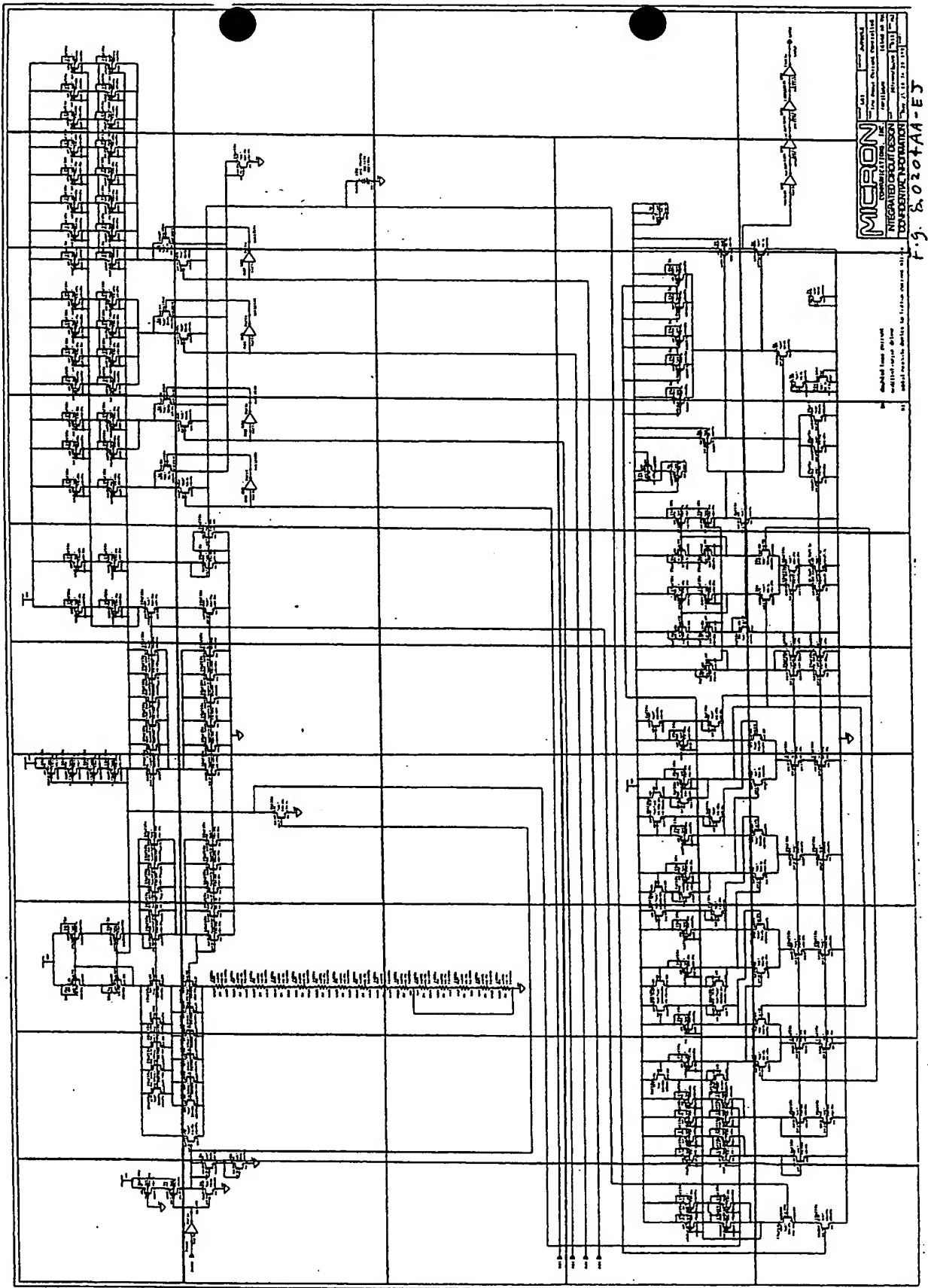


MICRON		PROJECT: JOTOOLE
COMMUNICATIONS, INC.		DESIGNER: L03
INTEGRATED CIRCUIT DESIGN		TITLE: LPFLL U/D Counter D Flip Flop
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:09:40 1994
		REV: B1
		SIZE: A

Fig. 8.020302

8.0204AA	8.0204AB	8.0204AC	8.0204AD	8.0204AE	8.0204AF	8.0204AG	8.0204AH	8.0204AI	8.0204AJ
8.0204BA	8.0204BB	8.0204BC	8.0204BD	8.0204BE	8.0204BF	8.0204BG	8.0204BH	8.0204BI	8.0204BJ
8.0204CA	8.0204CB	8.0204CC	8.0204CD	8.0204CE	8.0204CF	8.0204CG	8.0204CH	8.0204CI	
8.0204DA	8.0204DB	8.0204DC	8.0204DD	8.0204DE	8.0204DF	8.0204DG	8.0204DH	8.0204DI	
8.0204EA	8.0204EB	8.0204EC	8.0204ED	8.0204EE	8.0204EF	8.0204EG	8.0204EH	8.0204EI	8.0204EJ

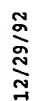
15.02.88 6.11



MICRON
INTEGRATED CIRCUIT DESIGN
CORPORATION
10000 NE 28th Ave.
Bellevue, WA 98005
Tel: (206) 882-2000
Fax: (206) 882-2001

80204AA-E3

Fig. 8.0204AA-E3



PROJECT#	L03	CUSTOMER	Ratzell	
TITLE				
Titled Lockout Divider Cell				
NUMBER	103reva/Eldecal		REV#	SIZE#
DATE			A	
Sep 22 15:26:56 1994			PROJECT#	

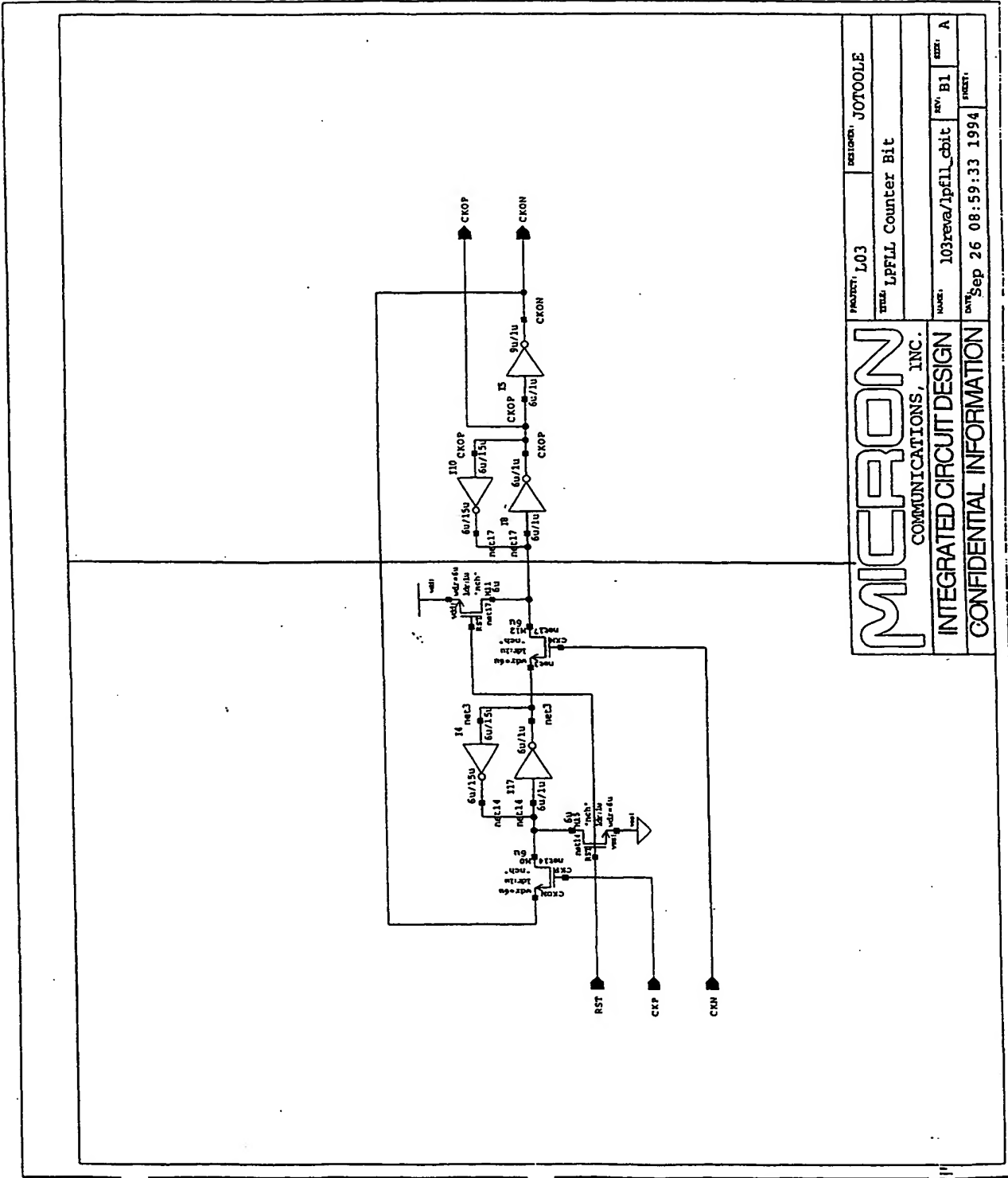
MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

8.03AB

8.03AA

LEWIS & CLARK

FIG. 8.03



MICRON		PROPERTY: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: LPFLL Counter Bit	
INTEGRATED CIRCUIT DESIGN		DATE: 103revA/lpfill_cbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 08:59:33 1994	REV: A

8.04AA	8.04AB	8.04AC	8.04AD	8.04AE	8.04AF
8.04BA	8.04BB	8.04BC	8.04BD	8.04BE	8.04BF
8.04CA	8.04CB	8.04CC	8.04CD	8.04CE	8.04CF
8.04DA	8.04DB	8.04DC	8.04DD	8.04DE	
8.04EA	8.04EB	8.04EC	8.04ED	8.04EE	

EE BB.0004

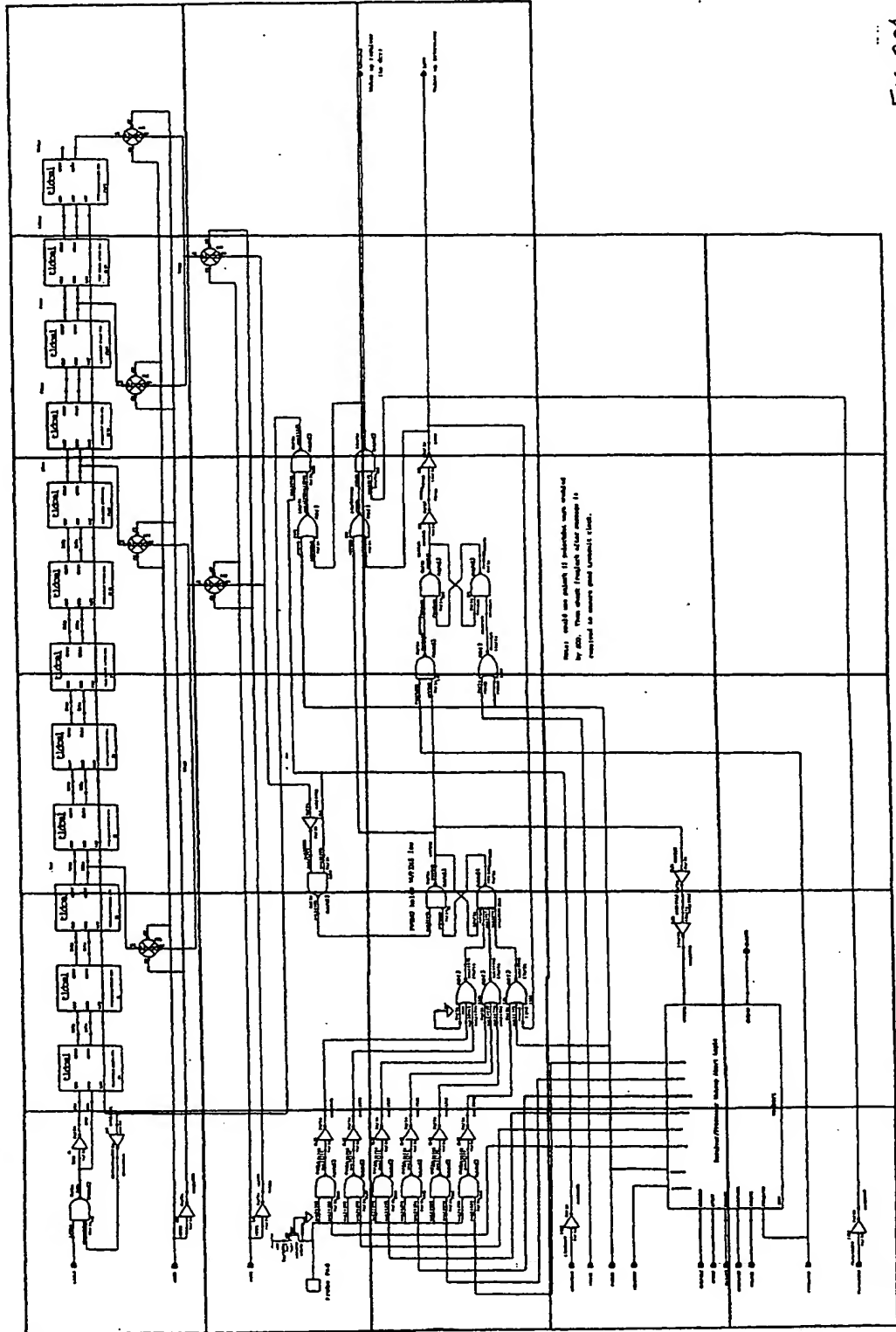


FIG. 804

MICRON
 INTEGRATED CIRCUITS
 CONFIDENTIAL INFORMATION

801: added channel to output
 802: added channel to output
 803: added channel to output
 804: added channel to output
 805: added channel to output
 806: added channel to output
 807: added channel to output
 808: added channel to output
 809: added channel to output
 810: added channel to output

8.0401AA	8.0401AB	8.0401AC	8.0401AD	8.0401AE
8.0401BA	8.0401BB	8.0401BC	8.0401BD	8.0401BE
8.0401CA	8.0401CB	8.0401CC	8.0401CD	8.0401CE
8.0401DA	8.0401DB	8.0401DC	8.0401DD	8.0401DE

II II II II II II

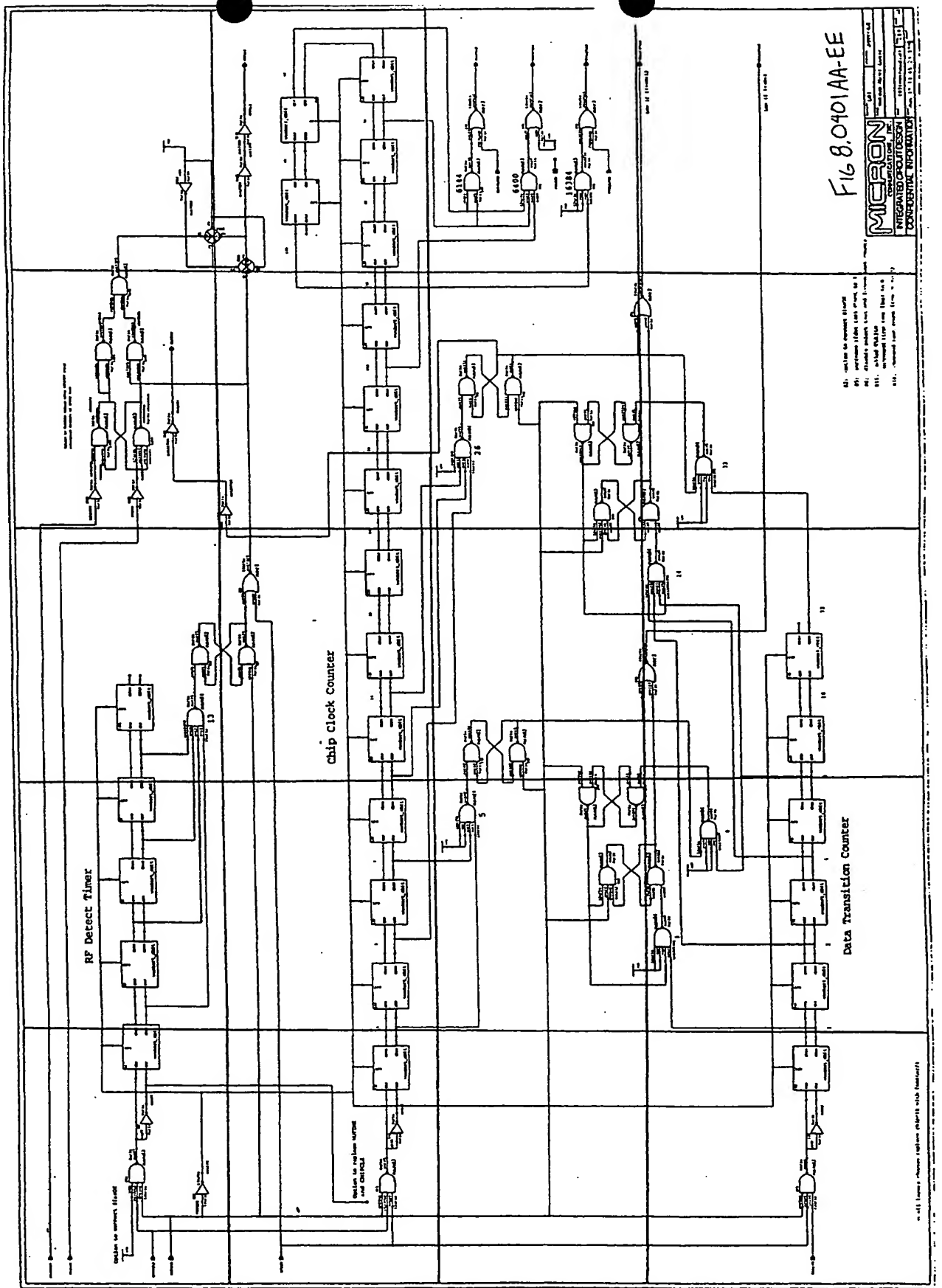


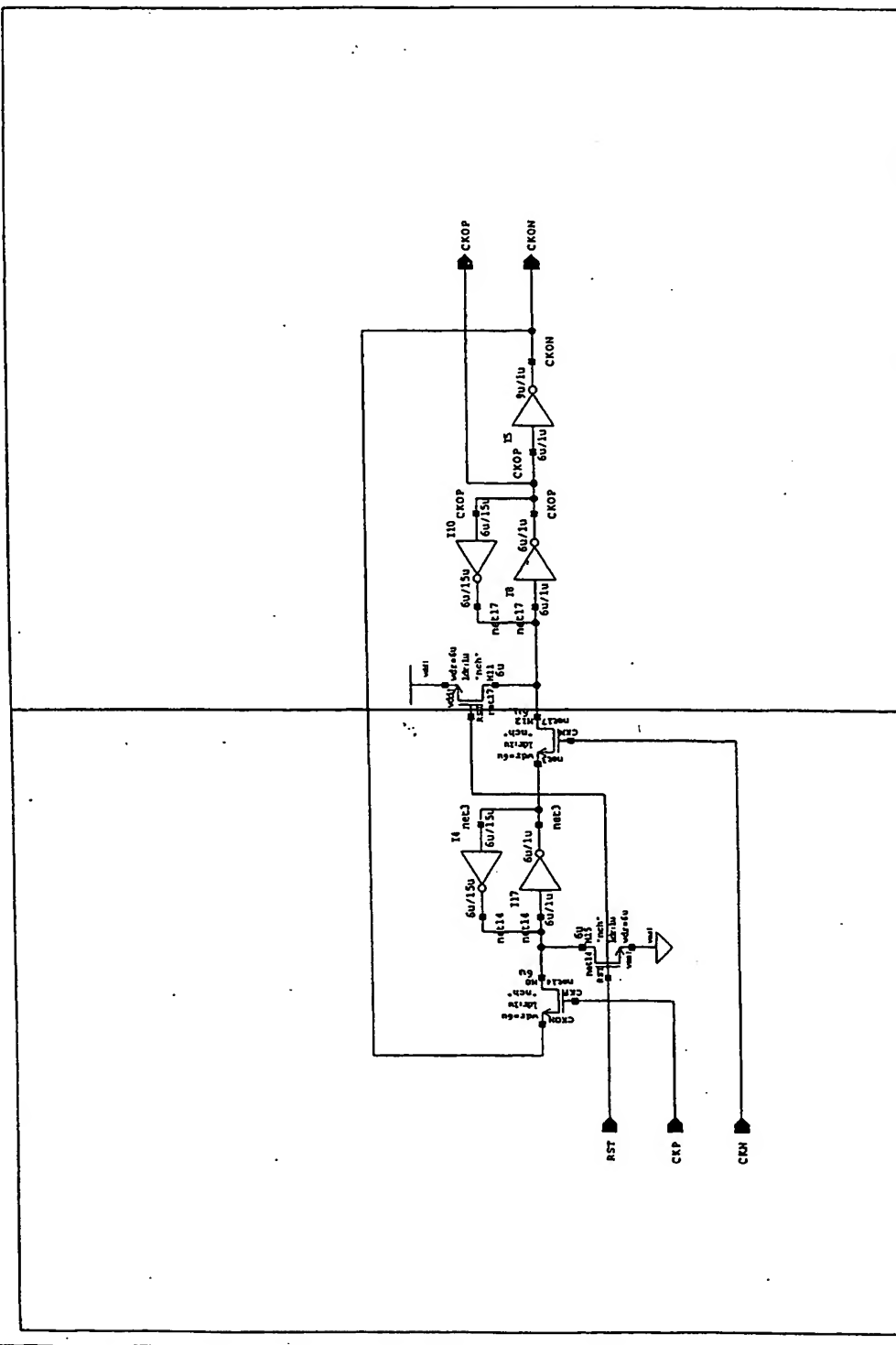
FIG 8.0401AA-EE

MICRON
INTEGRATED CIRCUITS
CONFIDENTIAL INFORMATION

- 81. option to support filter
- 82. option to support filter
- 83. option to support filter
- 84. option to support filter
- 85. option to support filter
- 86. option to support filter
- 87. option to support filter
- 88. option to support filter
- 89. option to support filter
- 90. option to support filter
- 91. option to support filter
- 92. option to support filter
- 93. option to support filter
- 94. option to support filter
- 95. option to support filter
- 96. option to support filter
- 97. option to support filter
- 98. option to support filter
- 99. option to support filter
- 100. option to support filter

8.040101AA

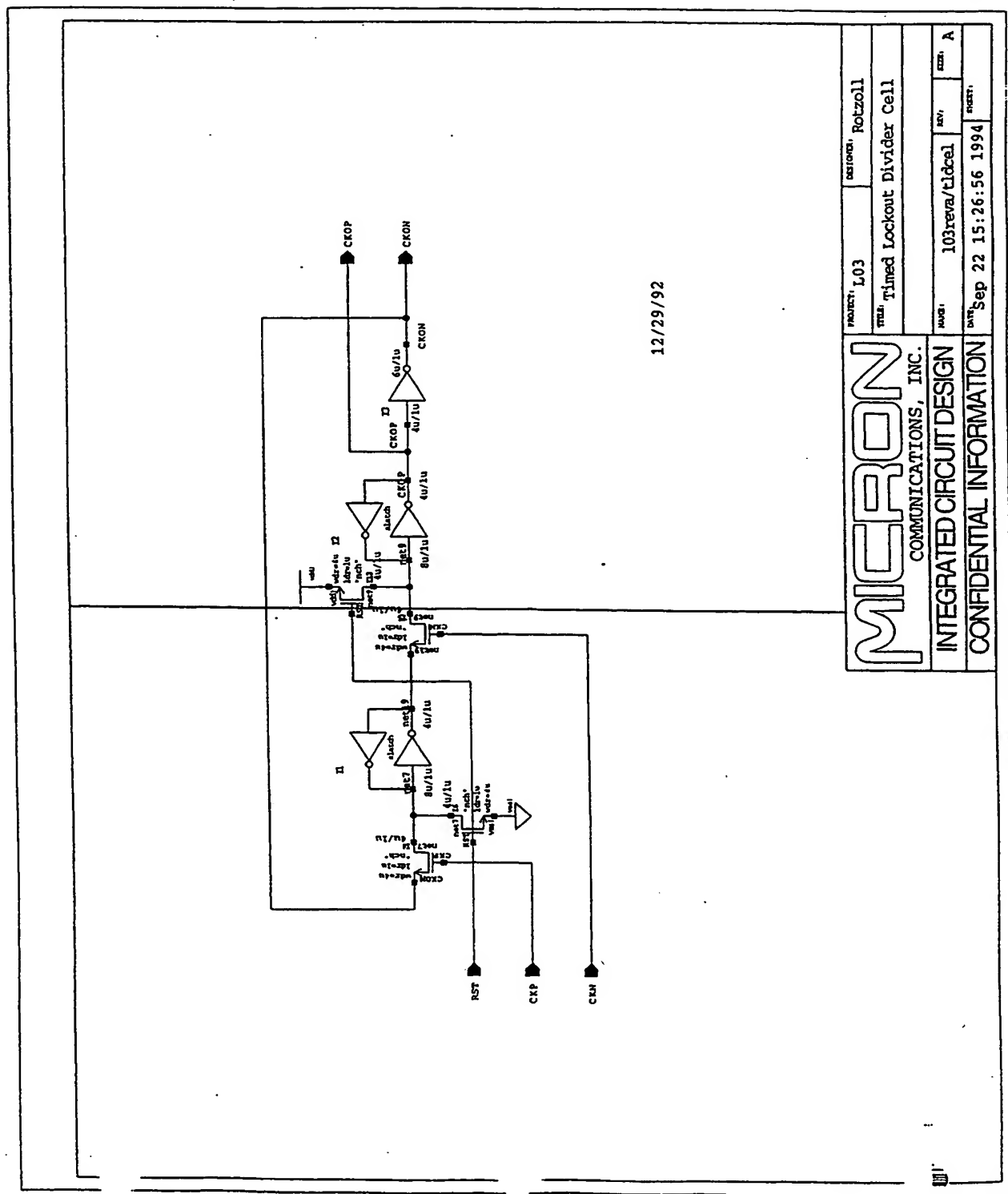
[illegible]



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Wakeup Abort Logic Counter Bit	
INTEGRATED CIRCUIT DESIGN		DATE: 103revA/wuabort_cbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 08:40:51 1994	DESIGN: A

8.0402AA	8.0402AB
----------	----------

EX 8.0402

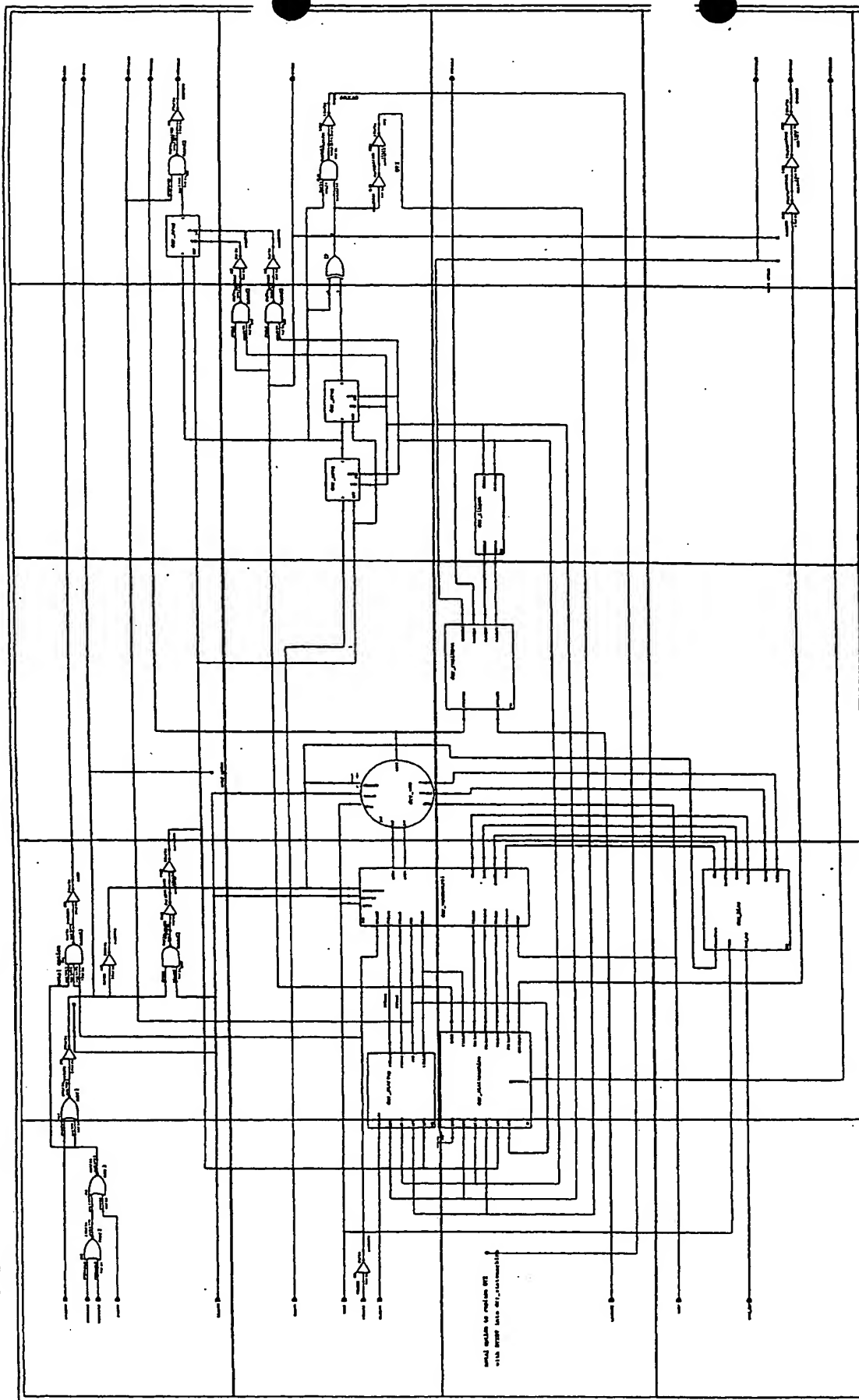


MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Timed Lockout Divider Cell	
INTEGRATED CIRCUIT DESIGN		DATE: 103reva/tldcel	REV: 0001 A
CONFIDENTIAL INFORMATION		DATE: Sep 22 15:26:56 1994	

FIG. 8.0402

8.05AA	8.05AB	8.05AC	8.05AD	8.05AE
8.05BA	8.05BB	8.05BC	8.05BD	8.05BE
8.05CA	8.05CB	8.05CC	8.05CD	8.05CE
8.05DA	8.05DB	8.05DC	8.05DD	8.05DE

EX-88.005



M1: Derived from logic
 M2: Derived from logic
 M3: Derived from logic
 M4: Derived from logic
 M5: Derived from logic
 M6: Derived from logic
 M7: Derived from logic
 M8: Derived from logic
 M9: Derived from logic
 M10: Derived from logic
 M11: Derived from logic
 M12: Derived from logic
 M13: Derived from logic
 M14: Derived from logic
 M15: Derived from logic
 M16: Derived from logic
 M17: Derived from logic
 M18: Derived from logic
 M19: Derived from logic
 M20: Derived from logic
 M21: Derived from logic
 M22: Derived from logic
 M23: Derived from logic
 M24: Derived from logic
 M25: Derived from logic
 M26: Derived from logic
 M27: Derived from logic
 M28: Derived from logic
 M29: Derived from logic
 M30: Derived from logic
 M31: Derived from logic
 M32: Derived from logic
 M33: Derived from logic
 M34: Derived from logic
 M35: Derived from logic
 M36: Derived from logic
 M37: Derived from logic
 M38: Derived from logic
 M39: Derived from logic
 M40: Derived from logic
 M41: Derived from logic
 M42: Derived from logic
 M43: Derived from logic
 M44: Derived from logic
 M45: Derived from logic
 M46: Derived from logic
 M47: Derived from logic
 M48: Derived from logic
 M49: Derived from logic
 M50: Derived from logic
 M51: Derived from logic
 M52: Derived from logic
 M53: Derived from logic
 M54: Derived from logic
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 M56: Derived from logic
 M57: Derived from logic
 M58: Derived from logic
 M59: Derived from logic
 M60: Derived from logic
 M61: Derived from logic
 M62: Derived from logic
 M63: Derived from logic
 M64: Derived from logic
 M65: Derived from logic
 M66: Derived from logic
 M67: Derived from logic
 M68: Derived from logic
 M69: Derived from logic
 M70: Derived from logic
 M71: Derived from logic
 M72: Derived from logic
 M73: Derived from logic
 M74: Derived from logic
 M75: Derived from logic
 M76: Derived from logic
 M77: Derived from logic
 M78: Derived from logic
 M79: Derived from logic
 M80: Derived from logic
 M81: Derived from logic
 M82: Derived from logic
 M83: Derived from logic
 M84: Derived from logic
 M85: Derived from logic
 M86: Derived from logic
 M87: Derived from logic
 M88: Derived from logic
 M89: Derived from logic
 M90: Derived from logic
 M91: Derived from logic
 M92: Derived from logic
 M93: Derived from logic
 M94: Derived from logic
 M95: Derived from logic
 M96: Derived from logic
 M97: Derived from logic
 M98: Derived from logic
 M99: Derived from logic
 M100: Derived from logic

Fig. 8.05

MICRON
 CORPORATION
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION
 10000 E. 1st Ave. Suite 100
 Denver, CO 80231
 (303) 751-1000
 FAX (303) 751-1001
 Telex 154444 MICRON
 Cable MICRON

8.0501AA	8.0501AB	8.0501AC	8.0501AD	8.0501AE
8.0501BA	8.0501BB	8.0501BC	8.0501BD	8.0501BE

BB.0500.11

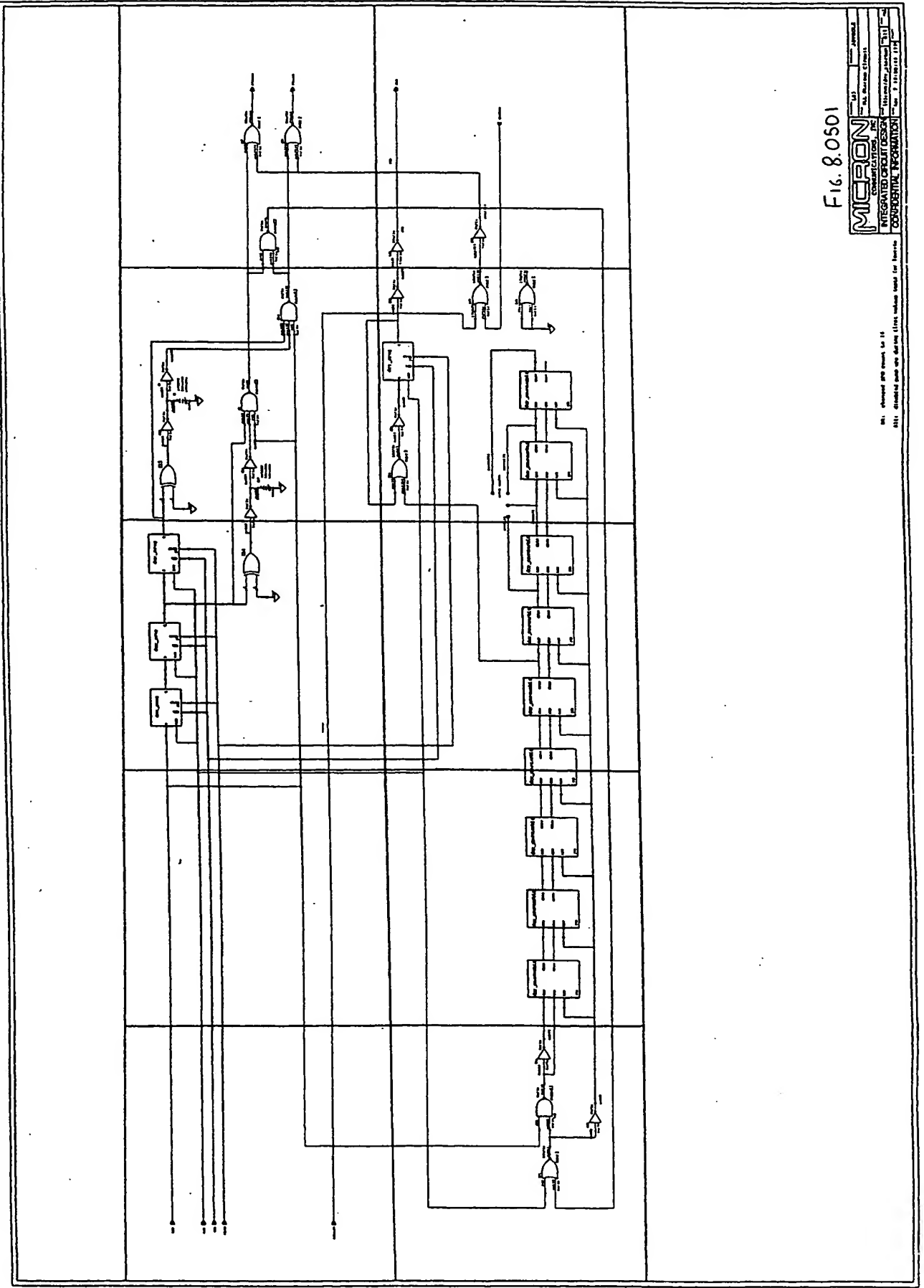


FIG. 8.0501

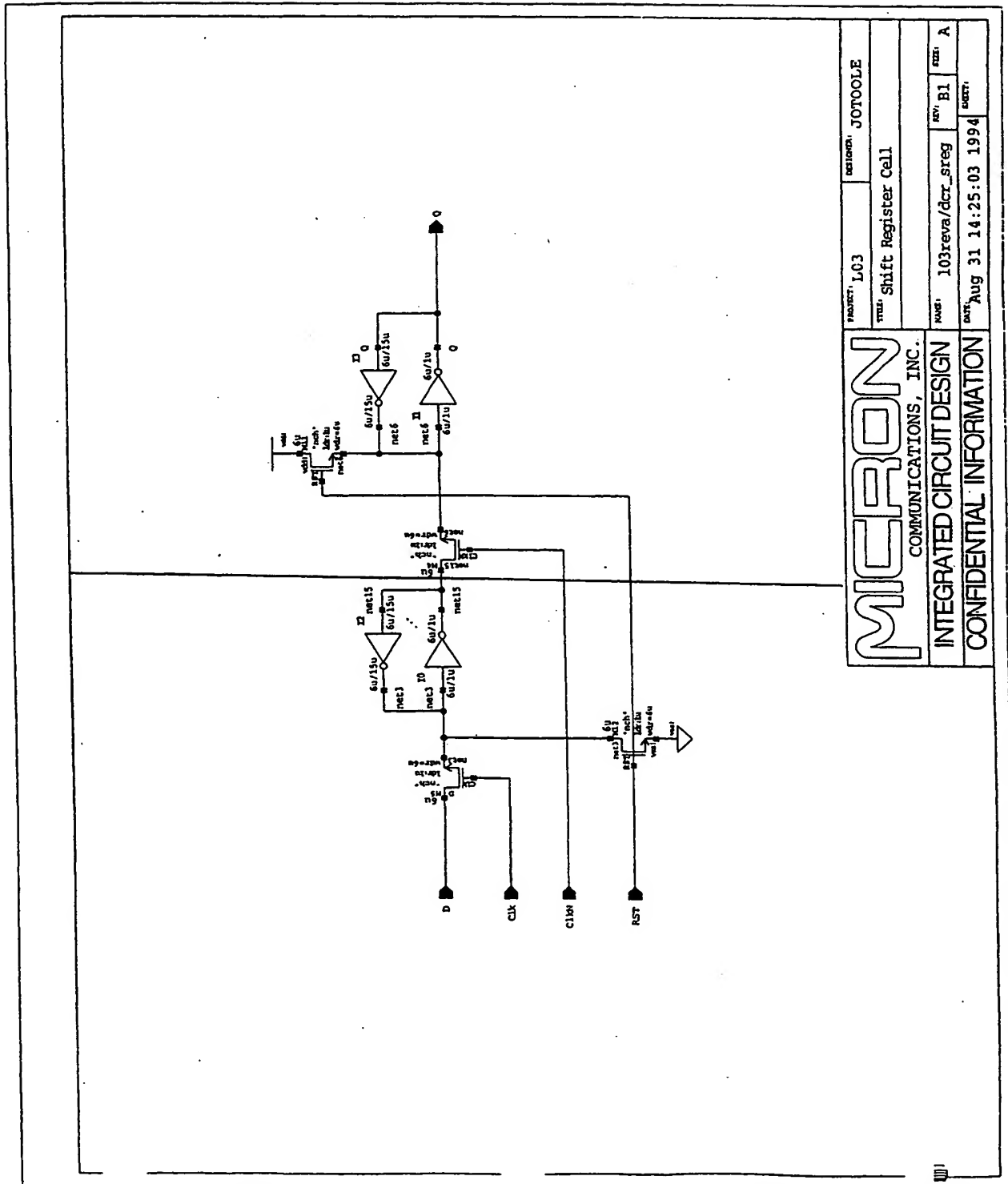
MICRON	
INTEGRATED CIRCUIT DESIGN	CONFIDENTIAL INFORMATION
Part Number: 8.0501	Revision: 1.0
Drawn: J. J. J.	Checked: J. J. J.
Date: 11/10/61	By: J. J. J.

NOT: Observed and tested in 10
 001: Observed and tested in 10

8.050101AB

8.050101AA

IL 11 05 00 11 11

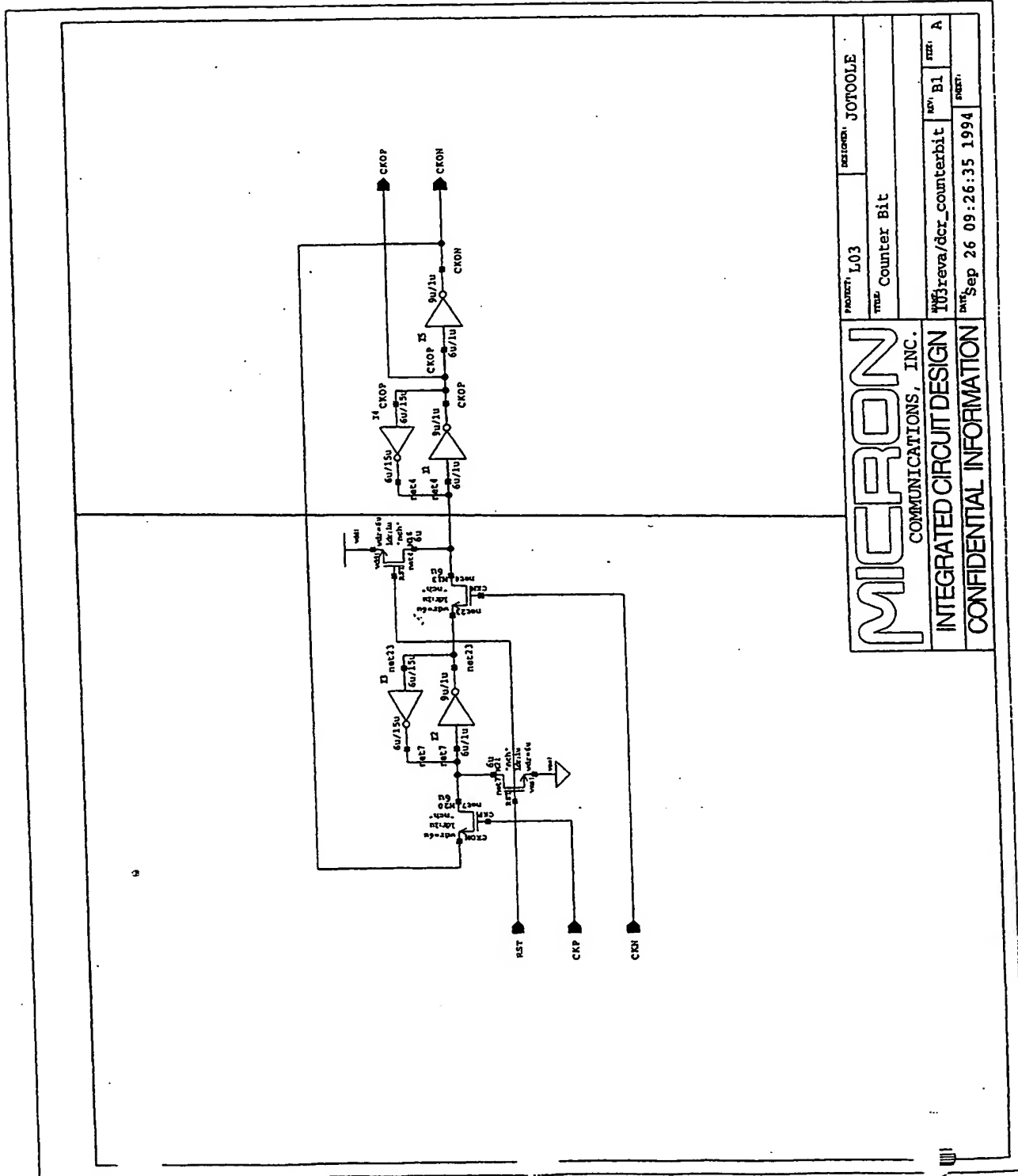


MICRON		PRODUCT: LC03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		NAME: Shift Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/dcr_sreg	REV: B1
CONFIDENTIAL INFORMATION		DATE: Aug 31 14:25:03 1994	REV: A

FIG. 8.050101

8.050102AB	8.050102AA
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EX-88-050102



MICRON		PRODUCT: L03	DESIGNED: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Counter Bit	
INTEGRATED CIRCUIT DESIGN		TOP: reva/dcr_counterbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:26:35 1994	SIZE: A

FIG. 8.050102

8.0502AA	8.0502AB	8.0502AC	8.0502AD
8.0502BA	8.0502BB	8.0502BC	8.0502BD
8.0502CA	8.0502CB	8.0502CC	8.0502CD

20508 688

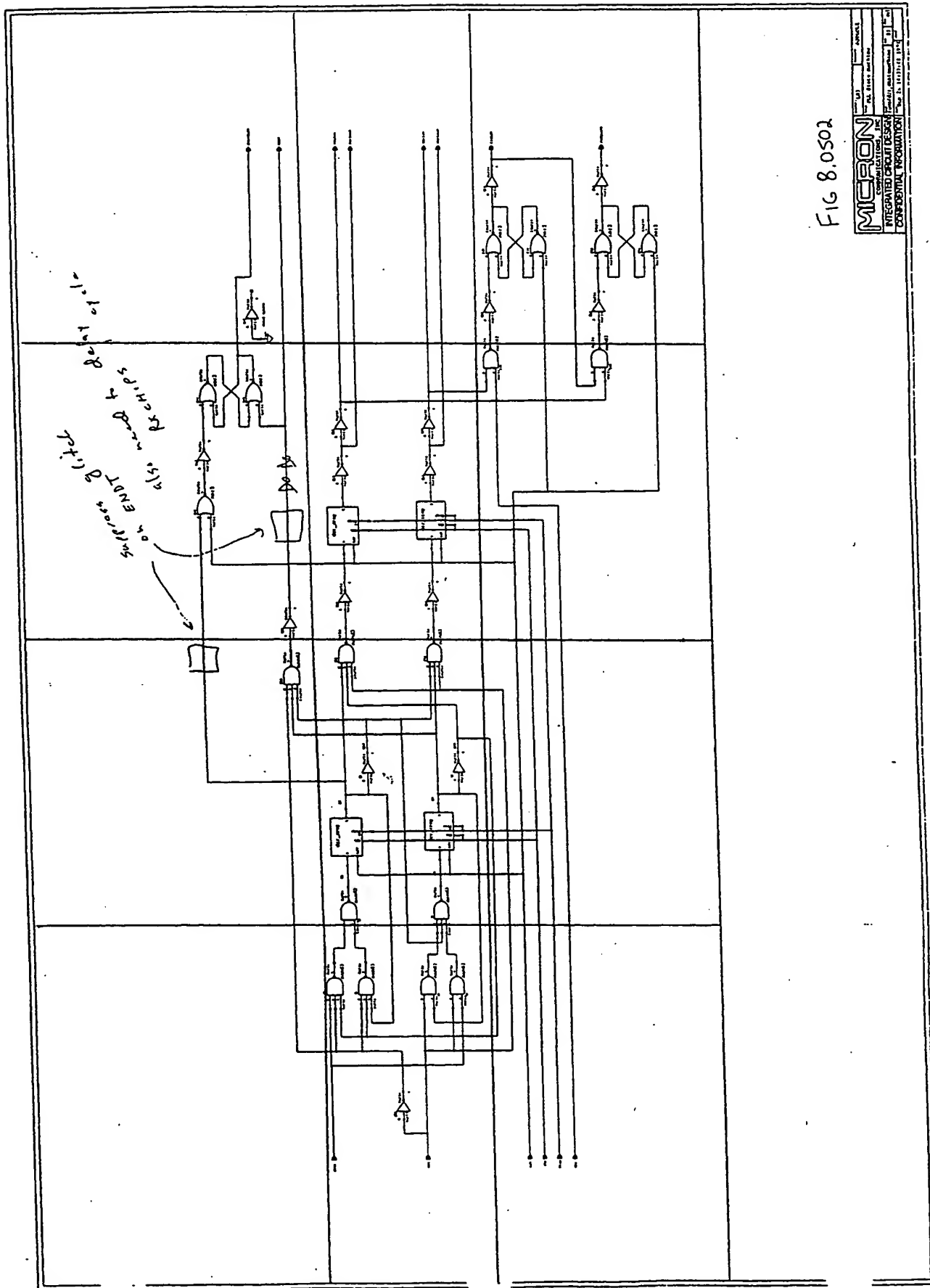
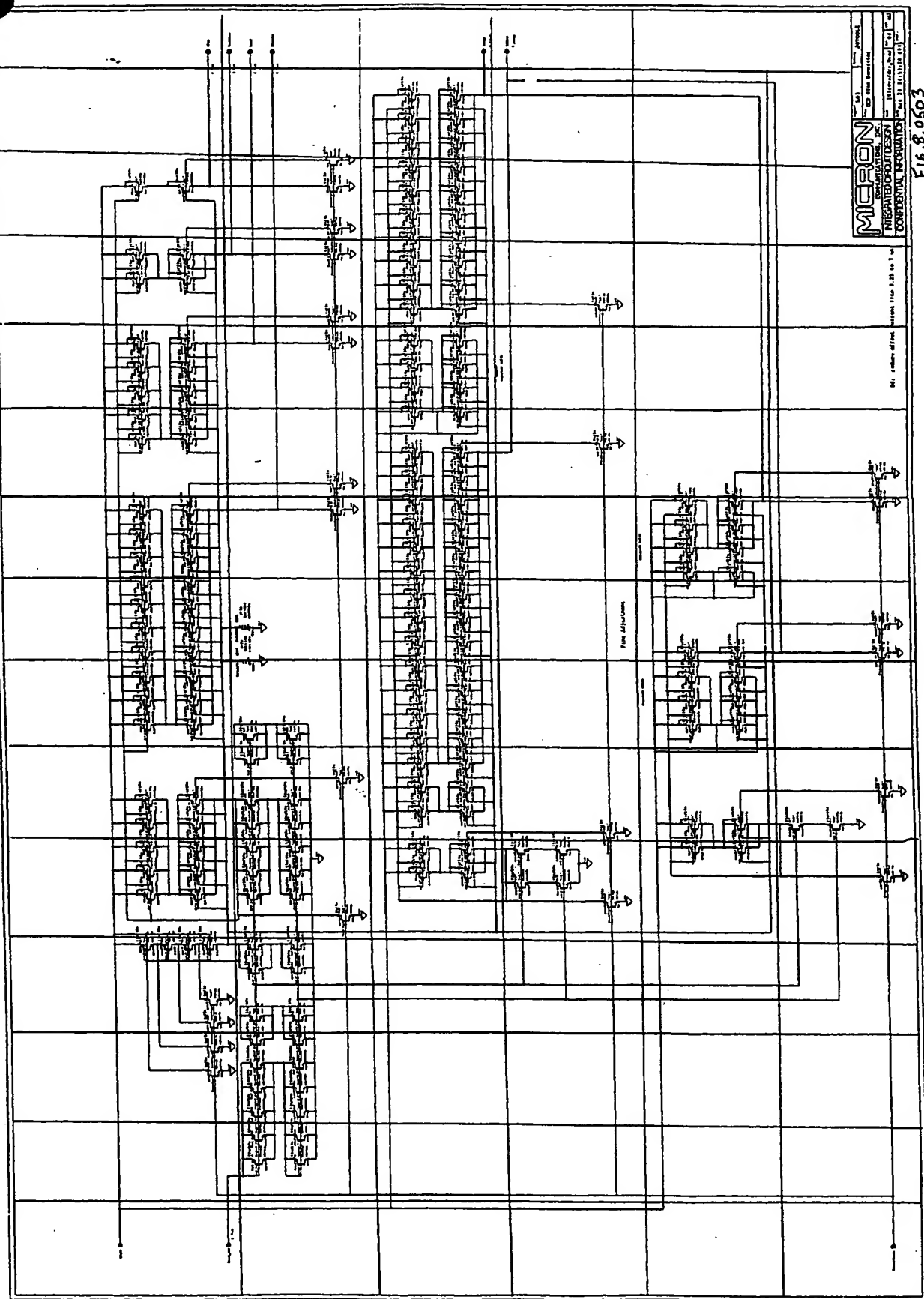


FIG 8.0502

8.05034A	8.05034B	8.05034C	8.05034D	8.05034E	8.05034F	8.05034G	8.05034H	8.05034I	8.05034J	8.05034K	8.05034L	8.05034M	8.05034N	8.05034O
8.05038A	8.05038B	8.05038C	8.05038D	8.05038E	8.05038F	8.05038G	8.05038H	8.05038I	8.05038J	8.05038K	8.05038L	8.05038M	8.05038N	8.05038O
8.0503CA	8.0503CB	8.0503CC	8.0503CD	8.0503CE	8.0503CF	8.0503CG	8.0503CH	8.0503CI	8.0503CJ	8.0503CK	8.0503CL	8.0503CM	8.0503CN	8.0503CO
8.0503DA	8.0503DB	8.0503DC	8.0503DD	8.0503DE	8.0503DF	8.0503DG	8.0503DH	8.0503DI	8.0503DJ	8.0503DK	8.0503DL	8.0503DM	8.0503DN	8.0503DO
8.0503EA	8.0503EB	8.0503EC	8.0503ED	8.0503EE	8.0503EF	8.0503EG	8.0503EH	8.0503EI	8.0503EJ	8.0503EK	8.0503EL	8.0503EM	8.0503EN	
8.0503FA	8.0503FB	8.0503FC	8.0503FD	8.0503FE	8.0503FF	8.0503FG	8.0503FH	8.0503FI	8.0503FJ	8.0503FK	8.0503FL	8.0503FM	8.0503FN	

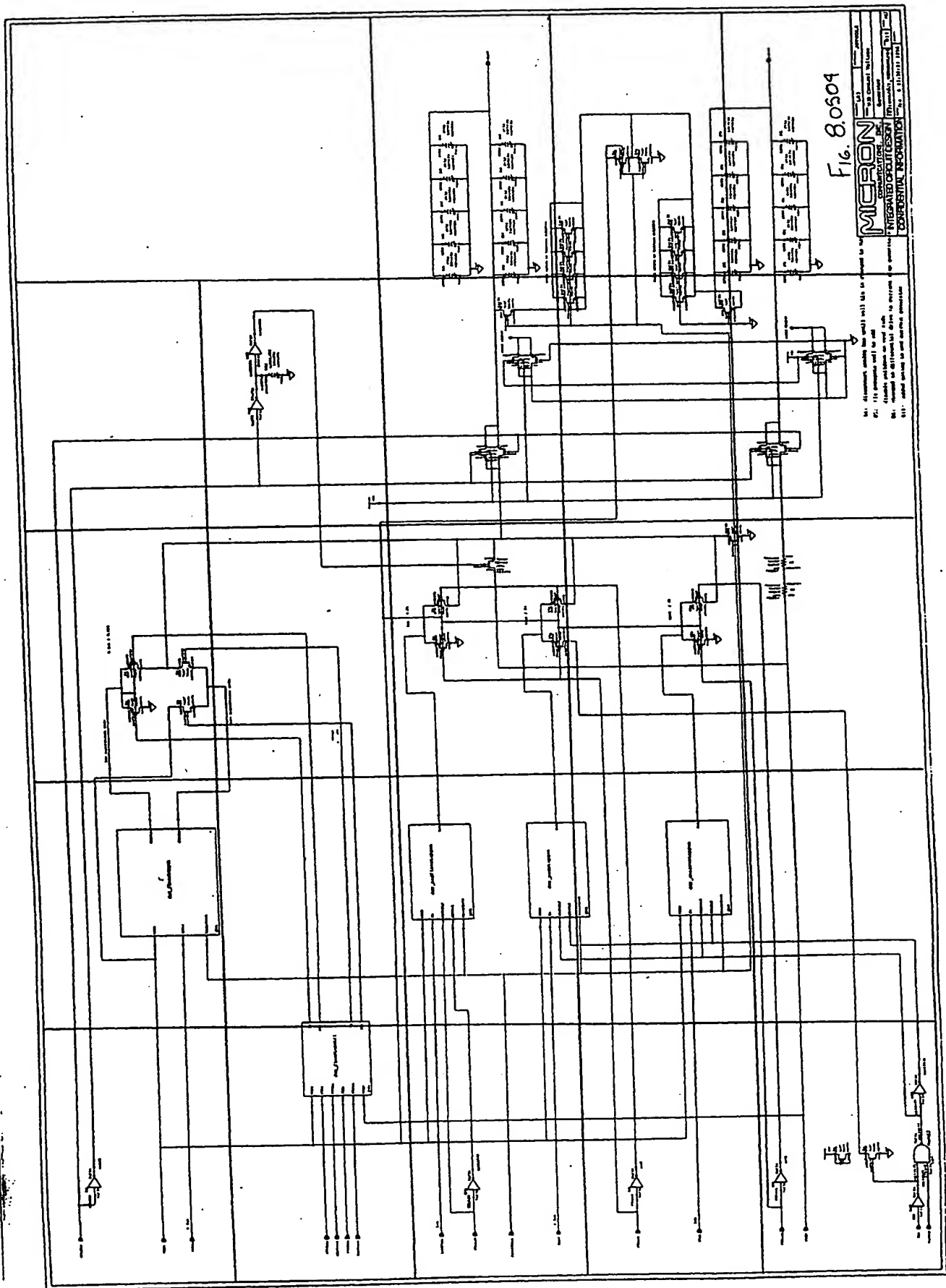

 DEPARTMENT OF DEFENSE

Fig. 8.0603



8.0504AA	8.0504AB	8.0504AC	8.0504AD	
8.0504BA	8.0504BB	8.0504BC	8.0504BD	
8.0506CA	8.0504CB	8.0504CC	8.0504CD	8.0504CE
8.0504DA	8.0504DB	8.0504DC	8.0504DD	8.0504DE
8.0504EA	8.0504EB	8.0504EC	8.0504ED	8.0504EE

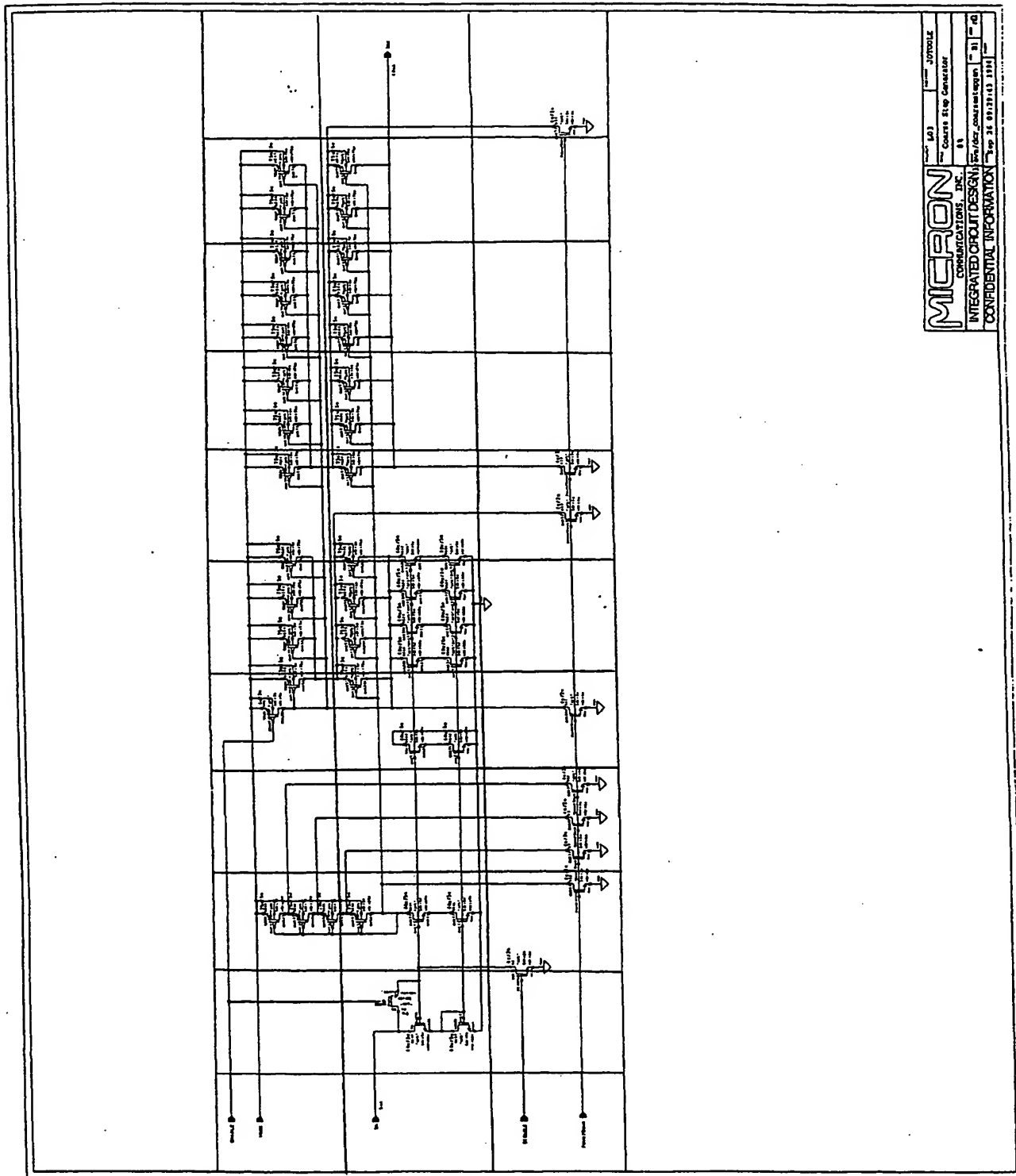
EE 8.0504



8.050401AA	8.050401AB	8.050401AC	8.050401AD	8.050401AE	8.050401AF	8.050401AG	8.050401AH	8.050401AI	8.050401AJ		
8.050401BA	8.050401BB	8.050401BC	8.050401BD	8.050401BE	8.050401BF	8.050401BG	8.050401BH	8.050401BI	8.050401BJ	8.050401BK	
8.050401CA	8.050401CB	8.050401CC	8.050401CD	8.050401CE	8.050401CF	8.050401CG	8.050401CH	8.050401CI	8.050401CJ	8.050401CK	

107505008 6022

Fig 8.050401

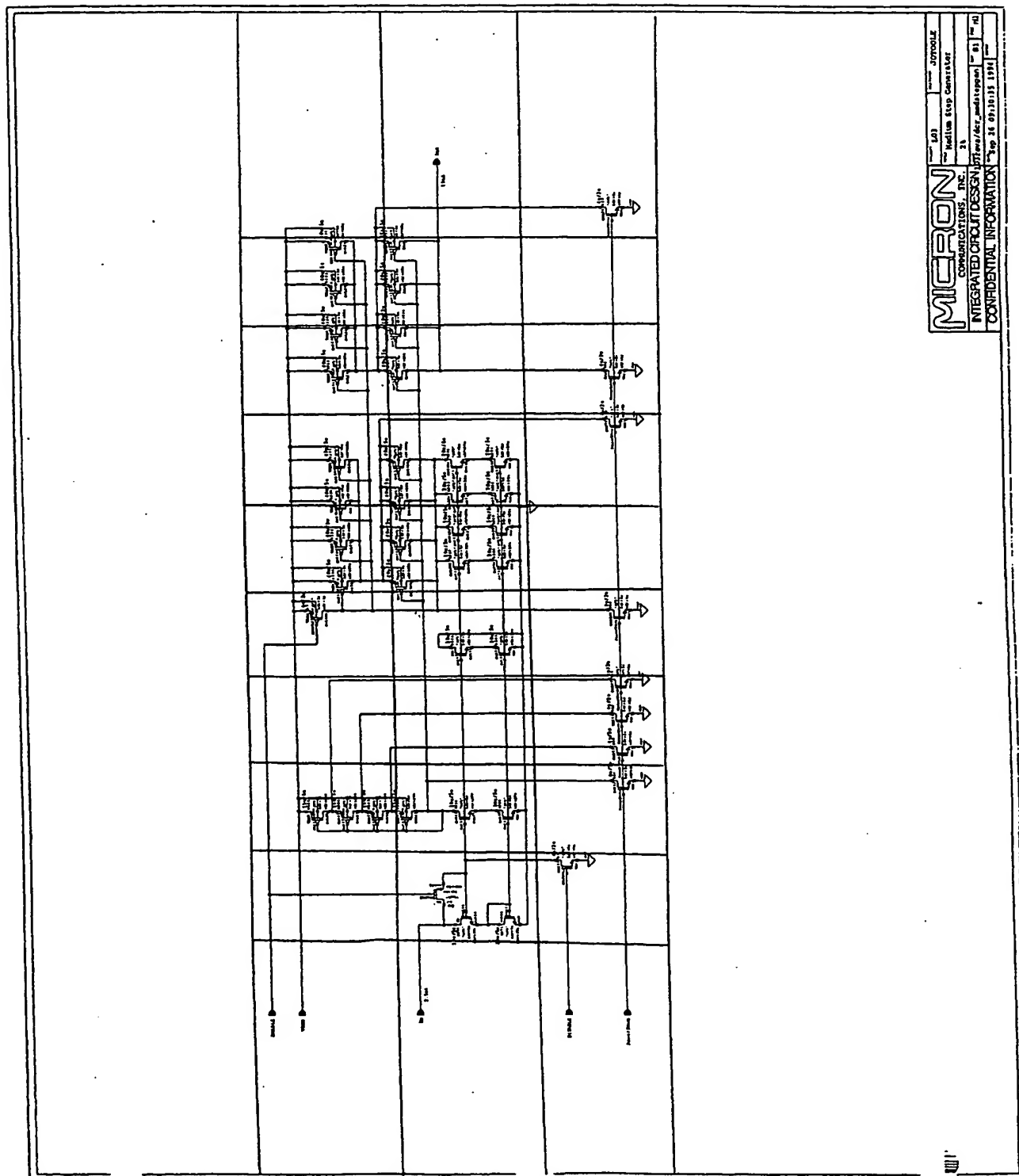


MICRON
 CORPORATION, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION
 L01 JUTV01E
 Counter Step Generator
 81
 Rev/Ver: 0000000000
 Top 21 01/27/13 1988

8.050402AA	8.050402AB	8.050402AC	8.050402AD	8.050402AE	8.050402AF	8.050402AG	8.050402AH	8.050402AI	8.050402AJ
8.050402BA	8.050402BB	8.050402BC	8.050402BD	8.050402BE	8.050402BF	8.050402BG	8.050402BH	8.050402BI	8.050402BJ
8.050402CA	8.050402CB	8.050402CC	8.050402CD	8.050402CE	8.050402CF	8.050402CG	8.050402CH	8.050402CI	8.050402CJ

8.050402

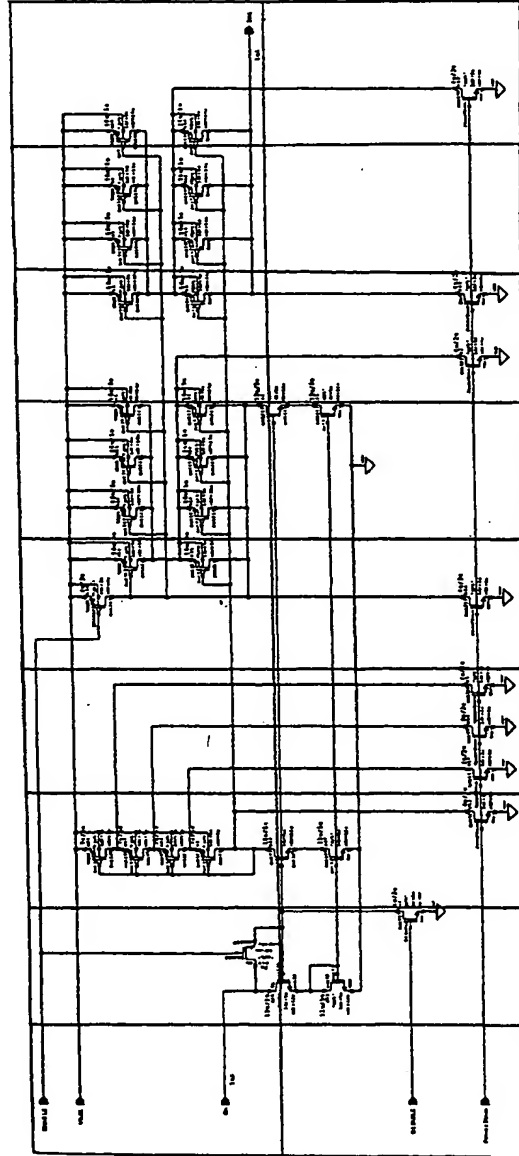
FIG. 8.050402



8.050403AA	8.050403AB	8.050403AC	8.050403AD	8.050403AE	8.050403AF	8.050403AG	8.050403AH	8.050403AI
8.050403BA	8.050403BB	8.050403BC	8.050403BD	8.050403BE	8.050403BF	8.050403BG	8.050403BH	8.050403BI

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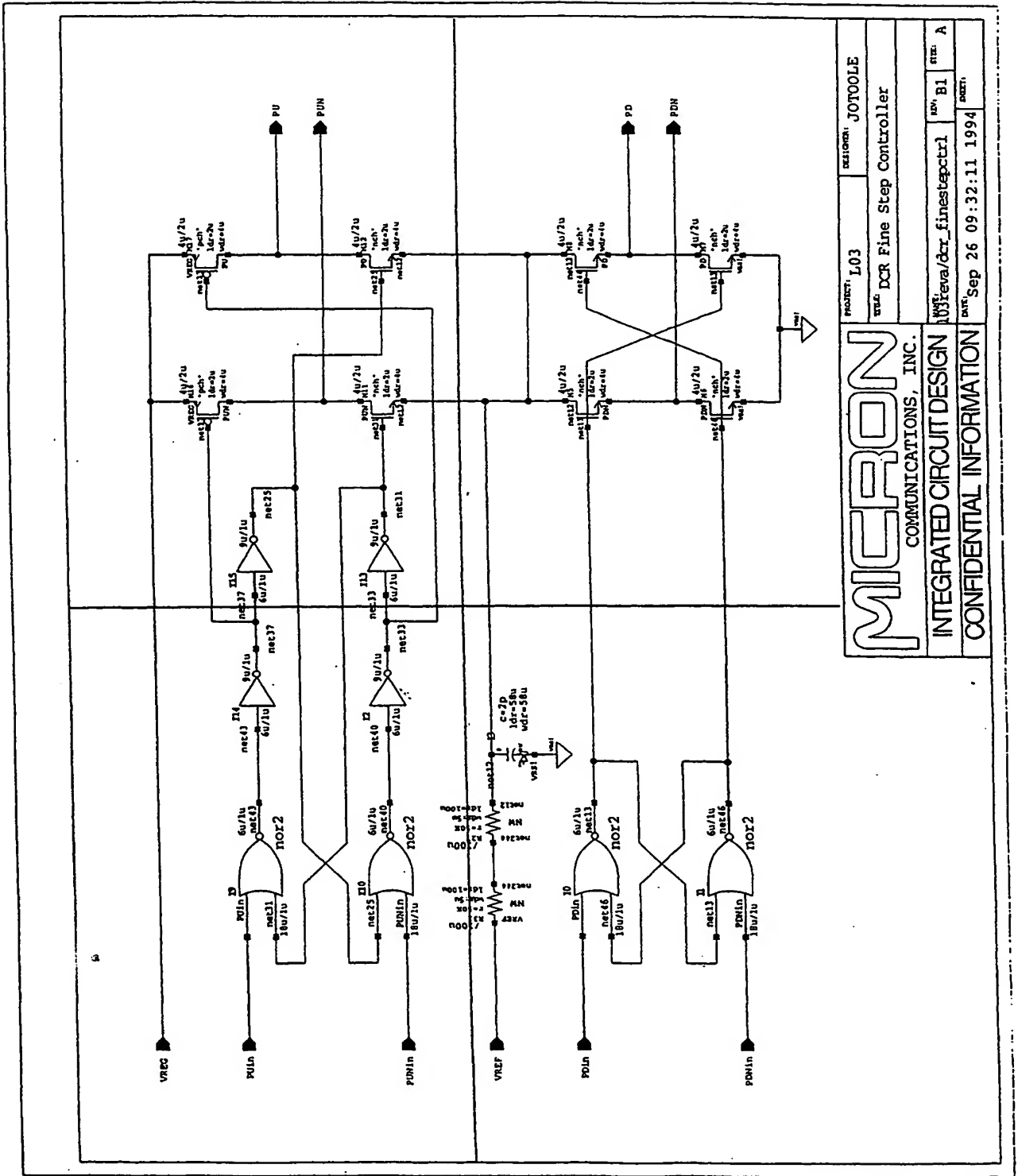
Fig. 8.050403



MICRON		U-101	JOB NO.
CONSULTING, INC.		Medium Plane Strip Controller	
INTEGRATED CIRCUIT DESIGN		8-73	
CONFIDENTIAL INFORMATION		Rev. 26 09/11/18 1974	81

8.050404AA	8.050404AB
8.050404BA	8.050404BB

EEB.050404A



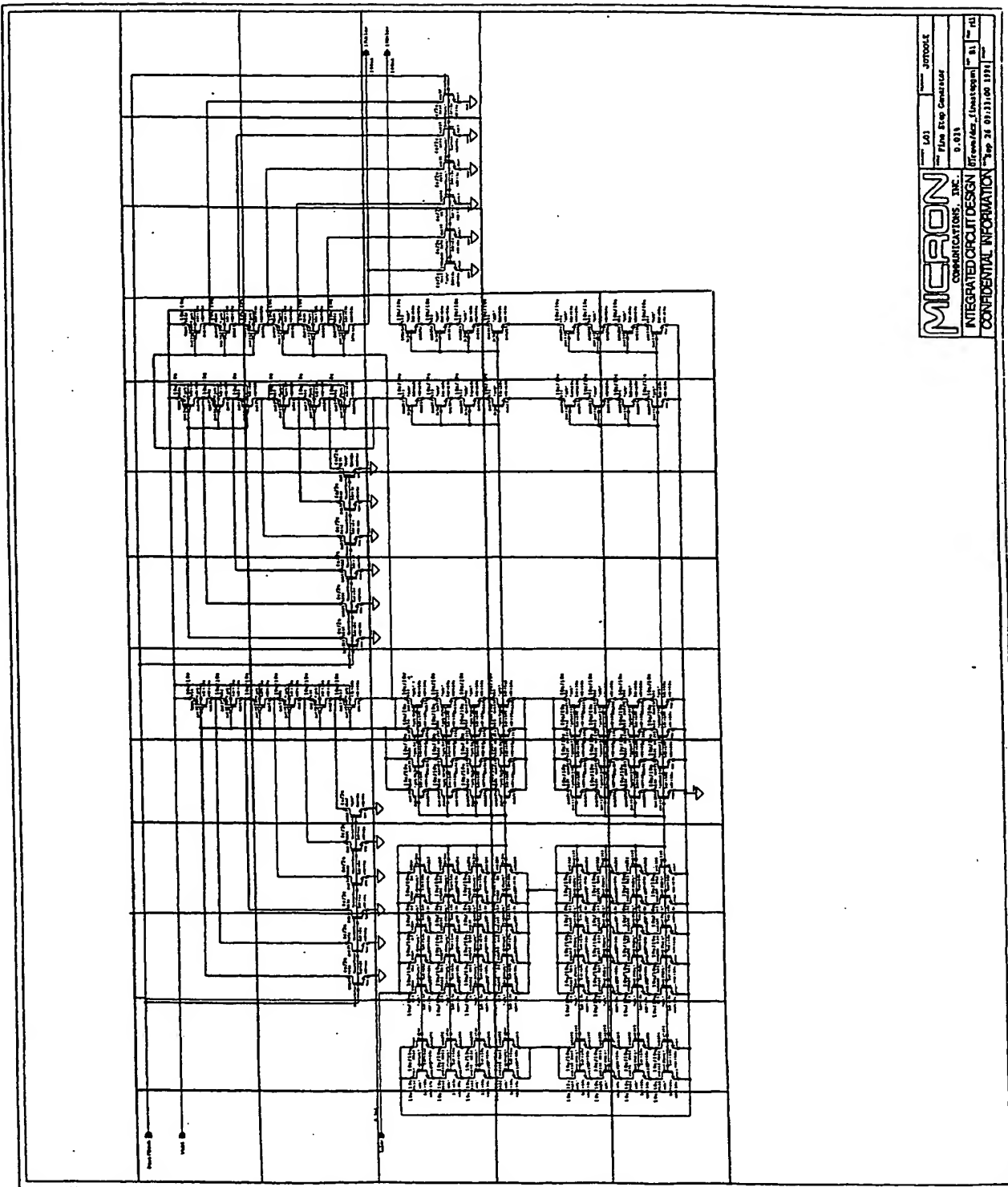
PROJECT: L03		REVISION: J0700LE	
TITLE: DCR Fine Step Controller		REV: B1	
DESIGNER: J037eva/dcr_finestepctrl		DATE: Sep 26 09:32:11 1994	
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION	
MICRON		COMMUNICATIONS, INC.	
SIZE: A		SHEET: 1	

Fig. 8.050909

8.0504054A	8.0504054B	8.0504054C	8.0504054D	8.0504054E	8.0504054F	8.0504054G	8.0504054H	8.0504054I	8.0504054J	8.0504054K	8.0504054L	8.0504054M
8.0504058A	8.0504058B	8.0504058C	8.0504058D	8.0504058E	8.0504058F	8.0504058G	8.0504058H	8.0504058I	8.0504058J	8.0504058K	8.0504058L	8.0504058M
8.0504052A	8.0504052B	8.0504052C	8.0504052D	8.0504052E	8.0504052F	8.0504052G	8.0504052H	8.0504052I	8.0504052J	8.0504052K	8.0504052L	8.0504052M
8.0504050A	8.0504050B	8.0504050C	8.0504050D	8.0504050E	8.0504050F	8.0504050G	8.0504050H	8.0504050I	8.0504050J			
8.0504052A	8.0504052B	8.0504052C	8.0504052D	8.0504052E	8.0504052F	8.0504052G	8.0504052H	8.0504052I	8.0504052J			

IF II III IV V VI VII VIII IX X XI XII

FIG 8.050405



MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
LAJ	JT0004
Title: Step Controller	
0.011	
Drawn: A.S. (Unit: 1000)	Rev: 1
Date: 24 01/31/00 11:01	

8.05054A	8.05054B	8.05054C	8.05054D	8.05054E	8.05054F	8.05054G	8.05054H	8.05054I	8.05054J	8.05054K	8.05054L	8.05054M	8.05054N
8.0505BA	8.0505BB	8.0505BC	8.0505BD	8.0505BE	8.0505BF	8.0505BG	8.0505BH	8.0505BI	8.0505BJ	8.0505BK	8.0505BL	8.0505BM	8.0505BN
8.0505CA	8.0505CB	8.0505CC	8.0505CD	8.0505CE	8.0505CF	8.0505CG	8.0505CH	8.0505CI	8.0505CJ	8.0505CK	8.0505CL	8.0505CM	8.0505CN
8.0505DA	8.0505DB	8.0505DC	8.0505DD	8.0505DE	8.0505DF	8.0505DG	8.0505DH	8.0505DI	8.0505DJ	8.0505DK	8.0505DL	8.0505DM	8.0505DN
8.0505EA	8.0505EB	8.0505EC	8.0505ED	8.0505EE	8.0505EF								

JE II CS BB 0505

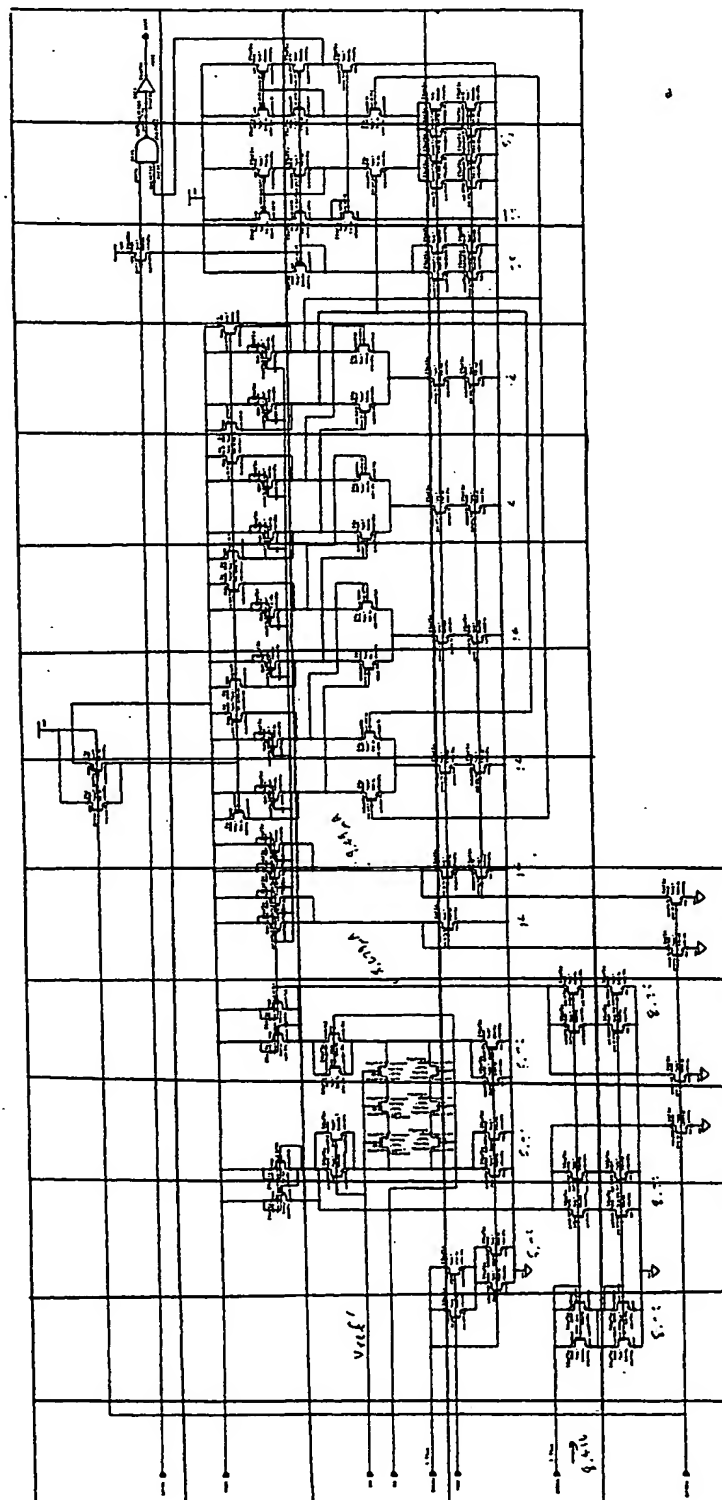


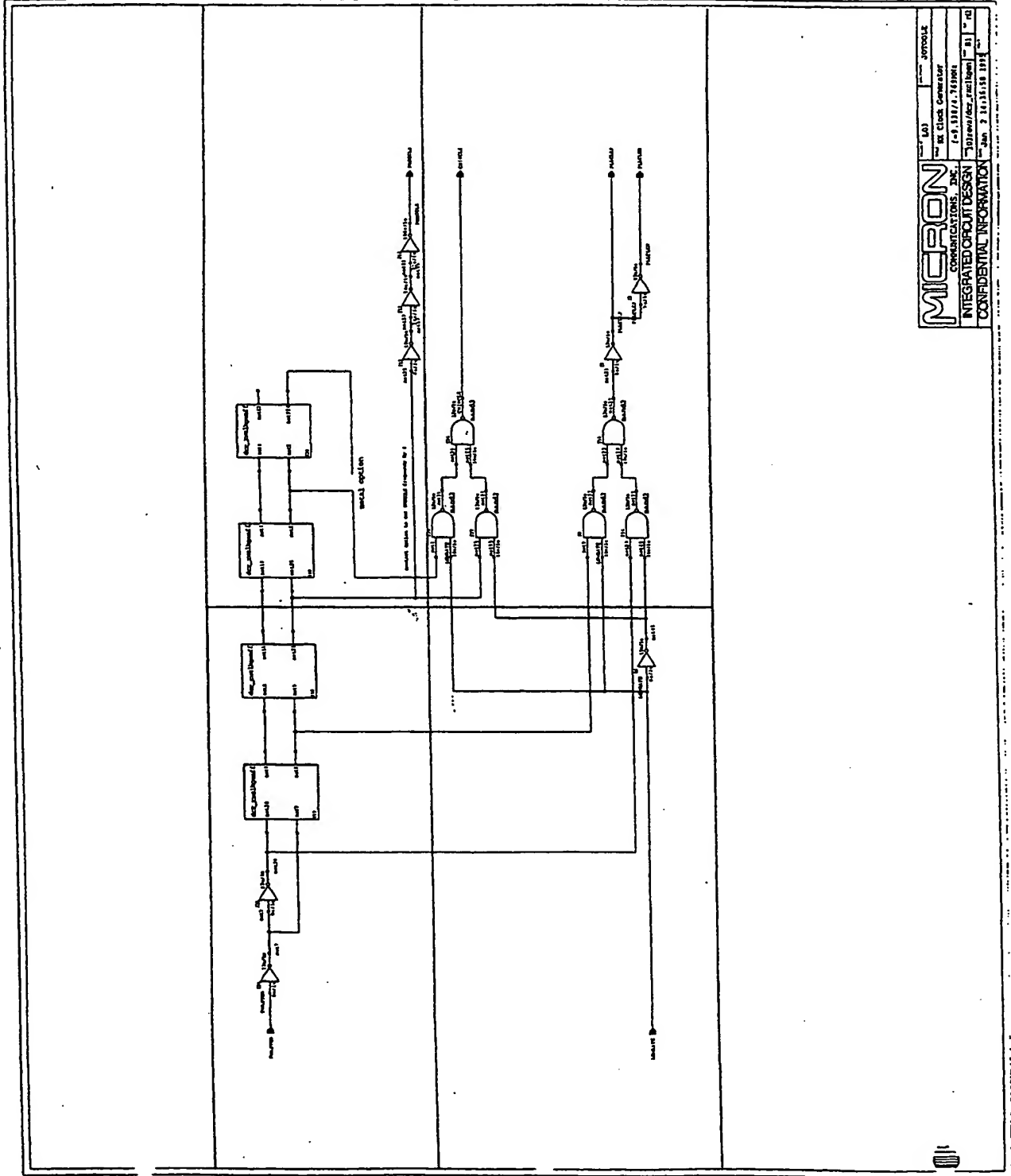
FIG. 8.0505

238,4.

8.0506AA	8.0506AB
8.0506BA	8.0506BB

90508 EPI

FIG. 8.0506



MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION
 Jan 2 11:15:18 1978

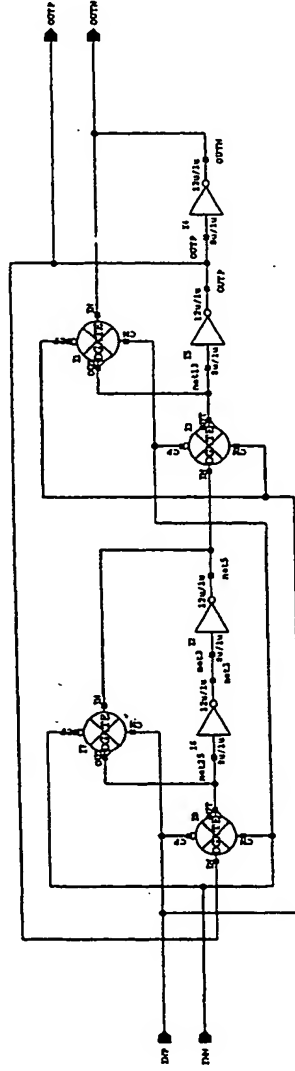


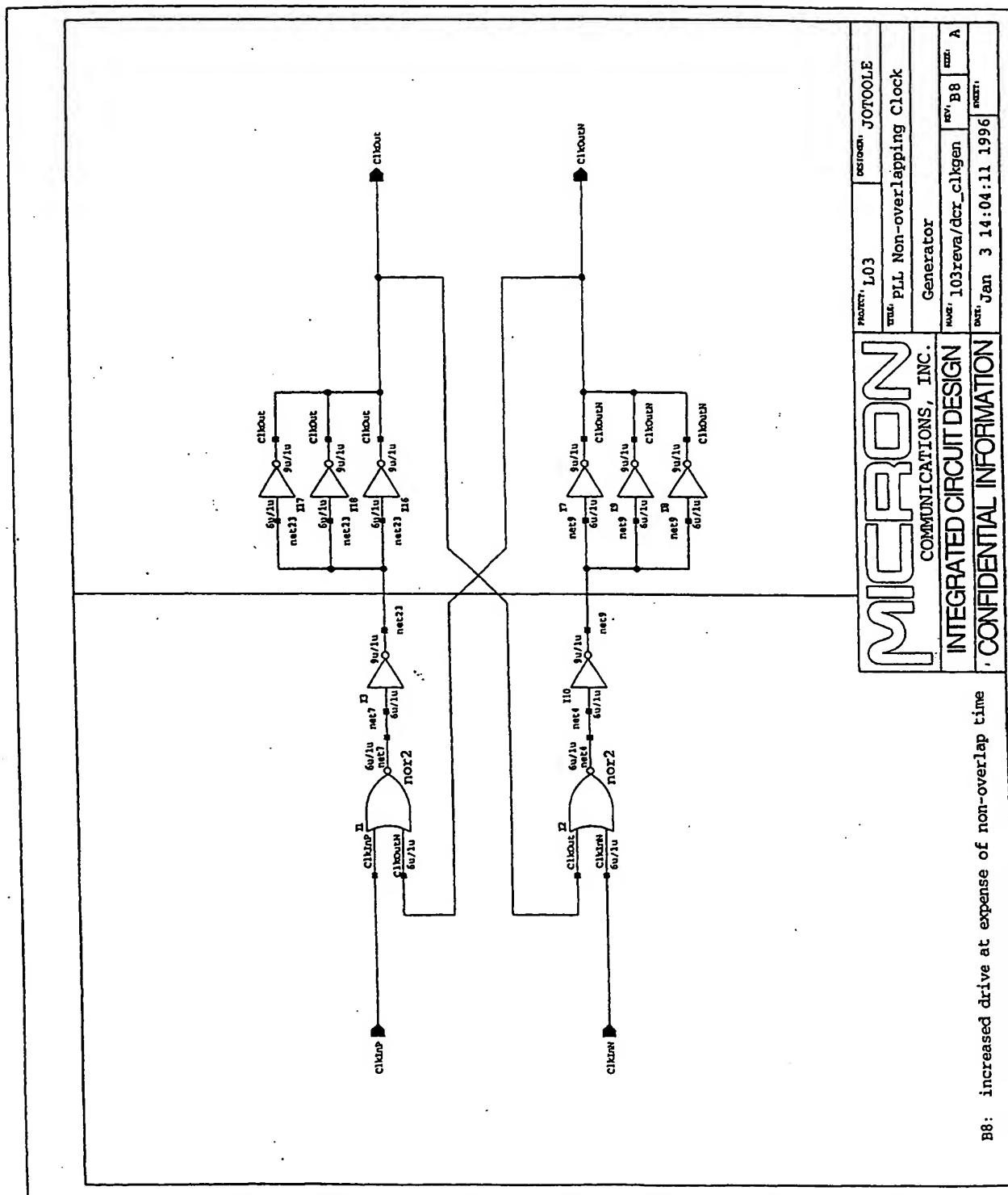
Fig. 8.050601

MICRON	COMMUNICATIONS, INC.		J0700LE	
	INTEGRATED CIRCUIT DESIGN		Rx Clock Generator	
CONFIDENTIAL INFORMATION		Flip-Flop	Rev. B1	Rev. 1
		Thyrea/dcr_pchlegentf	Rev. B1	Rev. 1
		Sep 26 09:36:05 1994		

8.0507AB

8.0507AA

EEB.0507



8.06AA	8.06AB	8.06AC	8.06AD
8.06BA	8.06BB	8.06BC	8.06BD
8.06CA	8.06CB	8.06CC	8.06CD
8.06DA	8.06DB	8.06DC	8.06DD
8.06EA	8.06EB	8.06EC	8.06ED

ILR 07 88.0016

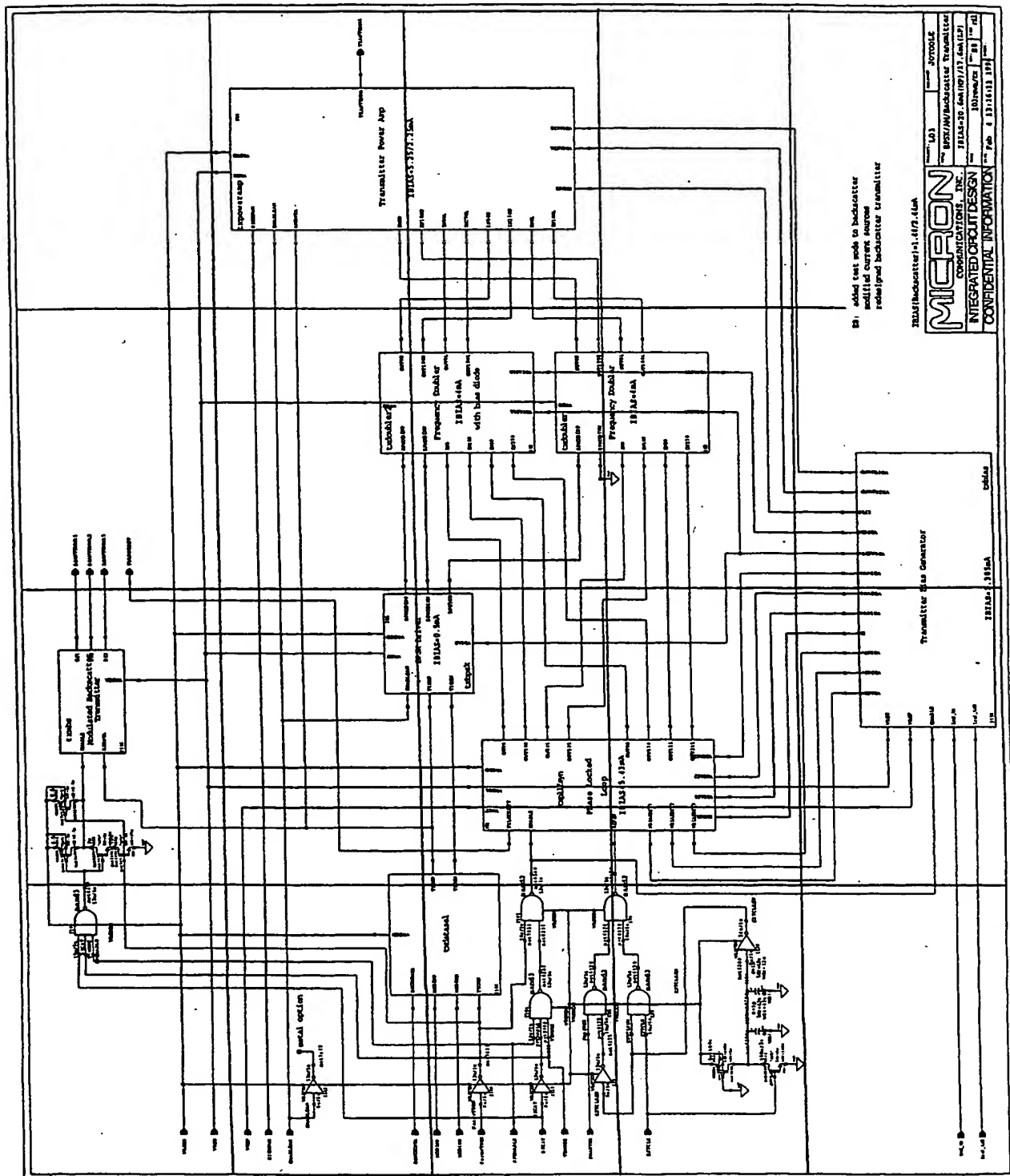


Fig. 8.06

8.0601AA	8.0601AB
8.0601BA	8.0601BB

EX 8.0601

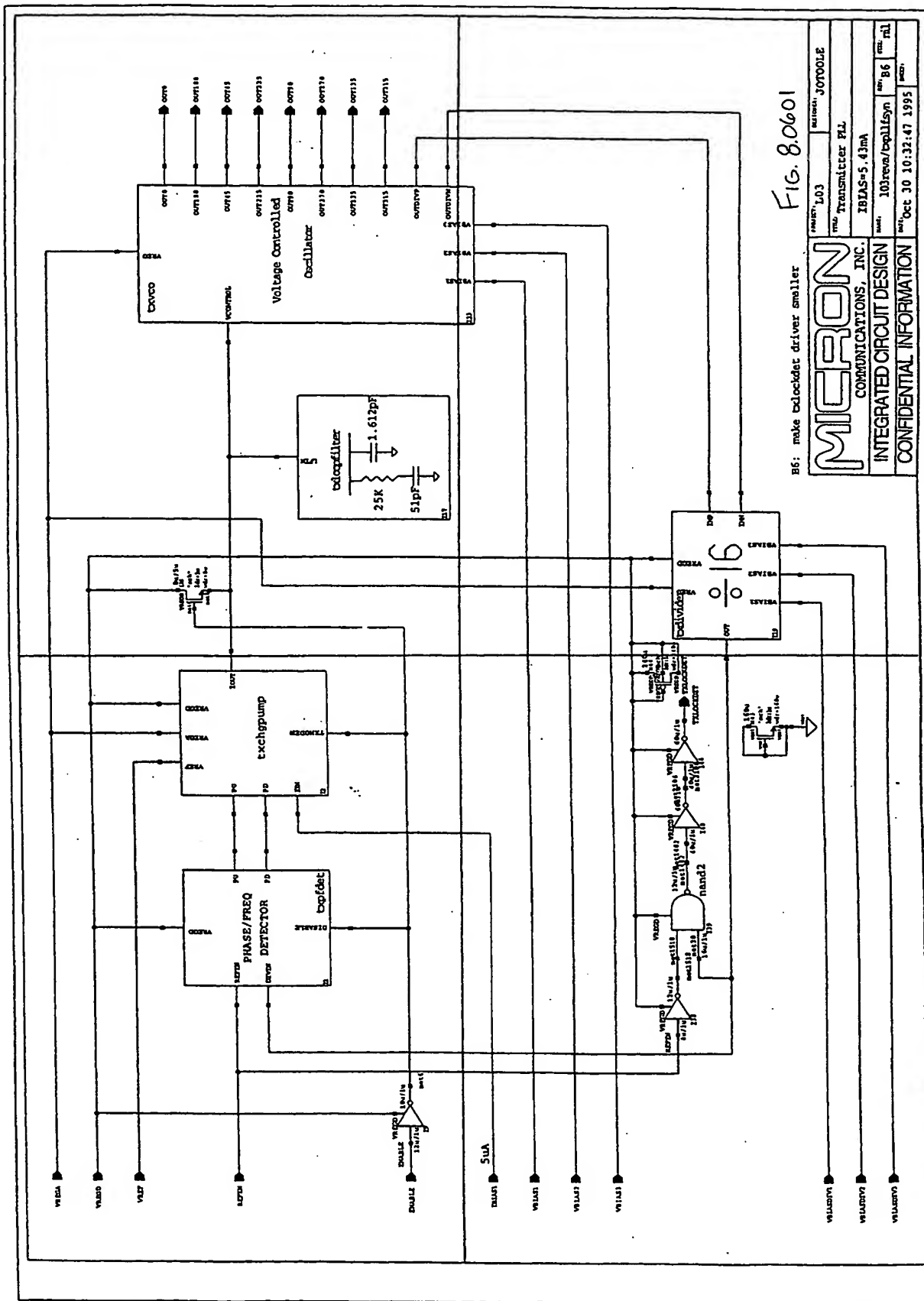


FIG. 8.0601

B5: make feedback driver smaller

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03	REVISION: J07000LE
TYPE: Transmitter PLL	
IBIAS=5.43mA	
100nsec/ballsyn	
Oct 30 10:32:47 1995	
B6	nil

8.060101AA	8.060101AB	8.060101AC
8.060101BA	8.060101BB	8.060101BC
8.060101CA	8.060101CB	8.060101CC

EX 8.060101

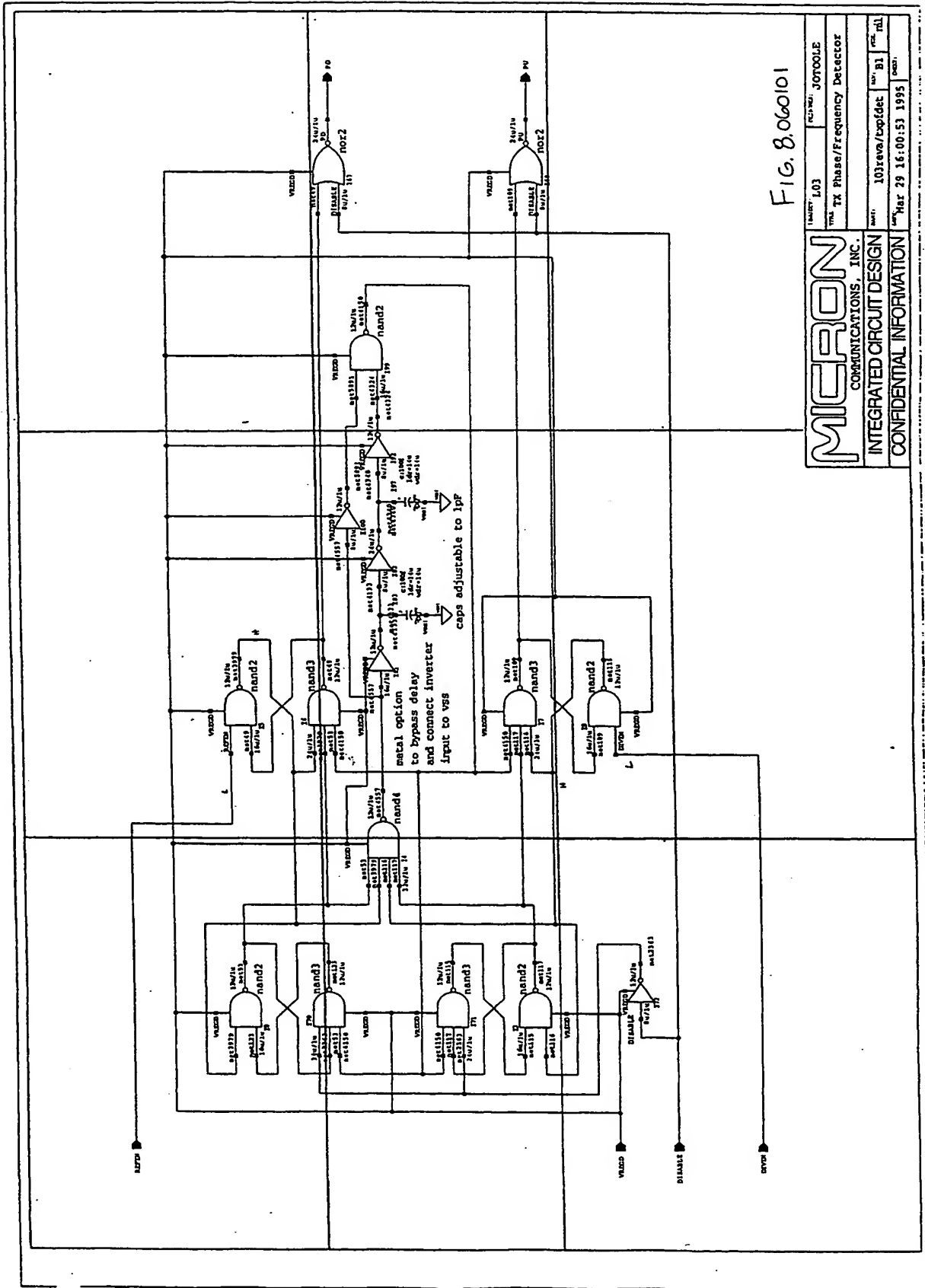
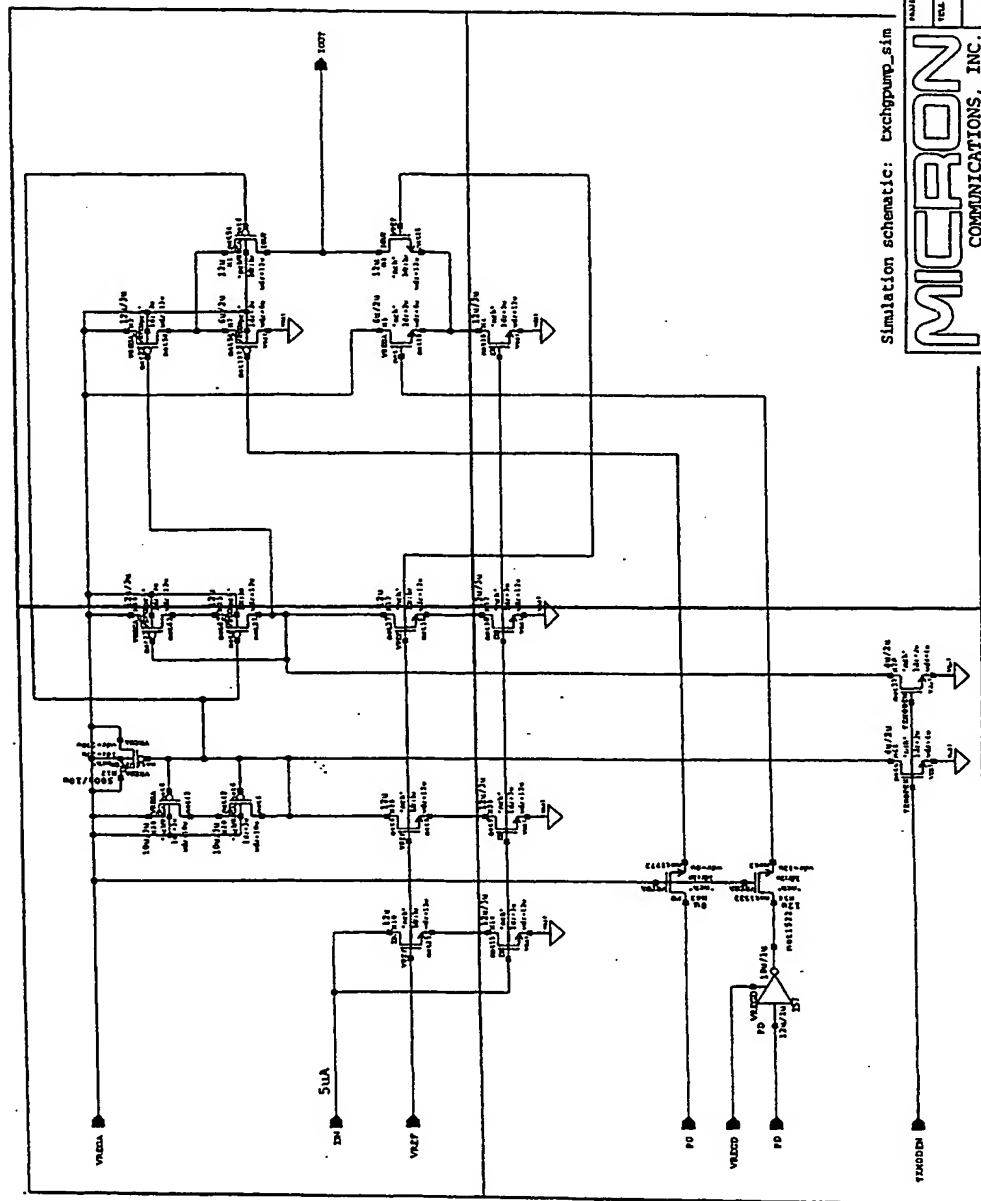


FIG. 8.060101

MICRON		REV. L03	REV. J07001E
COMMUNICATIONS, INC.		TX Phase/Frequency Detector	
INTEGRATED CIRCUIT DESIGN		103revn/topdet	Rev. B1
CONFIDENTIAL INFORMATION		Mar 29 16:00:53 1995	Doc.

8.060102AA	8.060102AB
8.060102BA	8.060102BB

И. П. Б. 100102



Simulation schematic: txchgump_sim

FIG. 8060102

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: 103	DESIGNER: JOTOOLE
FILE: TX PLL Charge Pump	
DATE: 103revA/exchgump	REV: B1
DATE: Feb 28 09:55:50 1995	REV: B1

8.060103AA	8.060103AB
8.060103BA	8.060103BB
8.060103CA	8.060103CB

EEB.060103

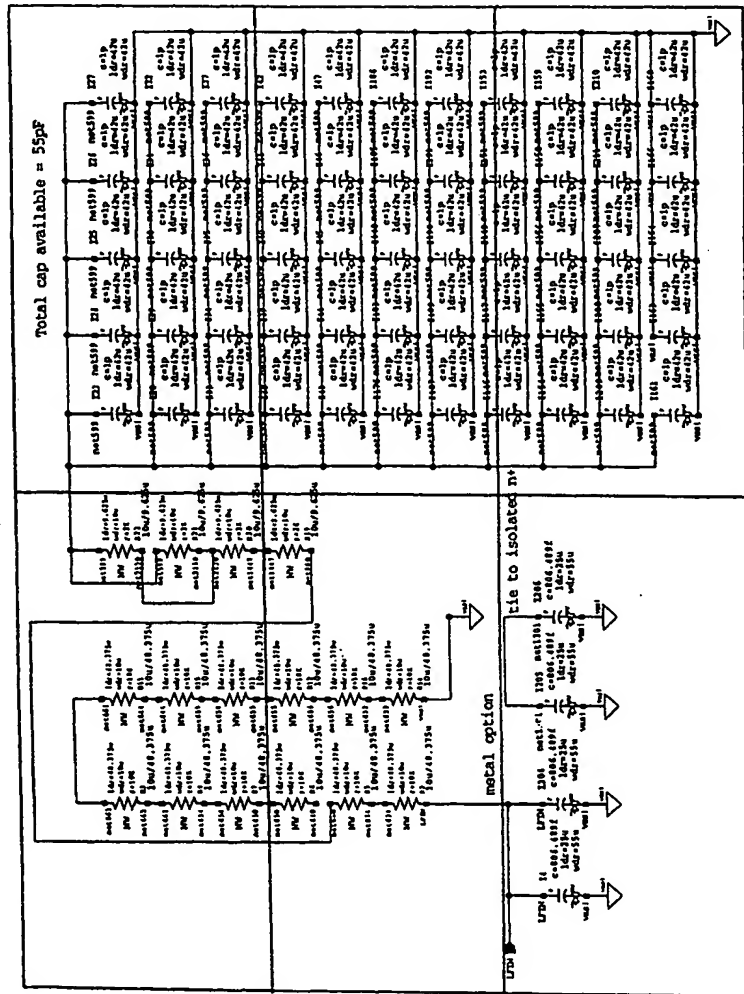


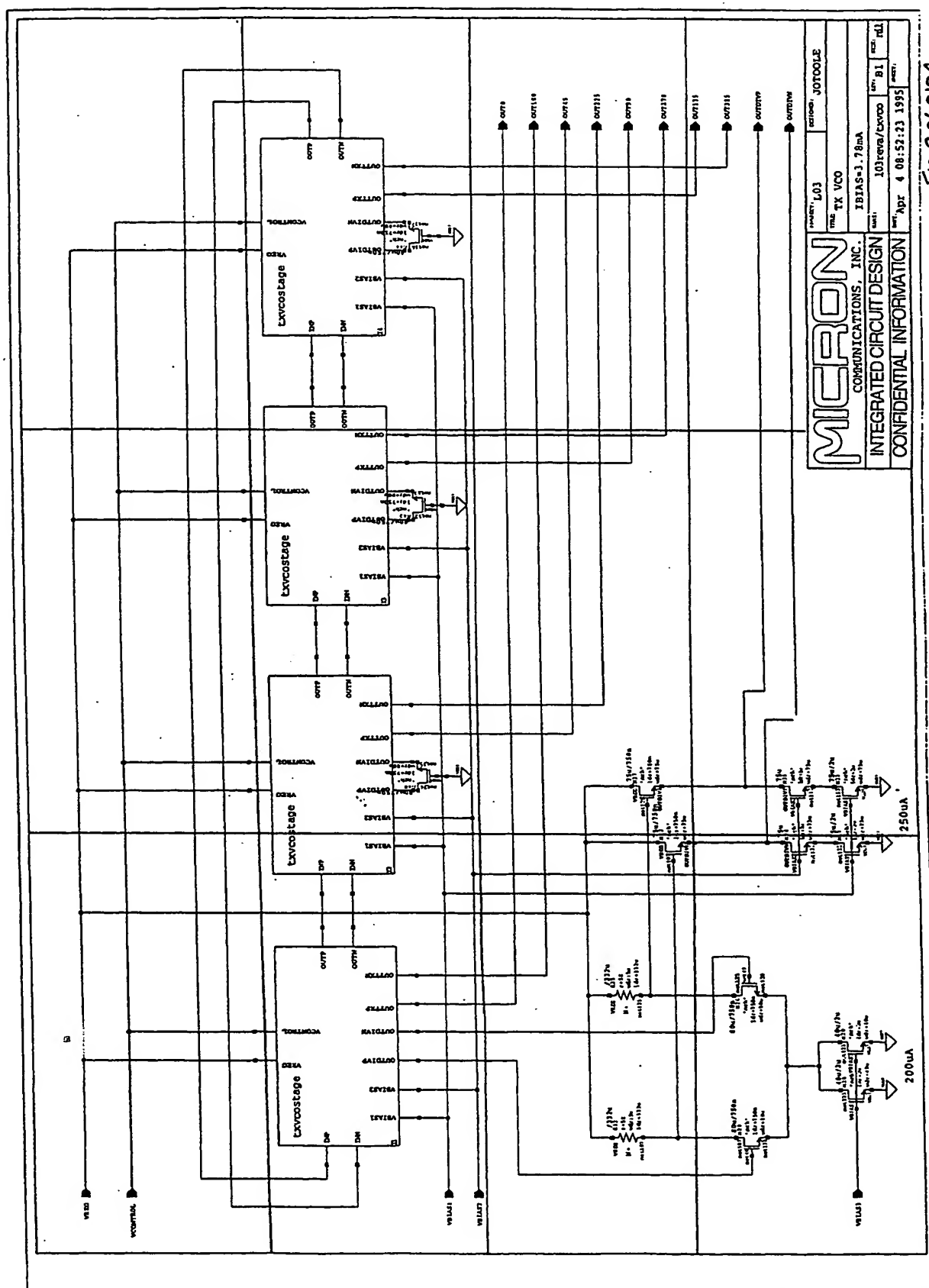
Fig. 8.060103

MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		TX PLL Loop Filter	
INTEGRATED CIRCUIT DESIGN		BW=700KHz	PH=60deg
CONFIDENTIAL INFORMATION		100res/cdopfilter	B8
		DATE: Feb 5 14:40:11 1996	USER:

B2: moved extra caps to biasok
 B8: moved 2 2K resistors to lombs

8.060104AA	8.060104AB	8.060104AC
8.060104BA	8.060104BB	8.060104BC
8.060104CA	8.060104CB	8.060104CC
8.060104DA	8.060104DB	8.060104DC

EE 88.060104

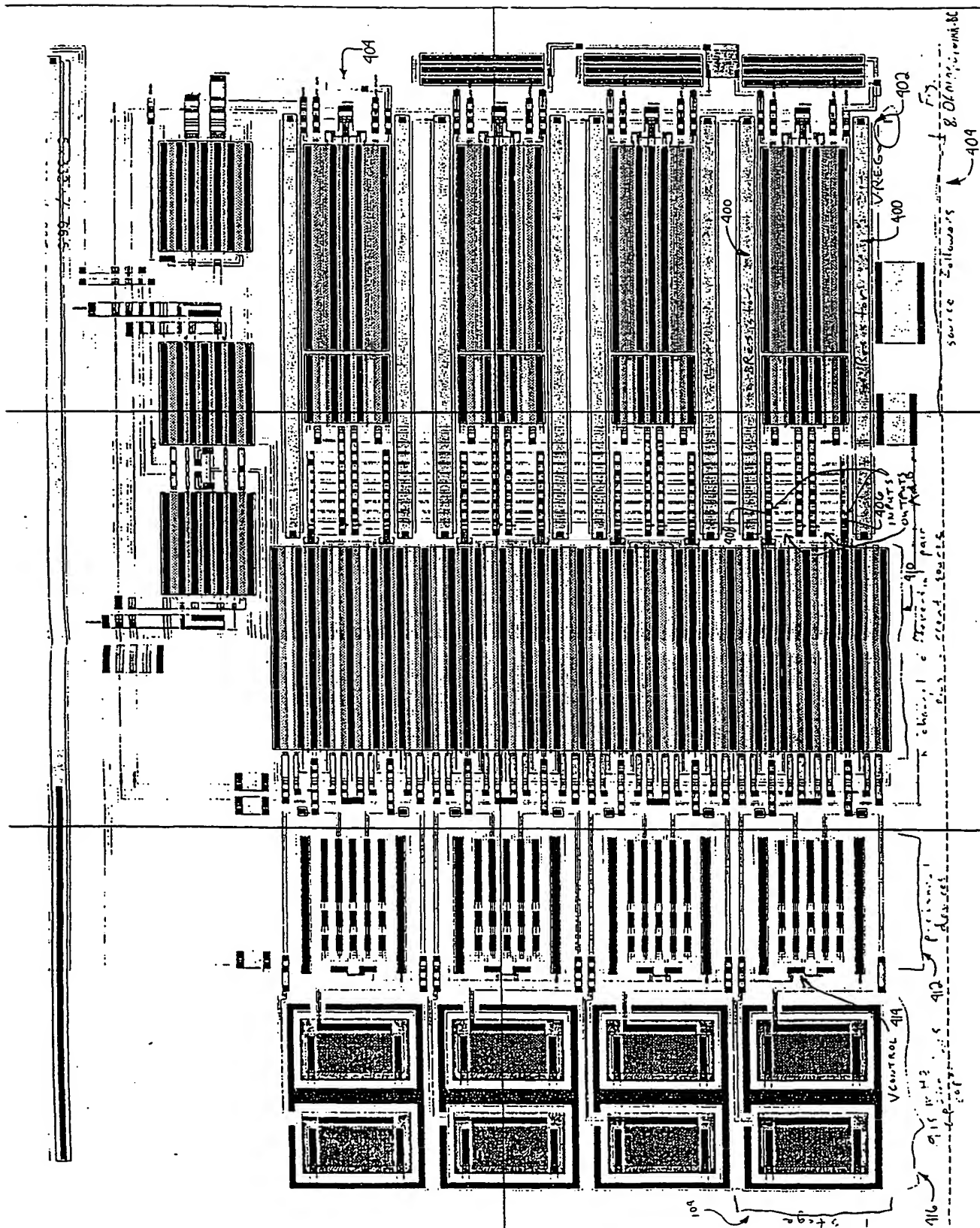


8.06010401AA	8.06010401AB	8.06010401AC	8.06010401AD
8.06010401BA	8.06010401BB	8.06010401BC	8.06010401BD

IL 11 11 11 11 11 11

8.0601040101AA	8.0601040101AB	8.0601040101AC
8.0601040101BA	8.0601040101BB	8.0601040101BC

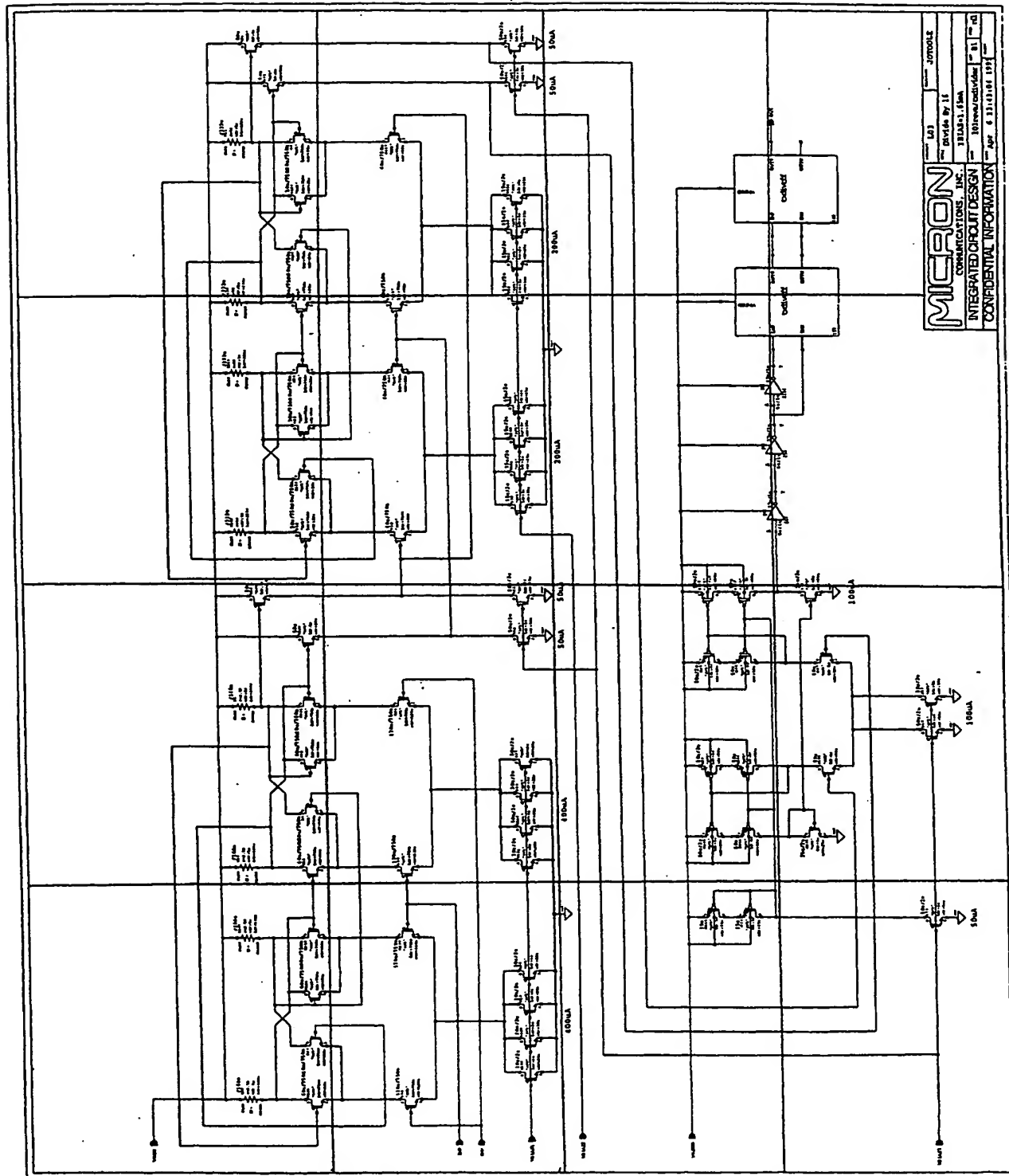
И. И. И. И. И. И. И. И. И. И. И. И. И. И. И.



8.060105AA	8.060105AB	8.060105AC	8.060105AD
8.060105BA	8.060105BB	8.060105BC	8.060105BD
8.060105CA	8.060105CB	8.060105CC	8.060105CD
8.060105DA	8.060105DB	8.060105DC	8.060105DD

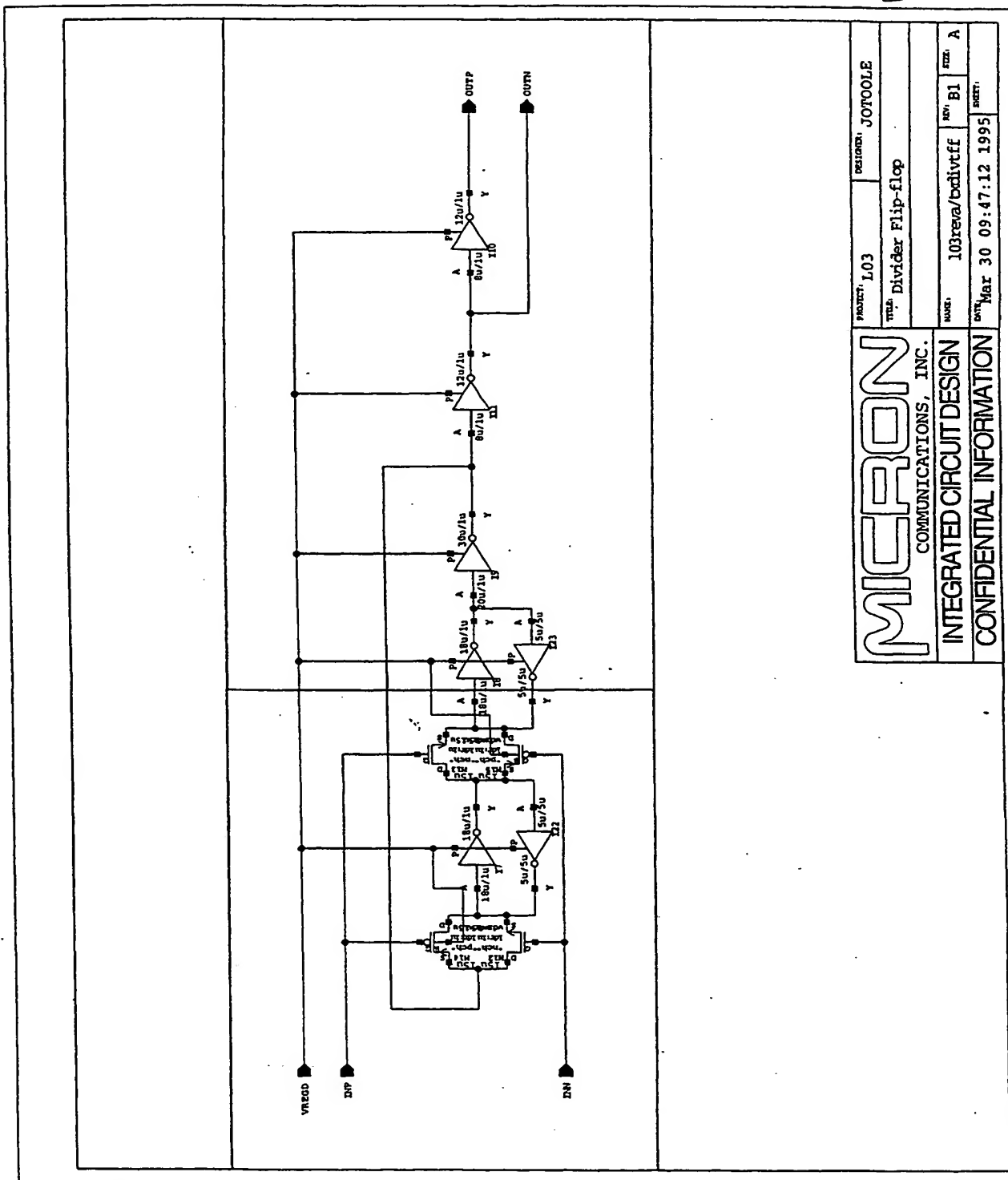
EX-88.060105

FIG. 8.060105



8.06010501AA	8.06010501AB
--------------	--------------

EX 8.06010501



MICRON		PROJECT: L03	DESIGNER: JOFOOLE
COMMUNICATIONS, INC.		TITLE: Divider Flip-flop	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/bcdvtf	REV: B1
CONFIDENTIAL INFORMATION		DATE: Mar 30 09:47:12 1995	SIZE: A
		SHEET: 1	

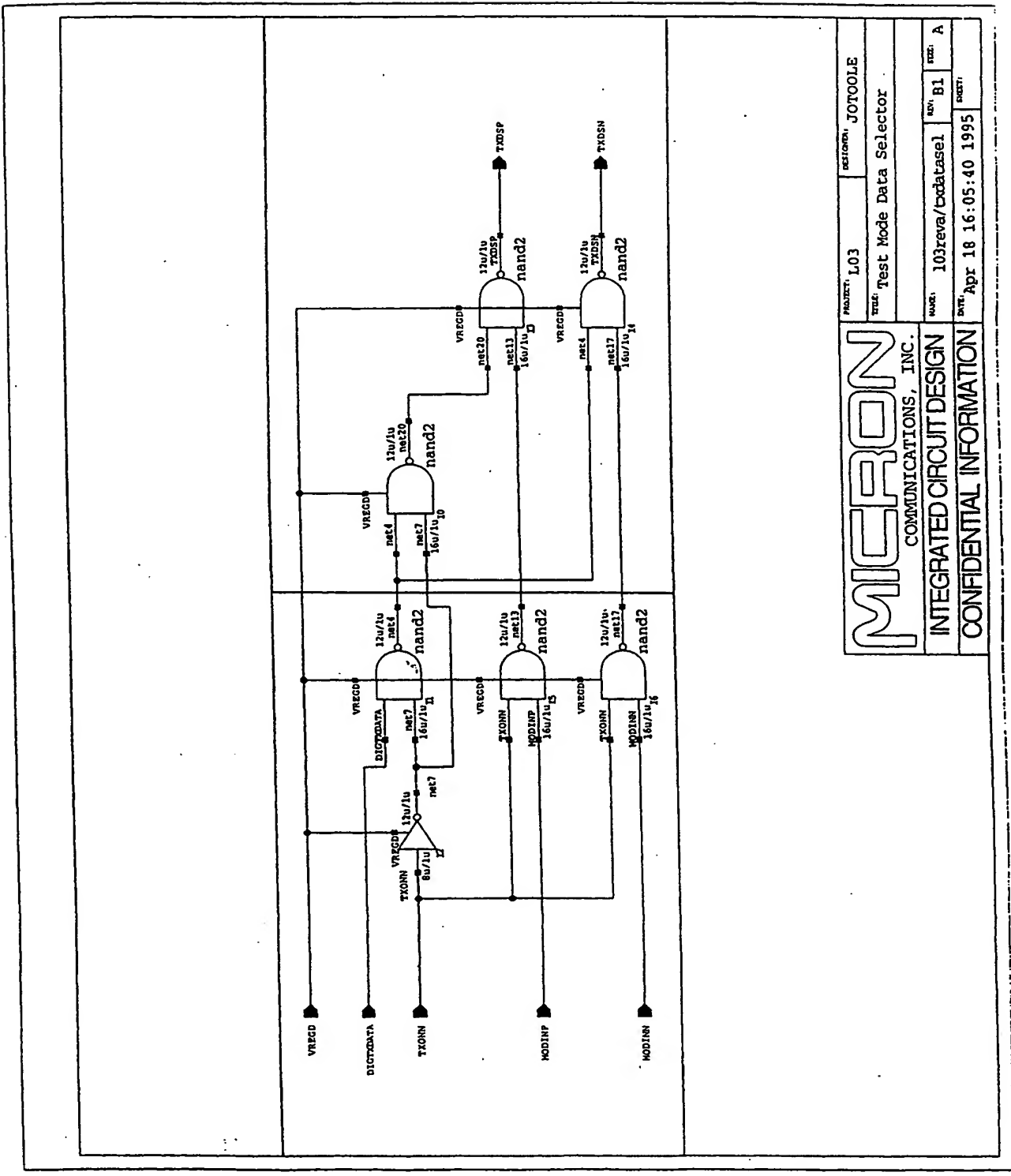
FIG. 8.06010501

8.0602AA	8.0602AB
----------	----------

8.0602AA

8.0502
II 117

Fig. 8.0602



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Test Mode Data Selector	
INTEGRATED CIRCUIT DESIGN		WAVE: 103revb/bcdasel	REV: B1
CONFIDENTIAL INFORMATION		DATE: Apr 18 16:05:40 1995	FILE: A

8.0603AA	8.0603AB
----------	----------

EE 88.0603

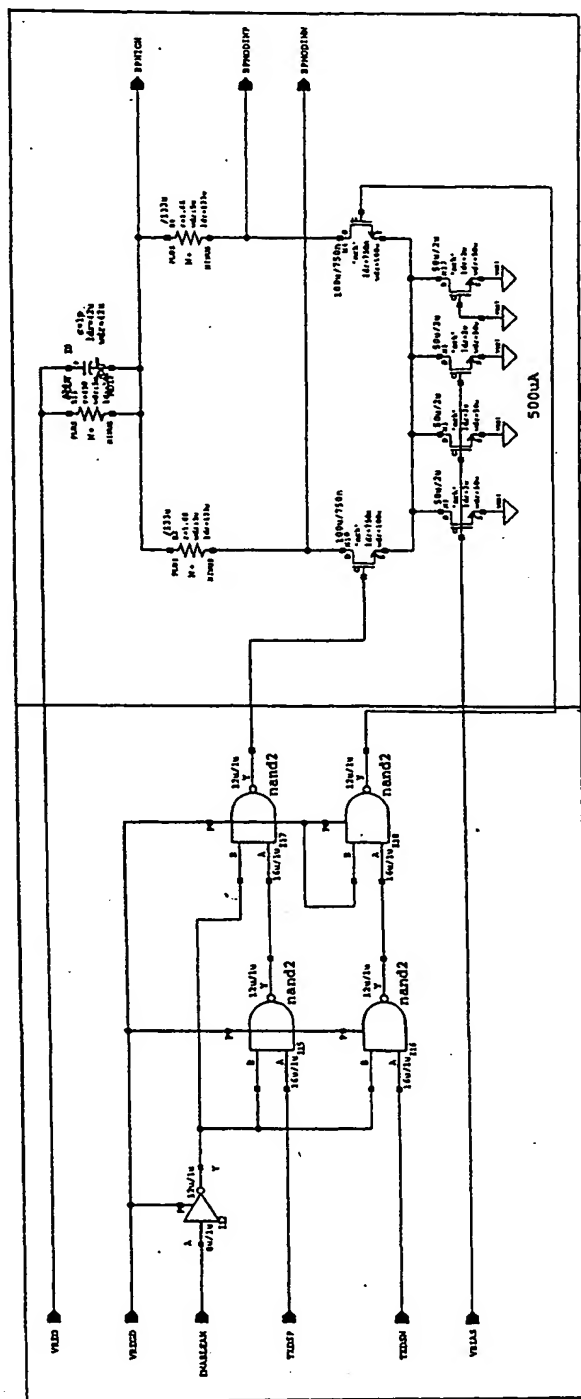


Fig. 8.0603

B8: modified current source

NOORON
COMMUNICATIONS INC.

COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN

CONFIDENTIAL INFORMATION

370040F

NOV 05 1979	L03	DOC ID: A12345
-------------	-----	----------------

BPSK Modulation

IBIAS=500uA

1037

Jan 18 10:28:

8.0604AB

8.0604AA

8.0604AA

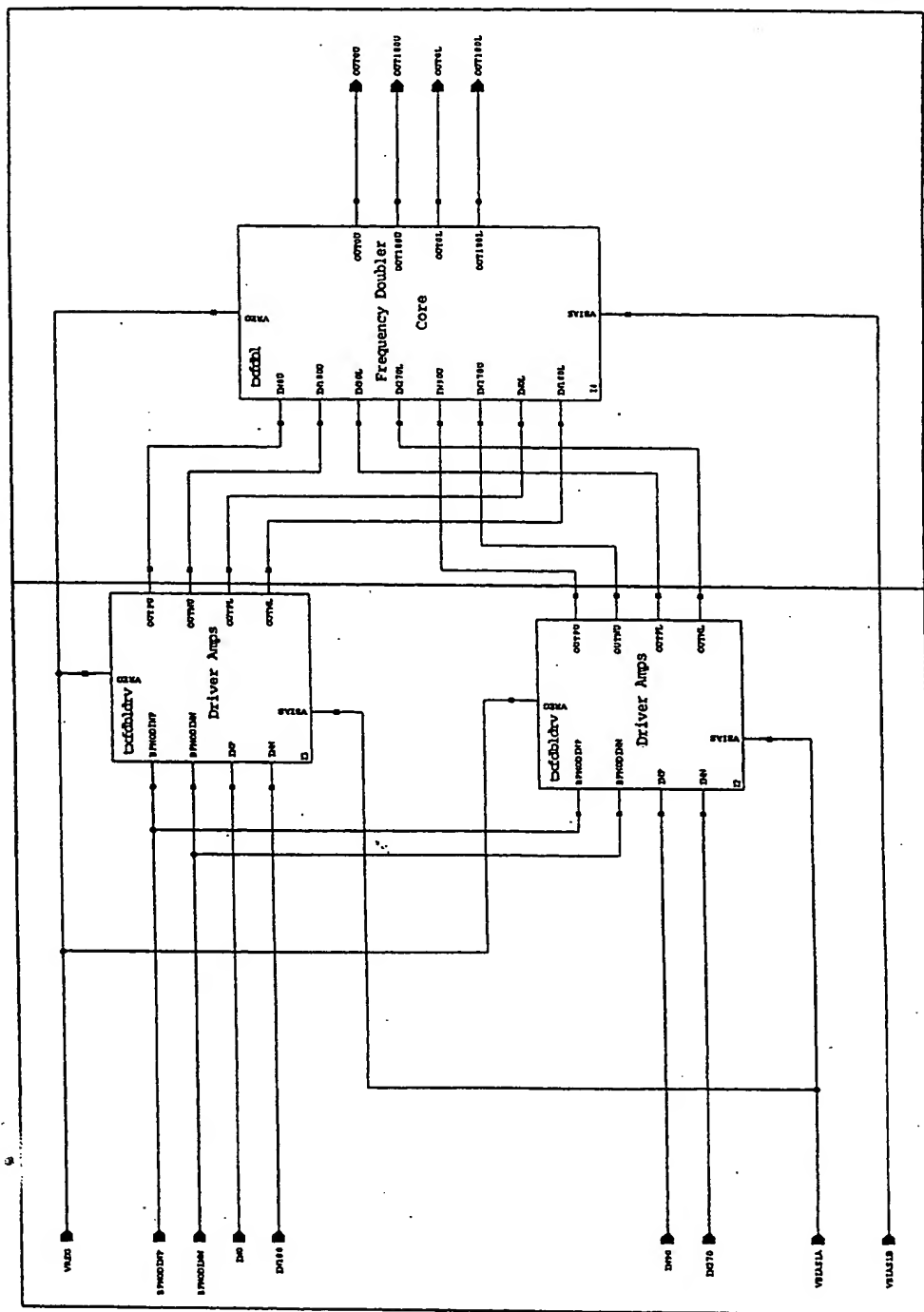
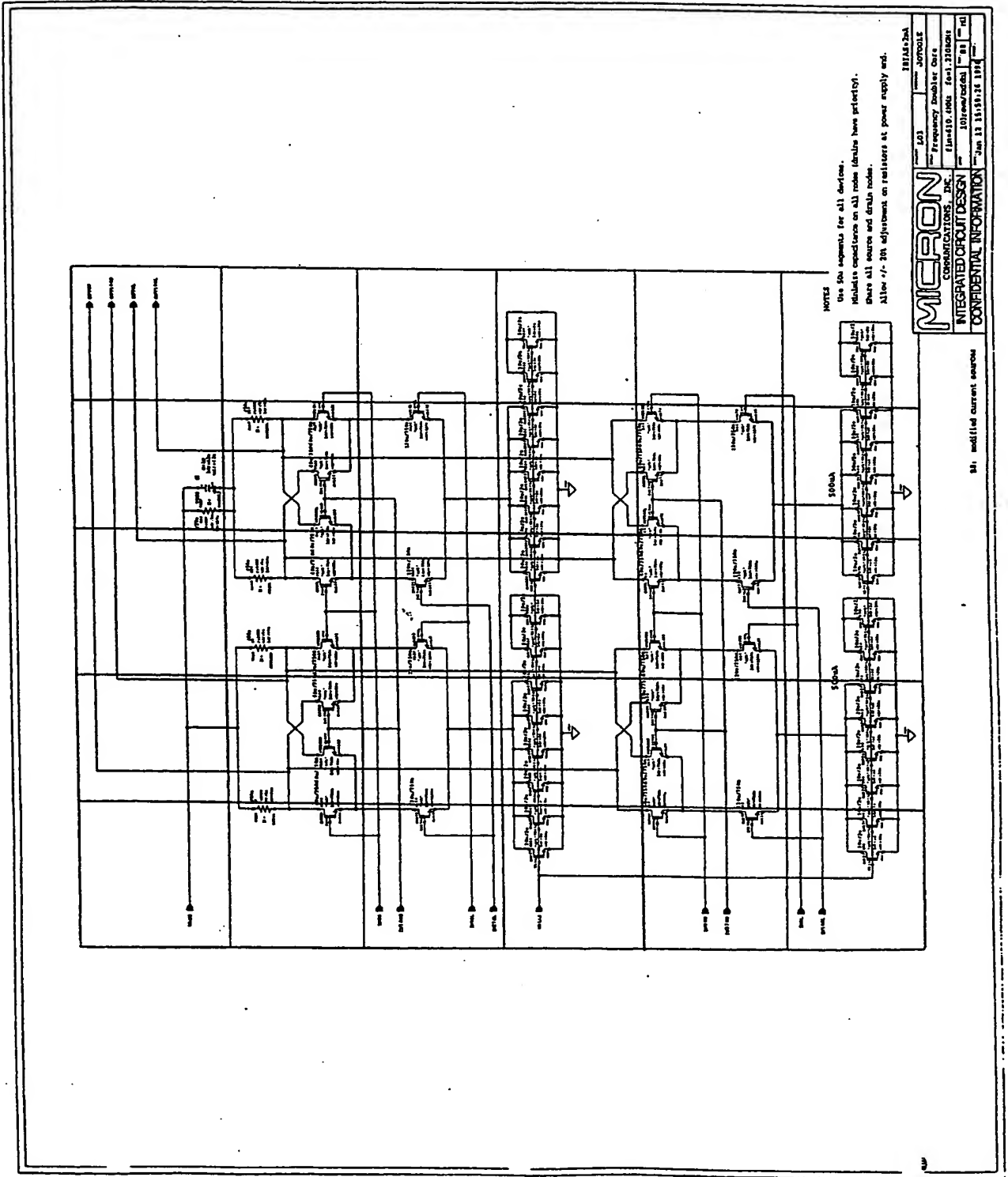


Fig. 8.0604

MICRON		PRODUCT: L03	REVISION: J07000LE
COMMUNICATIONS, INC.		Frequency Doubler	
INTEGRATED CIRCUIT DESIGN		IBIAS=4mA	
CONFIDENTIAL INFORMATION		DATE: 103revs/doubler	REV: 81
		DATE: Apr 5 10:17:13 1995	REV: 81

8.060401AA	8.060401AB	8.060401AC	8.060401AD	8.060401AE
8.060401BA	8.060401BB	8.060401BC	8.060401BD	8.060401BE
8.060401CA	8.060401CB	8.060401CC	8.060401CD	8.060401CE
8.060401DA	8.060401DB	8.060401DC	8.060401DD	8.060401DE
8.060401EA	8.060401EB	8.060401EC	8.060401ED	8.060401EE
8.060401FA	8.060401FB	8.060401FC	8.060401FD	8.060401FE

END 8.060401



NOTES
 Use 500 ohms for all devices.
 Indicate capacitors on all nodes (include bias polarity).
 Show all source and drain nodes.
 Allow +/- 20% adjustment on resistors at power supply end.

101	J070015
Frequency Doubler Core	
Line#10.1000	Rev. 2/20/82
101.000/2000	Rev. 2/20/82
101.000/2000	Rev. 2/20/82
101.000/2000	Rev. 2/20/82

34: modified current source

Fig. 8.06.0901

8.0605AB

8.0605AA

8.0605

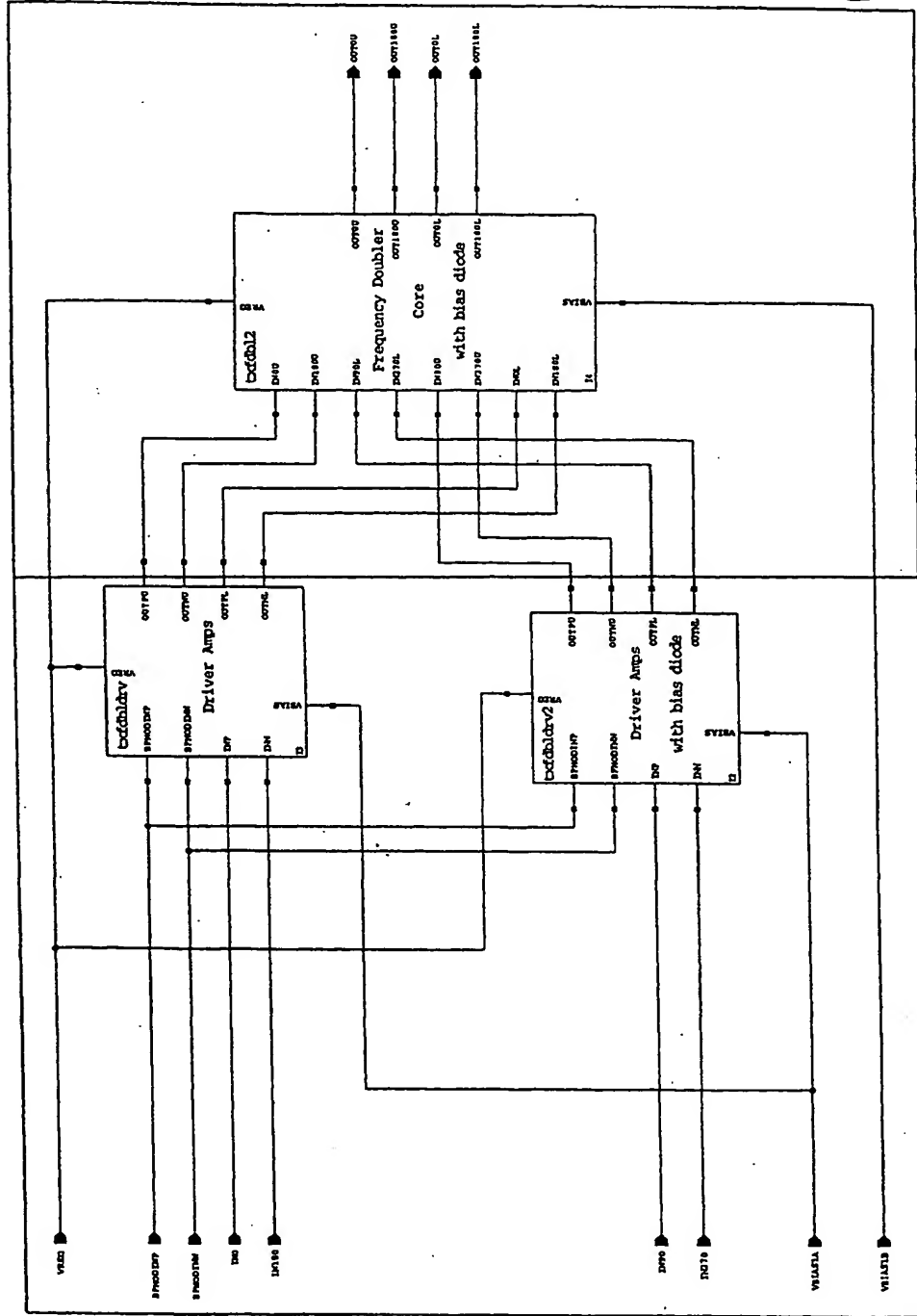


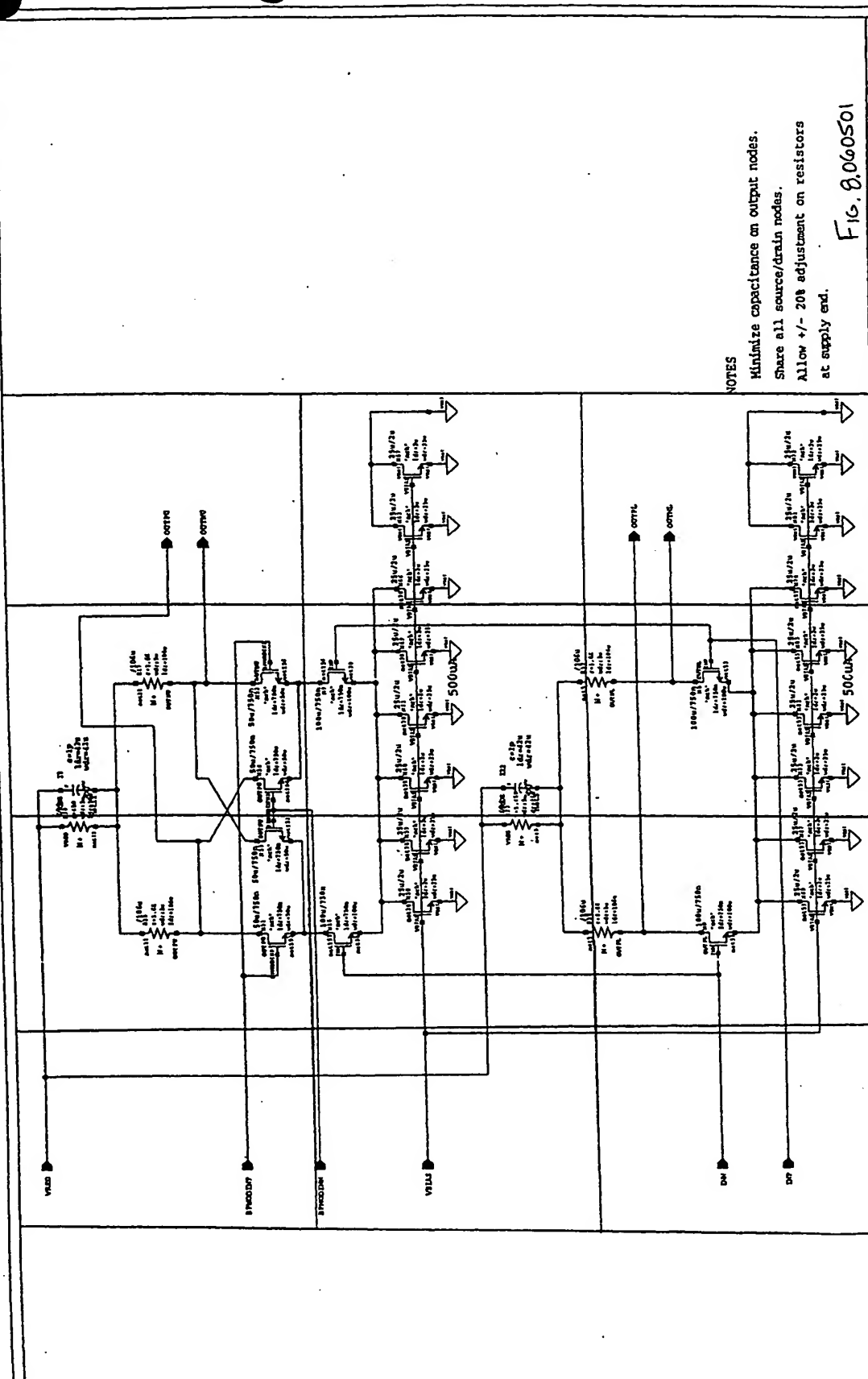
FIG. 8.0605

MICRON		REV. 1.03	REV. 1.03	REV. 1.03
COMMUNICATIONS, INC.		Frequency Doubler		
INTEGRATED CIRCUIT DESIGN		IBIAS=fma		
CONFIDENTIAL INFORMATION		REV. 101rev/bd0b12	REV. B8	REV. JH
		REV. Jan 12 17:22:51 1996		

B8: current sources modified

<i>8.060501AA</i>	<i>8.060501AB</i>	<i>8.060501AC</i>	<i>8.060501AD</i>
<i>8.060501BA</i>	<i>8.060501BB</i>	<i>8.060501BC</i>	<i>8.060501BD</i>
<i>8.060501CA</i>	<i>8.060501CB</i>	<i>8.060501CC</i>	<i>8.060501CD</i>

IEG *8.060501*



NOTES

- Minimize capacitance on output nodes.
- Share all source/drain nodes.
- Allow +/- 20% adjustment on resistors at supply end.

FIG. 8.060501

MICRON		DESIGN	DATE	REV	BY	CHK	APP
COMMUNICATIONS, INC.		103856/bcd/bldv	Jan 12 15:37:26 1996	88			
INTEGRATED CIRCUIT DESIGN		CONFIDENTIAL INFORMATION					
Doubling Driver Amps		J07001E					
IBIAS=1mA							

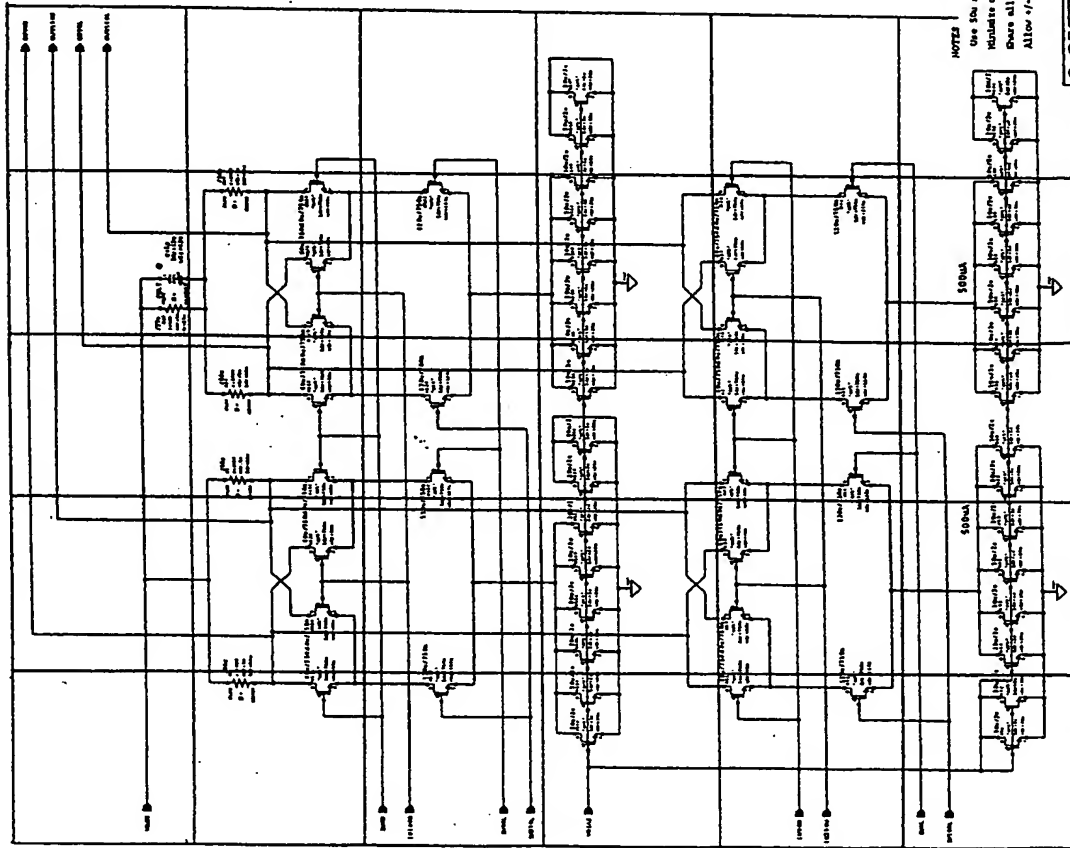
B8: modified current sources

8.060502AA	8.060502AB	8.060502AC	8.060502AD
8.060502BA	8.060502BB	8.060502BC	8.060502BD
8.060502CA	8.060502CB	8.060502CC	8.060502CD

Fig 8.060502

8.060503AA	8.060503AB	8.060503AC	8.060503AD	8.060503AE
8.060503BA	8.060503BB	8.060503BC	8.060503BD	8.060503BE
8.060503CA	8.060503CB	8.060503CC	8.060503CD	8.060503CE
8.060503DA	8.060503DB	8.060503DC	8.060503DD	8.060503DE
8.060503EA	8.060503EB	8.060503EC	8.060503ED	8.060503EE
8.060503FA	8.060503FB	8.060503FC	8.060503FD	8.060503FE

И И 8.060503



NOTES
 Use the sequence for all devices.
 Minimize capacitance on all nodes (nodes have priority).
 Short all sources and drain nodes.
 Allow +/- 20% adjustment on resistors at power supply end.

181A020A	181A020A
Frequency Doubler Core	181A020A
INTEGRATED CIRCUIT DESIGN	181A020A
CONFIDENTIAL INFORMATION	181A020A

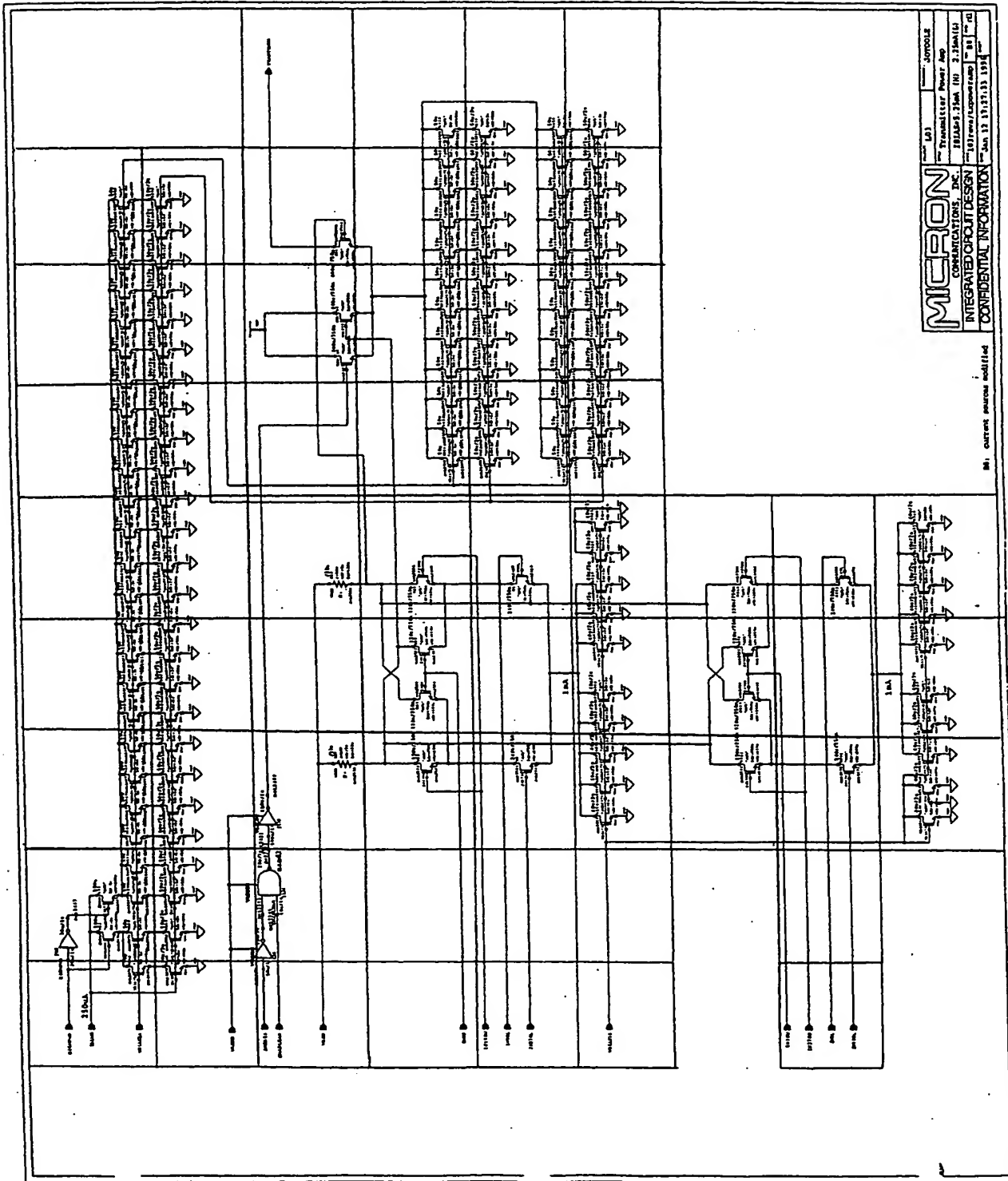
81. modified current source

FIG. 8.060503

8.0606AA	8.0606AB	8.0606AC	8.0606AD	8.0606AE	8.0606AF	8.0606AG	8.0606AH
8.0606BA	8.0606BB	8.0606BC	8.0606BD	8.0606BE	8.0606BF	8.0606BG	8.0606BH
8.0606CA	8.0606CB	8.0606CC	8.0606CD	8.0606CE	8.0606CF	8.0606CG	8.0606CH
8.0606DA	8.0606DB	8.0606DC	8.0606DD	8.0606DE	8.0606DF	8.0606DG	8.0606DH
8.0606EA	8.0606EB	8.0606EC	8.0606ED	8.0606EE	8.0606EF	8.0606EG	8.0606EH
8.0606FA	8.0606FB	8.0606FC	8.0606FD	8.0606FE	8.0606FF	8.0606FG	8.0606FH
		8.0606GC	8.0606GD	8.0606GE			
8.0606HA	8.0606HB	8.0606HC	8.0606HD	8.0606HE			
	8.0606IB	8.0606IC	8.0606ID	8.0606IE			


 DEPARTMENT OF DEFENSE

FIG. 8.0606

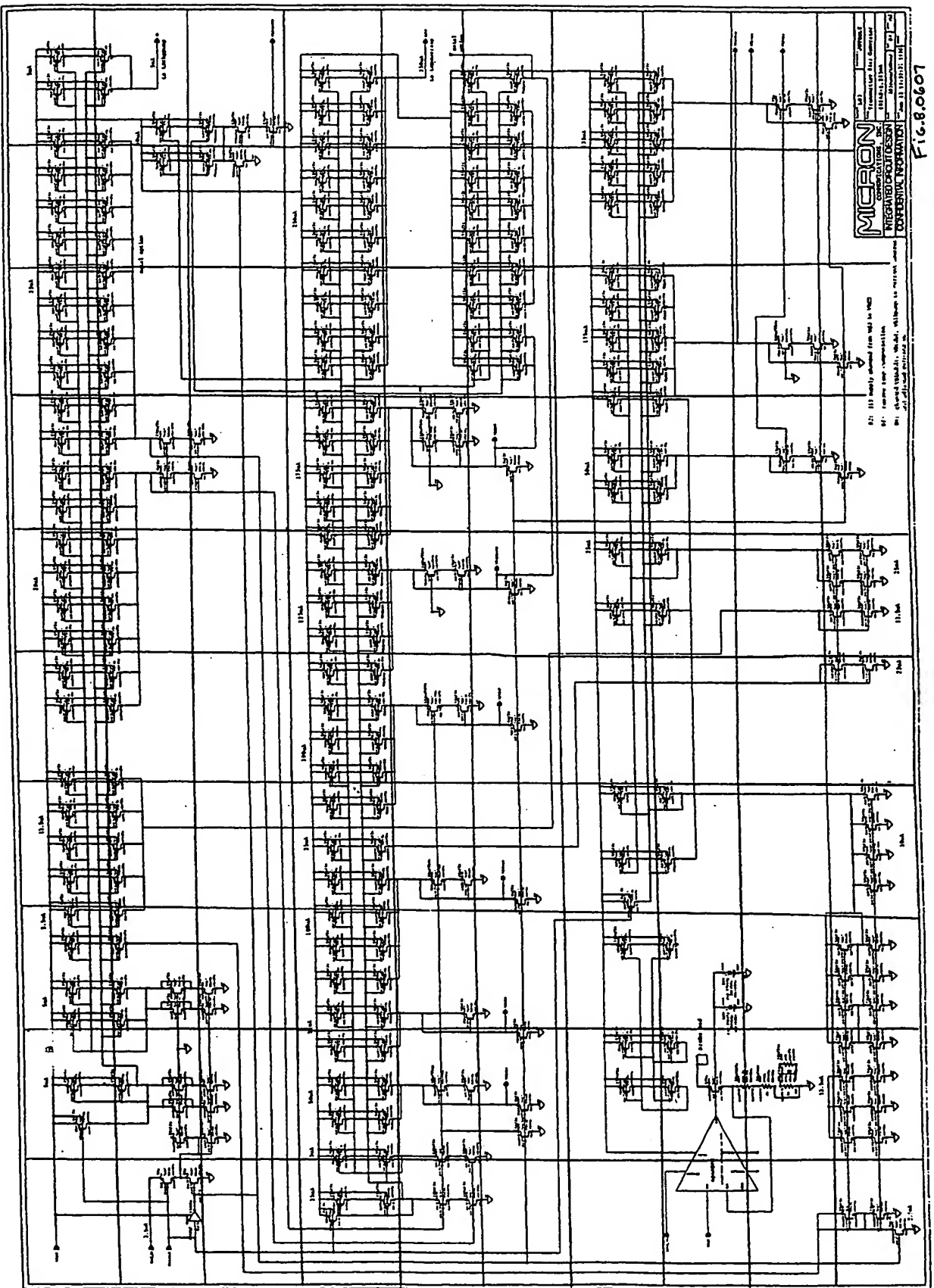


8.0607AA	8.0607AB	8.0607AC	8.0607AD	8.0607AE	8.0607AF	8.0607AG	8.0607AH	8.0607AI	8.0607AJ
8.0607BA	8.0607BB	8.0607BC	8.0607BD	8.0607BE	8.0607BF	8.0607BG	8.0607BH	8.0607BI	8.0607BJ
8.0607CA	8.0607CB	8.0607CC	8.0607CD	8.0607CE	8.0607CF	8.0607CG	8.0607CH	8.0607CI	8.0607CJ
8.0607DA	8.0607DB	8.0607DC	8.0607DD	8.0607DE	8.0607DF	8.0607DG	8.0607DH	8.0607DI	8.0607DJ
8.0607EA	8.0607EB	8.0607EC	8.0607ED	8.0607EE	8.0607EF	8.0607EG	8.0607EH	8.0607EI	8.0607EJ
8.0607FA	8.0607FB	8.0607FC	8.0607FD	8.0607FE	8.0607FF	8.0607FG	8.0607FH	8.0607FI	8.0607FJ
8.0607GA	8.0607GB	8.0607GC	8.0607GD	8.0607GE	8.0607GF	8.0607GG	8.0607GH	8.0607GI	8.0607GJ
8.0607HA	8.0607HB	8.0607HC	8.0607HD	8.0607HE	8.0607HF	8.0607HG	8.0607HH	8.0607HI	8.0607HJ
8.0607IA	8.0607IB	8.0607IC	8.0607ID	8.0607IE	8.0607IF	8.0607IG	8.0607IH	8.0607II	8.0607IJ
8.0607JA	8.0607JB	8.0607JC	8.0607JD	8.0607JE	8.0607JF	8.0607JG	8.0607JH	8.0607JI	8.0607JJ

IL 11 07 88.006007

Fig. 8.0607

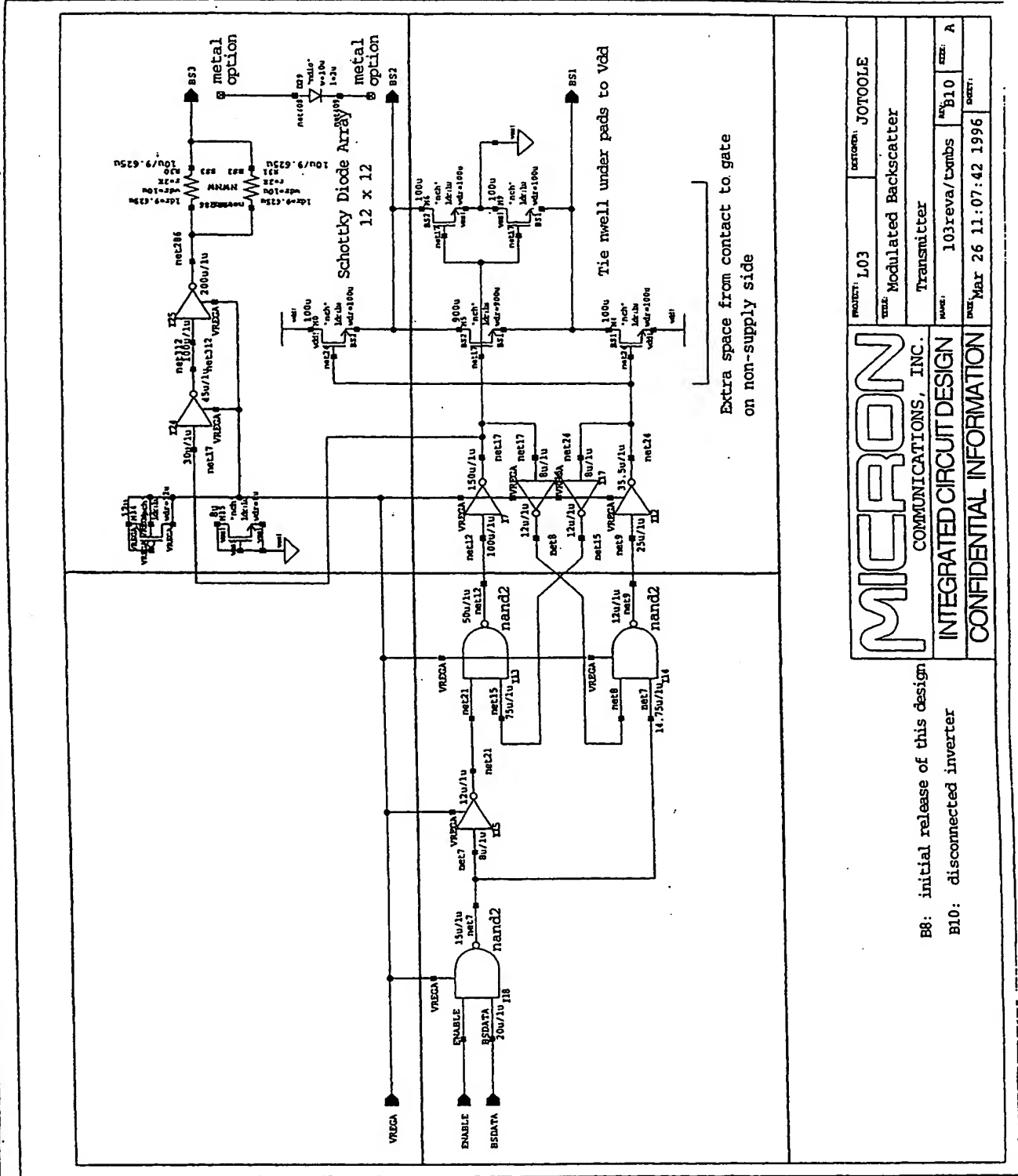
MICRON
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION
DATE 11/10/72
BY 11/10/72



1: 111 Input/output from cell to I/O
2: Input/output from cell to I/O
3: Input/output from cell to I/O
4: Input/output from cell to I/O
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78: Input/output from cell to I/O
79: Input/output from cell to I/O
80: Input/output from cell to I/O
81: Input/output from cell to I/O
82: Input/output from cell to I/O
83: Input/output from cell to I/O
84: Input/output from cell to I/O
85: Input/output from cell to I/O
86: Input/output from cell to I/O
87: Input/output from cell to I/O
88: Input/output from cell to I/O
89: Input/output from cell to I/O
90: Input/output from cell to I/O
91: Input/output from cell to I/O
92: Input/output from cell to I/O
93: Input/output from cell to I/O
94: Input/output from cell to I/O
95: Input/output from cell to I/O
96: Input/output from cell to I/O
97: Input/output from cell to I/O
98: Input/output from cell to I/O
99: Input/output from cell to I/O
100: Input/output from cell to I/O

8.0608AA	8.0608AB
8.0608BA	8.0608BB

IL 11 07 88.01150088



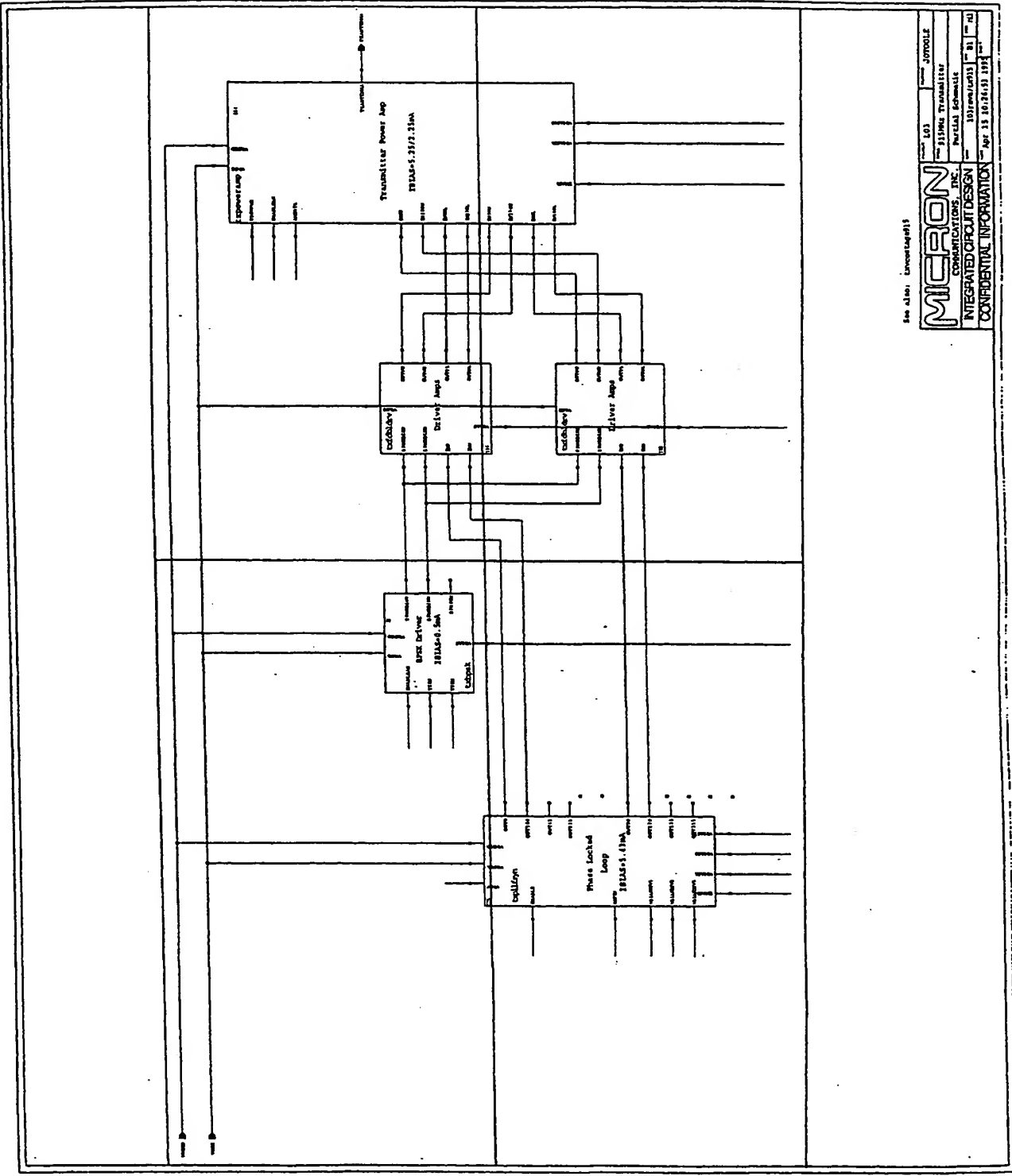
MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

PROJECT: L03	DESIGNED: JOTOOLE
TITLE: Modulated Backscatter	
TRANSMITTER	
NAME: 103reva/ombs	REV: B10
DATE: Mar 26 11:07:42 1996	SIZE: A

B8: initial release of this design
 B10: disconnected inverter

8.07AB	8.07BB
8.07AA	8.07BA

BB.007



See also: Encapsulated

MICRON		LOT	J070012
COMMUNICATIONS, INC.		315MHz Transmitter	
INTEGRATED CIRCUIT DESIGN		Partial Schematic	
CONFIDENTIAL INFORMATION		181AS-1.51/2.25MA	21
		Apr 13 10:26:13 1981	

Fig. 8.07

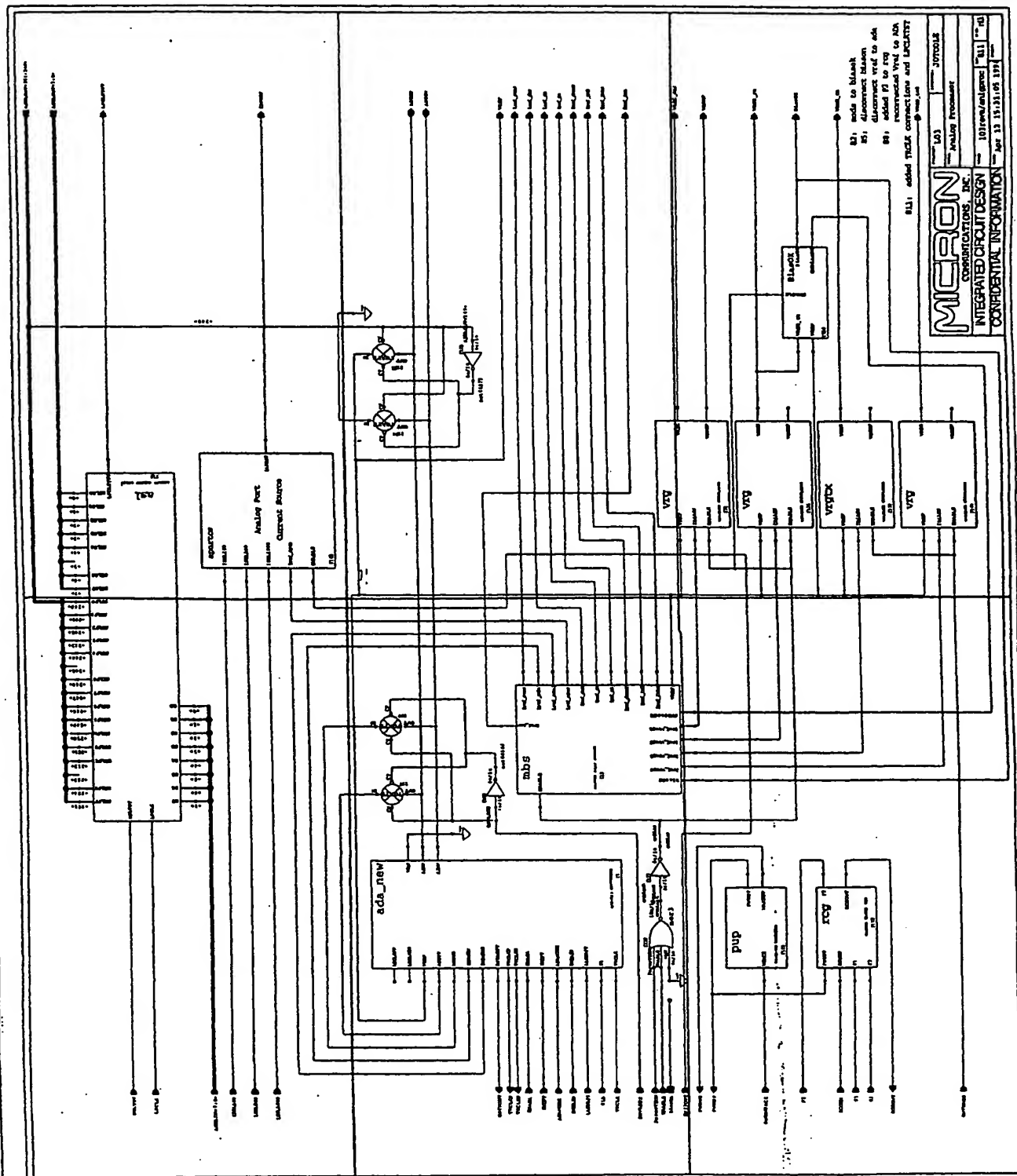
8.0701AA	8.0701AB
8.0701BA	8.0701BB
8.0701CA	8.0701CB

8.0701

9AA	9AB
9BA	9BB
9CA	9CB

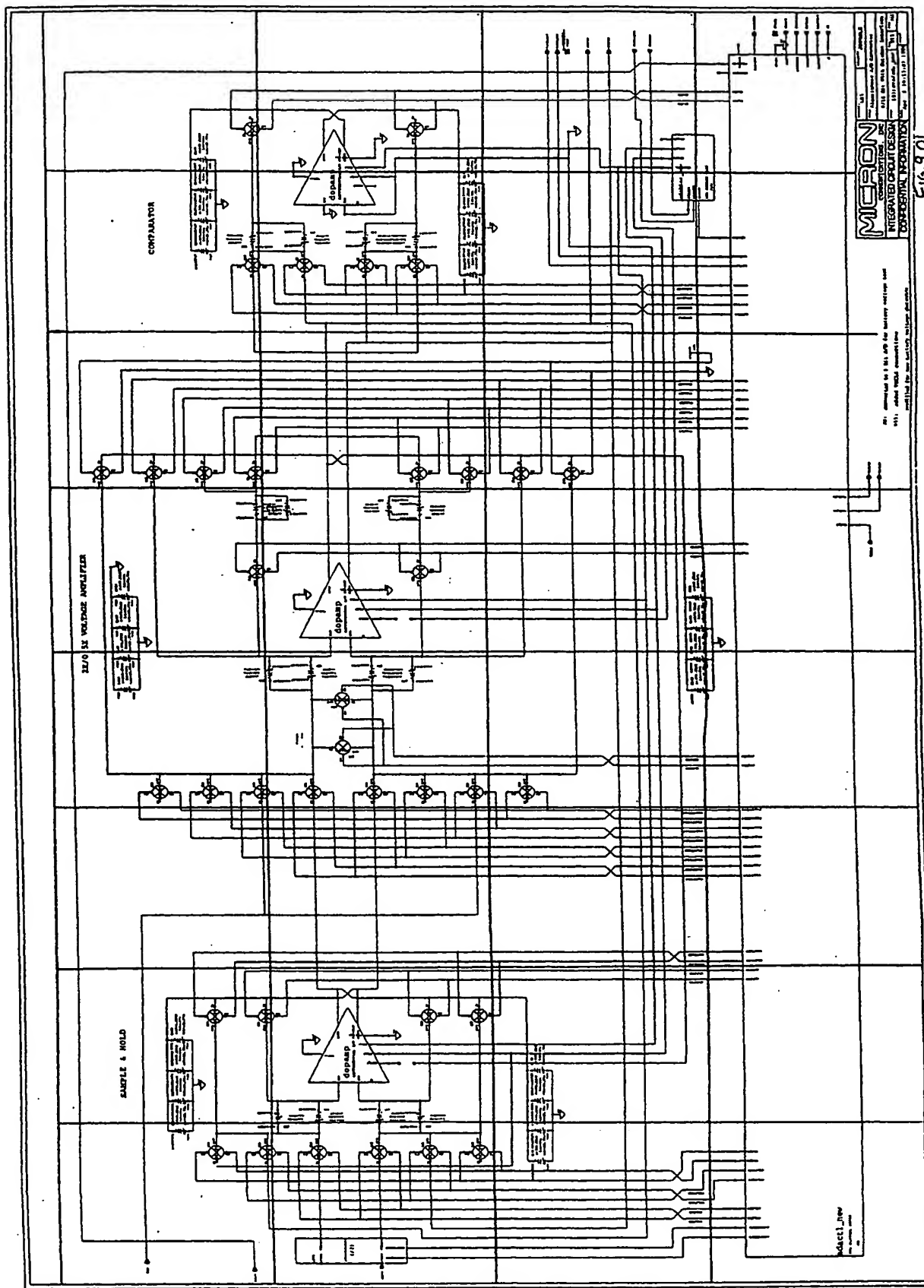
SECRET

Fig. 9



9.01AA	9.01AB	9.01AC	9.01AD	9.01AE	9.01AF	9.01AG	9.01AH
9.01BA	9.01BB	9.01BC	9.01BD	9.01BE	9.01BF	9.01BG	9.01BH
9.01CA	9.01CB	9.01CC	9.01CD	9.01CE	9.01CF	9.01CG	9.01CH
9.01DA	9.01DB	9.01DC	9.01DD	9.01DE	9.01DF	9.01DG	9.01DH

II II II II II



MICRON
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION
 10000 E. 1st Ave. Suite 100
 Denver, CO 80231
 Tel: 303.733.1100
 Fax: 303.733.1101

3-bit, 3 1/2 X DAC for Battery Monitor Unit
 10000 E. 1st Ave. Suite 100
 Denver, CO 80231
 Tel: 303.733.1100
 Fax: 303.733.1101

Fig. 9.01

9.0101AA	9.0101AB	9.0101AC	9.0101AD	9.0101AE	9.0101AF	9.0101AG	9.0101AH	9.0101AI	9.0101AJ	9.0101AK
9.0101BA	9.0101BB	9.0101BC	9.0101BD	9.0101BE	9.0101BF	9.0101BG	9.0101BH	9.0101BI	9.0101BJ	9.0101BK
9.0101CA	9.0101CB	9.0101CC	9.0101CD	9.0101CE	9.0101CF	9.0101CG	9.0101CH	9.0101CI	9.0101CJ	9.0101CK

IX XX XXX XXXX XXXXX

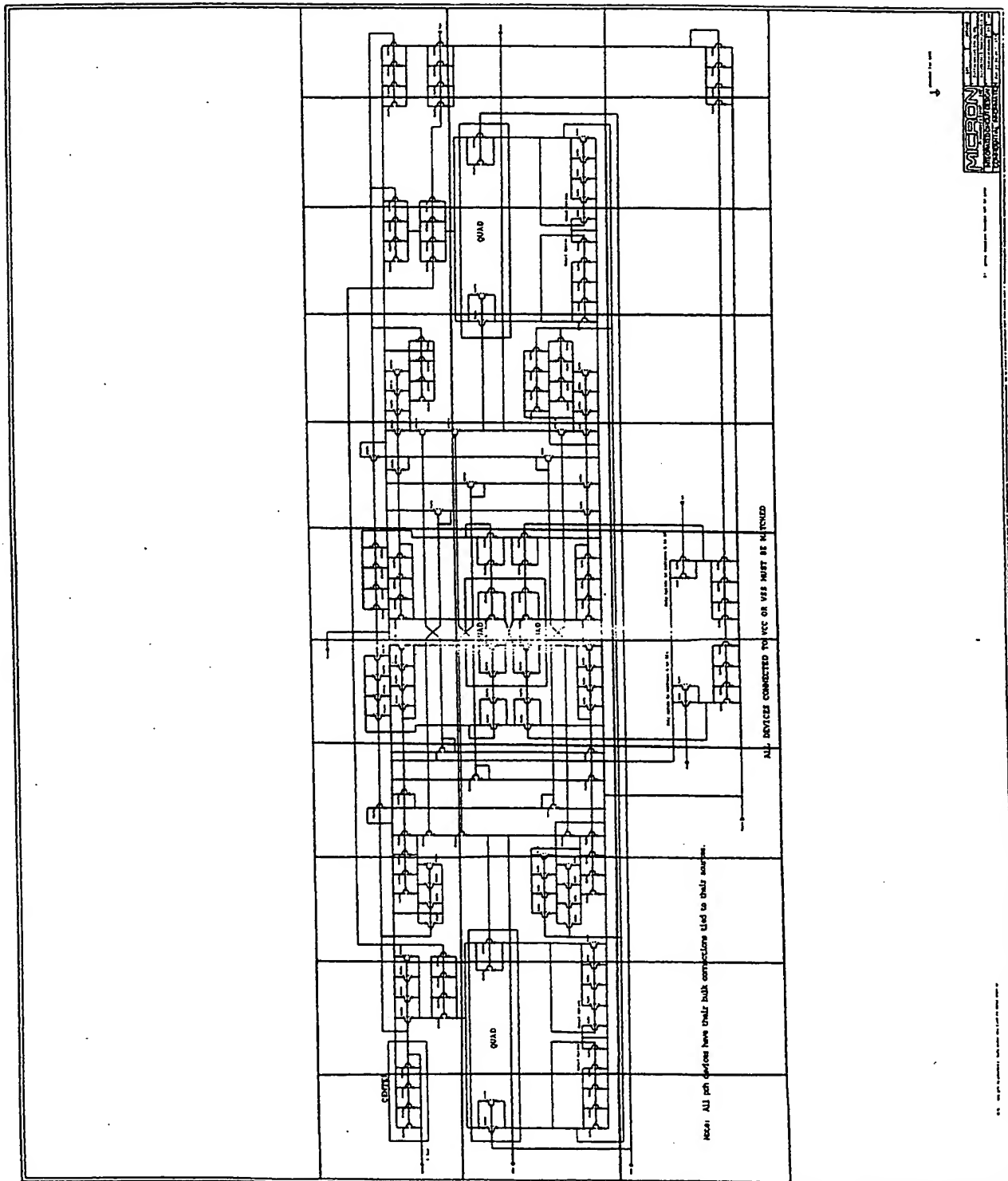


FIG. 9.0101

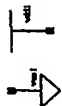
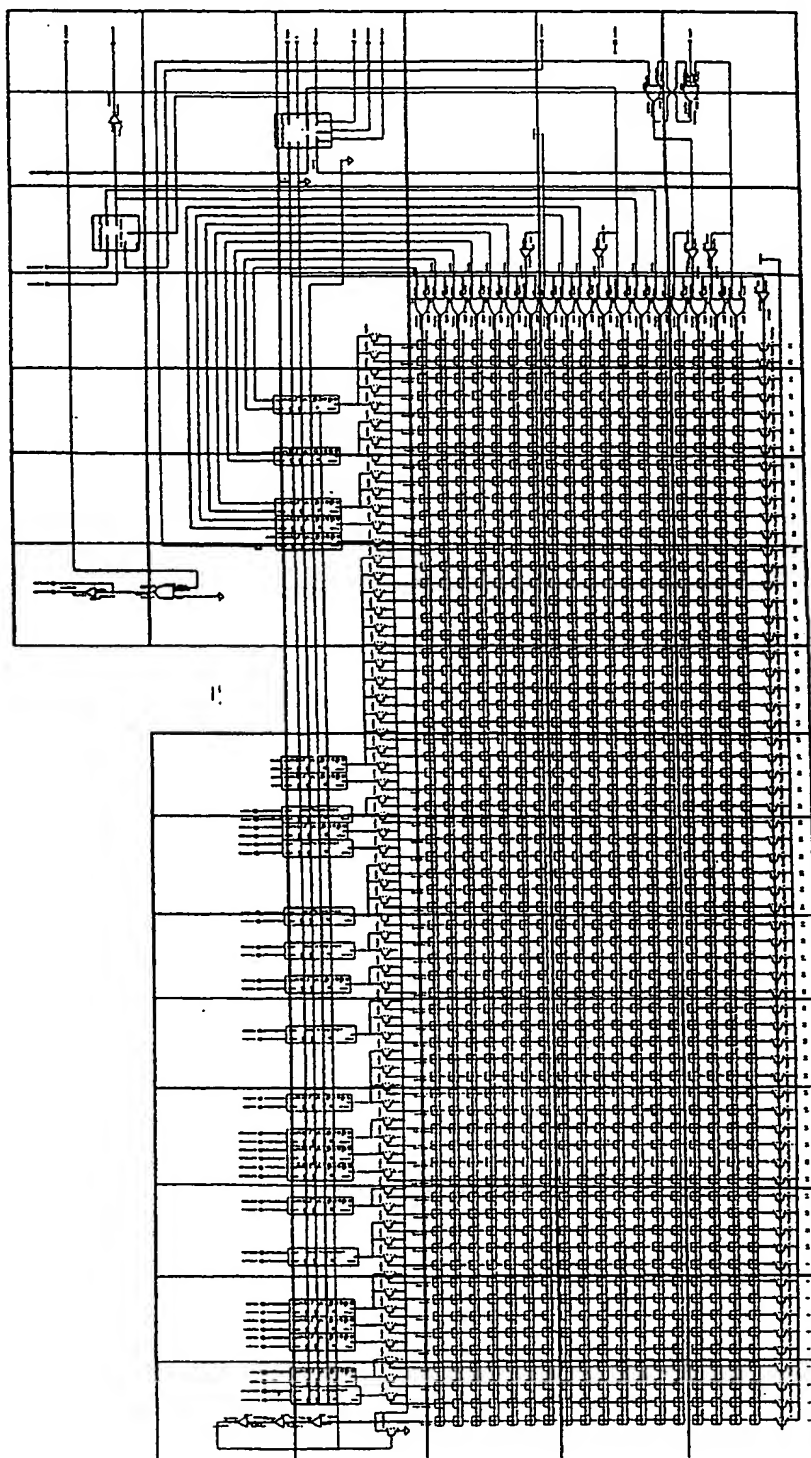


Fig. 9.0102

MICRON		PROJECT: L03		DESIGNER: JOTOOLE	
COMMUNICATIONS, INC.		TITLE: Analog Divide by 2			
INTEGRATED CIRCUIT DESIGN		NAME: l03reva/adaprescale		REV: B1	CODE: A
CONFIDENTIAL INFORMATION		DATE: May 19 16:34:53 1995		CLOCK:	

9.0103BA	9.0103BB	9.0103BC	9.0103BD	9.0103BE	9.0103BF	9.0103BG	9.0103BH		9.0103AJ	9.0103AK	9.0103AL	9.0103AM	9.0103AN	9.0103AO	9.0103AP
9.0103CA	9.0103CB	9.0103CC	9.0103CD	9.0103CE	9.0103CF	9.0103CG	9.0103CH	9.0103CI	9.0103BJ	9.0103BK	9.0103BL	9.0103BM	9.0103BN	9.0103BO	9.0103BP
9.0103DA	9.0103DB	9.0103DC	9.0103DD	9.0103DE	9.0103DF	9.0103DG	9.0103DH	9.0103DI	9.0103DJ	9.0103DK	9.0103DL	9.0103DM	9.0103DN	9.0103DO	9.0103DP
9.0103EA	9.0103EB	9.0103EC	9.0103ED	9.0103EE	9.0103EF	9.0103EG	9.0103EH	9.0103EI	9.0103EJ	9.0103EK	9.0103EL	9.0103EM	9.0103EN	9.0103EO	9.0103EP
9.0103FA	9.0103FB	9.0103FC	9.0103FD	9.0103FE	9.0103FF	9.0103FG	9.0103FH	9.0103FI	9.0103FJ	9.0103FK	9.0103FL	9.0103FM	9.0103FN	9.0103FO	9.0103FP

Fig. 9.0103



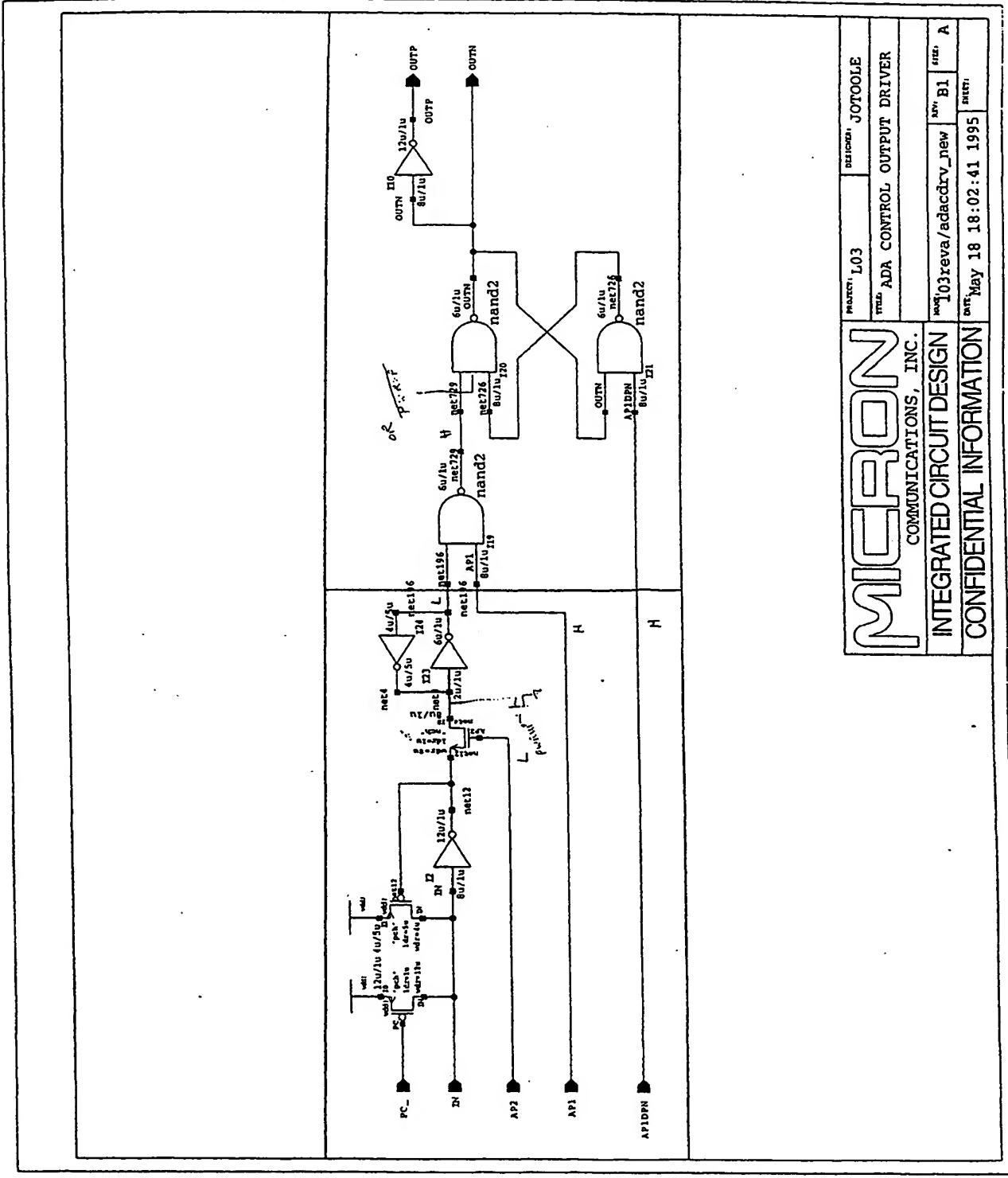
MICRON
MEMORY SYSTEM
100-1000

9.010301AA	9.010301AB	9.010301AC
9.010301BA	9.010301BB	9.010301BC
9.010301CA	9.010301CB	9.010301CC

SECRET

9.010302AA	9.010302AB
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EX-9010302

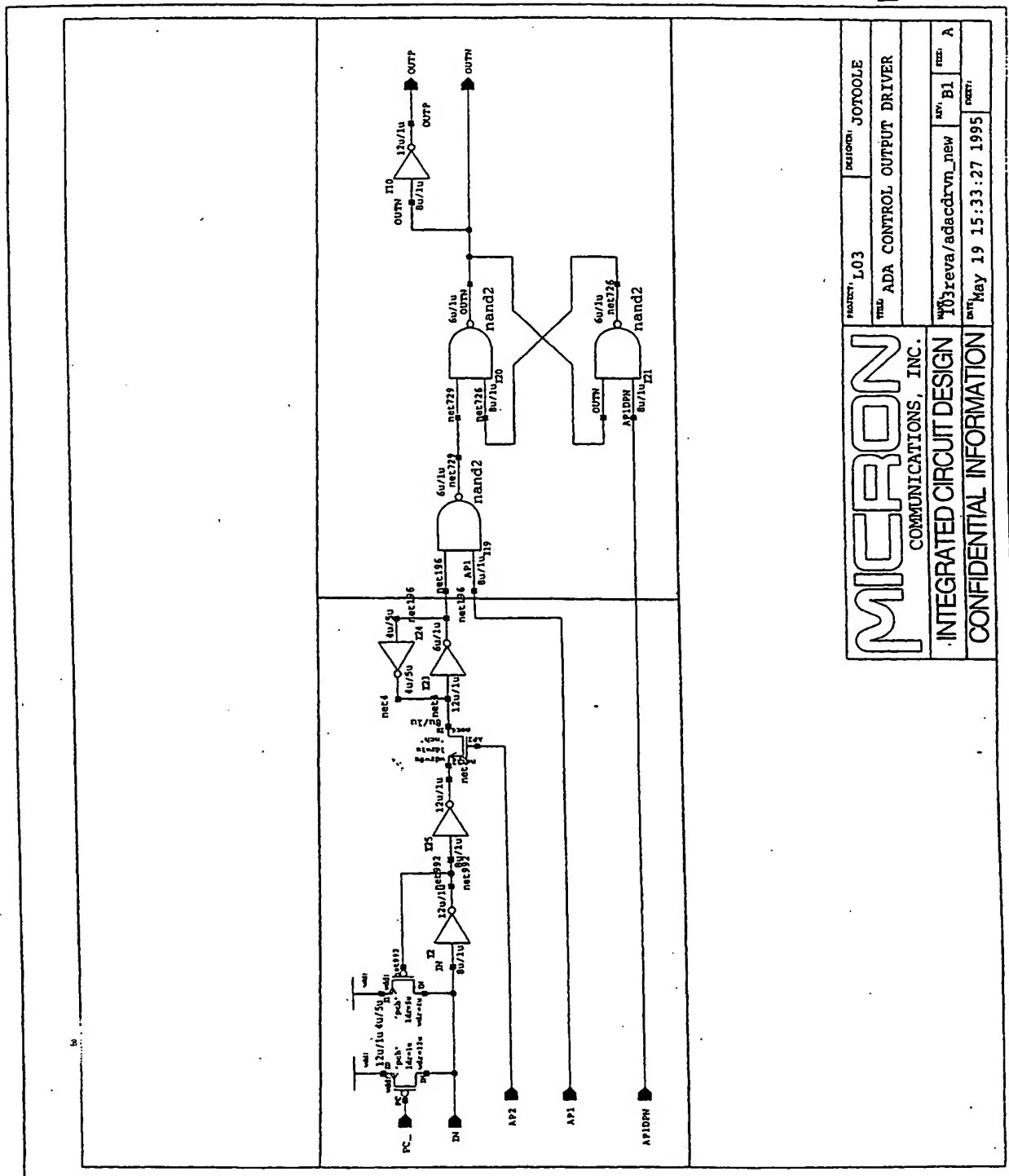


MICRON		PROJECT: L03	DESIGNER: JOTOOLE	
COMMUNICATIONS, INC.		TITLE: ADA CONTROL OUTPUT DRIVER		
INTEGRATED CIRCUIT DESIGN		WORK: J03reva/adacdrv_new	REV: B1	REV: A
CONFIDENTIAL INFORMATION		DATE: May 18 18:02:41 1995		

FIG. 9.010302

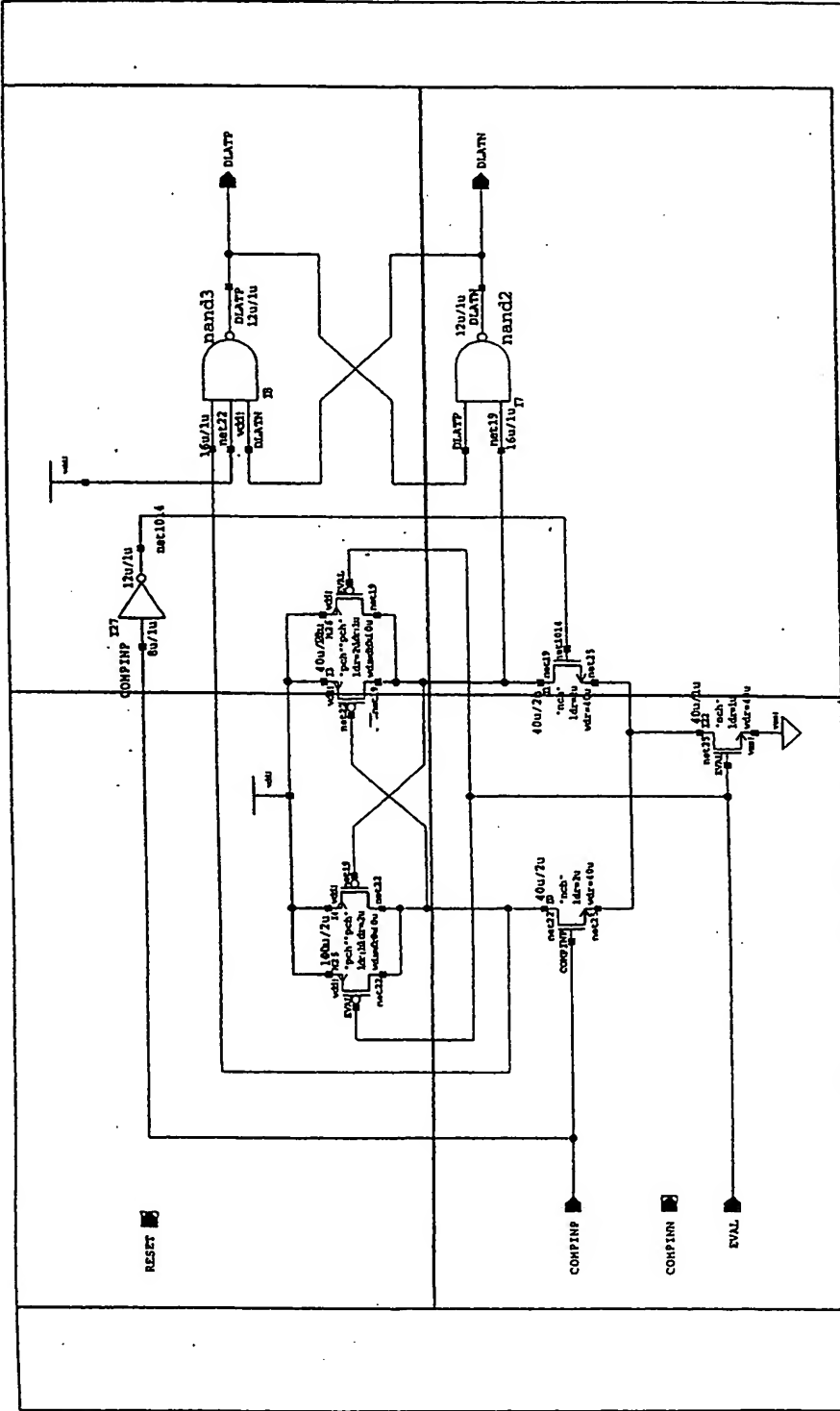
9.010303AA	9.010303AB
------------	------------

9.010303



9.010304AA	9.010304AB
9.010304BA	9.010304BB

II II 9.010304

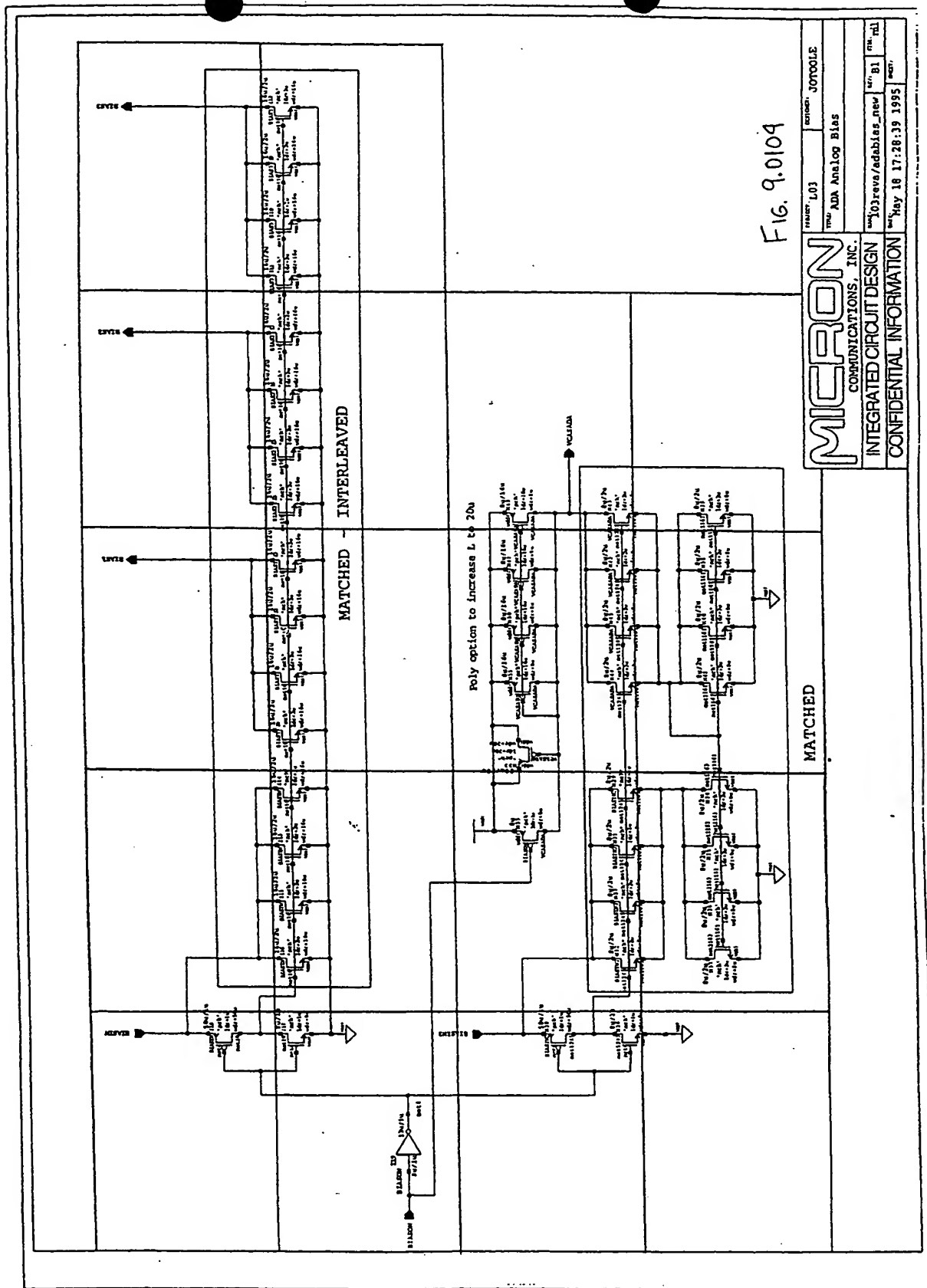


- B5: disconnect eval input; connect to vss
- B8: reconnect eval and change reset polarity
- B11: modified for use in battery voltage sensor

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: ADA Data Latch	
INTEGRATED CIRCUIT DESIGN		REV: 103revA/adadlat_new	REV: B11
CONFIDENTIAL INFORMATION		DATE: Apr 8 10:39:12 1996	DATE: A

9.0104AA	9.0104AB	9.0104AC	9.0104AD	9.0104AE
9.0104BA	9.0104BB	9.0104BC	9.0104BD	9.0104BE
9.0104CA	9.0104CB	9.0104CC	9.0104CD	
9.0104DA	9.0104DB	9.0104DC	9.0104DD	

EE 9.0104



MICRON COMMUNICATIONS, INC. INTEGRATED CIRCUIT DESIGN CONFIDENTIAL INFORMATION	REVISION: 1.03	DATE: J0700LE
	NEW: ADA Analog Bias	
	DESIGNER: J0700LE	DATE: 01/01/95
	DESIGNED BY: J0700LE	DATE: 01/01/95

9.02AA	9.02AB	9.02AC	9.02AD	9.02AE	9.02AF	9.02AG	9.02AH	9.02AI	9.02AJ	9.02AK
9.02BA	9.02BB	9.02BC	9.02BD	9.02BE	9.02BF	9.02BG	9.02BH	9.02BI	9.02BJ	9.02BK
9.02CA		9.02CC	9.02CD	9.02CE	9.02CF	9.02CG	9.02CH	9.02CI	9.02CJ	9.02CK
9.02DA	9.02DB	9.02DC	9.02DD		9.02DF	9.02DG	9.02DH	9.02DI	9.02DJ	9.02DK

11.11.11 07.11.11

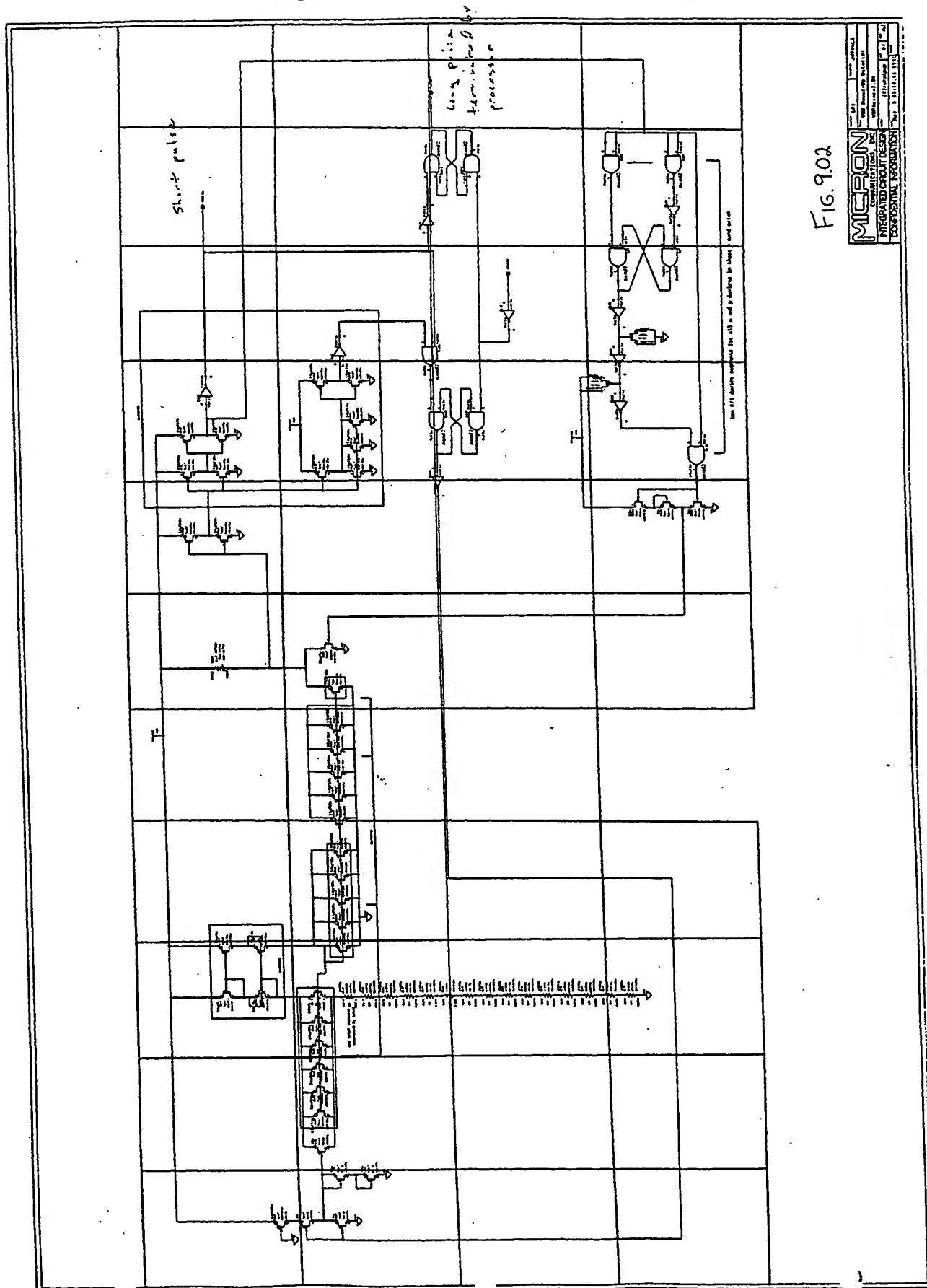
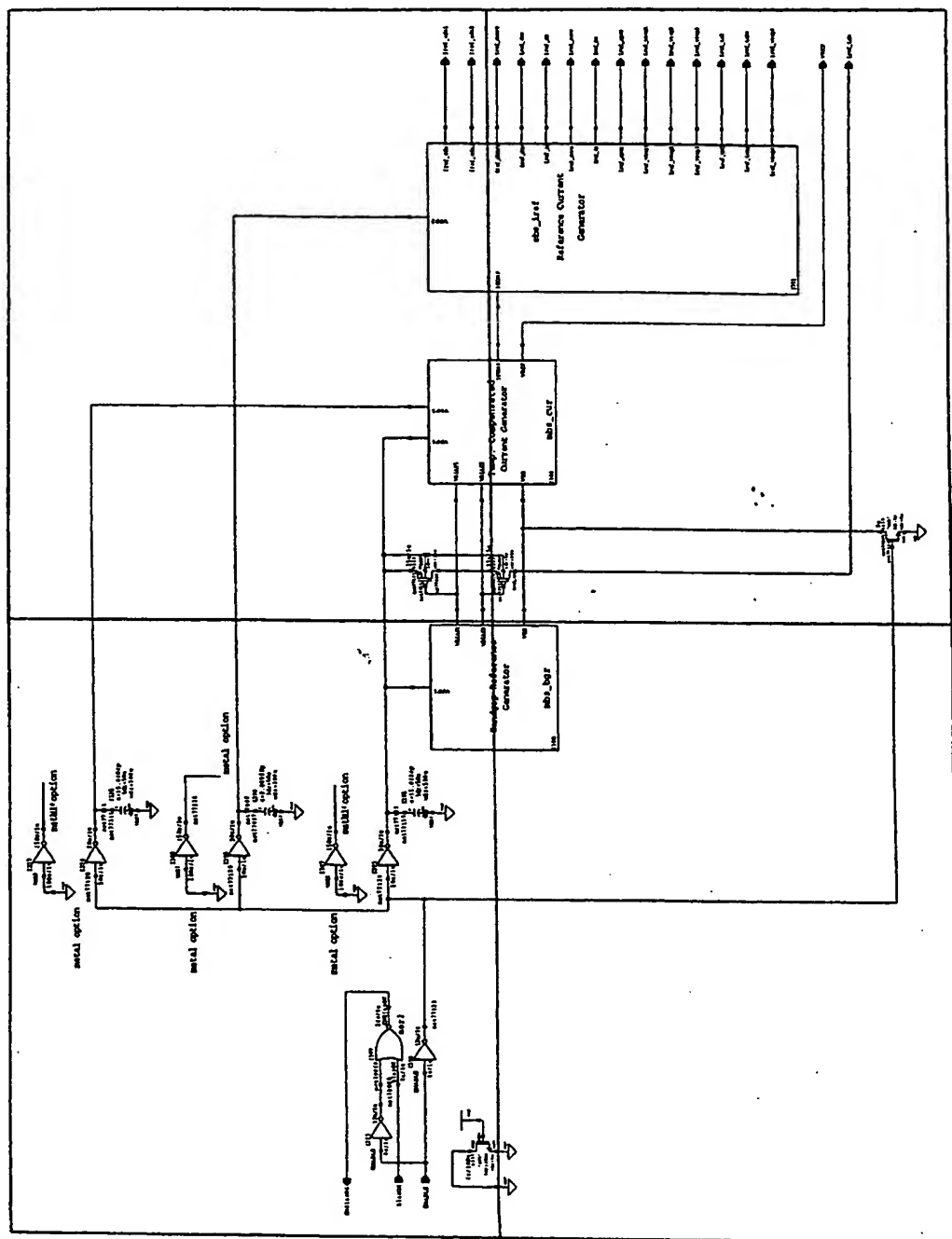


FIG. 9.02

9.03AB	9.03BB
9.03AA	9.03BA

9.03

FIG. 9.03



02: deleted TESTING function
added DBMSOK logic
created buffered VADP

MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN

PROJECT	103199a/mbe	83	10
DATE	Jul 28 11:00:13 1995		

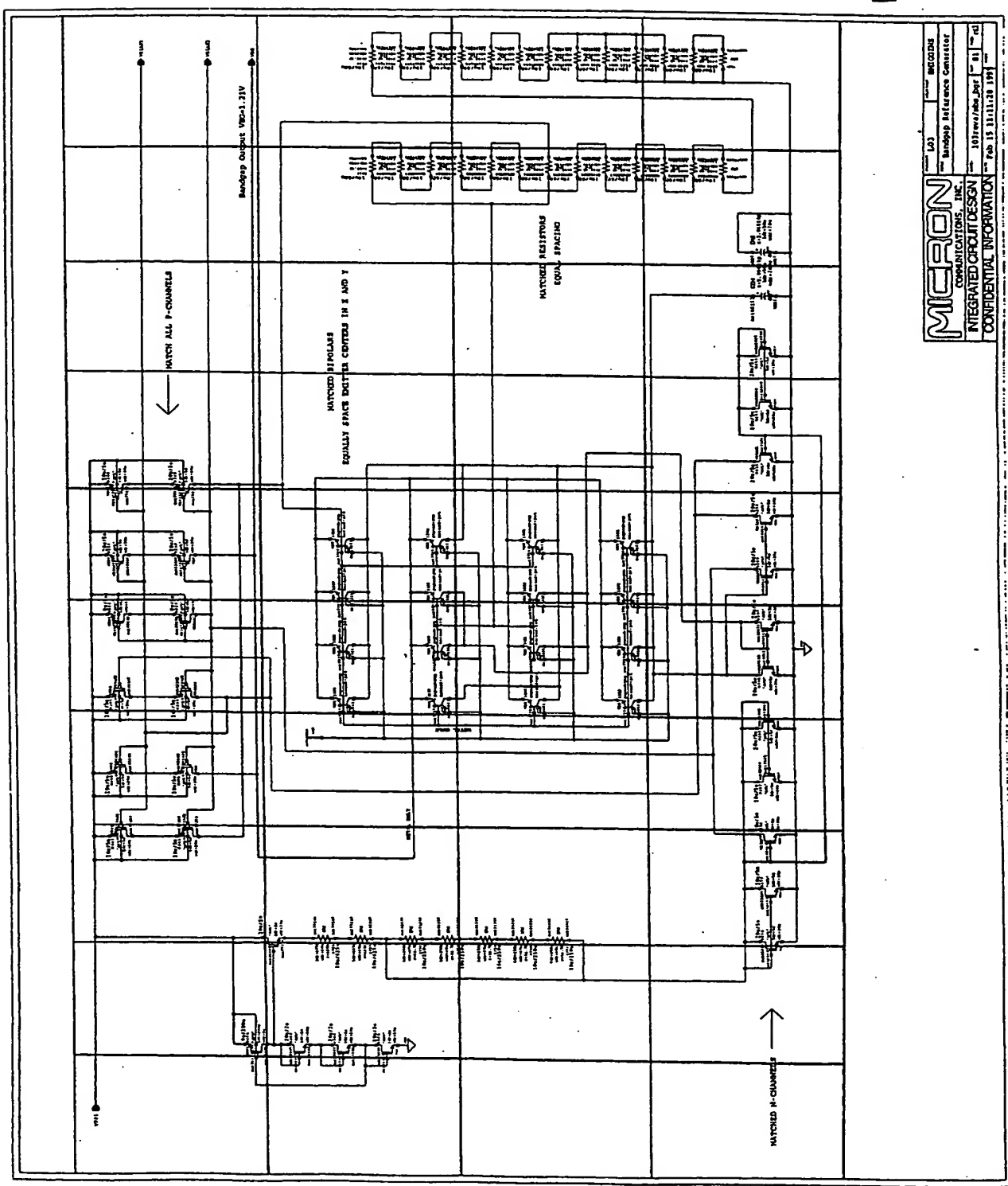
CONFIDENTIAL INFORMATION

CLASSIFICATION	TOP SECRET	REMARKS	ISSUING
Source		Master Bias Source	

9.0301AA	9.0301AB	9.0301AC	9.0301AD	9.0301AE	9.0301AF	9.0301AG	9.0301AH	9.0301AI	9.0301AJ
9.0301BA	9.0301BB	9.0301BC	9.0301BD	9.0301BE	9.0301BF	9.0301BG	9.0301BH	9.0301BI	9.0301BJ
	9.0301CB	9.0301CC	9.0301CD	9.0301CE	9.0301CF	9.0301CG	9.0301CH	9.0301CI	9.0301CJ
	9.0301DB	9.0301DC	9.0301DD	9.0301DE	9.0301DF	9.0301DG	9.0301DH	9.0301DI	9.0301DJ



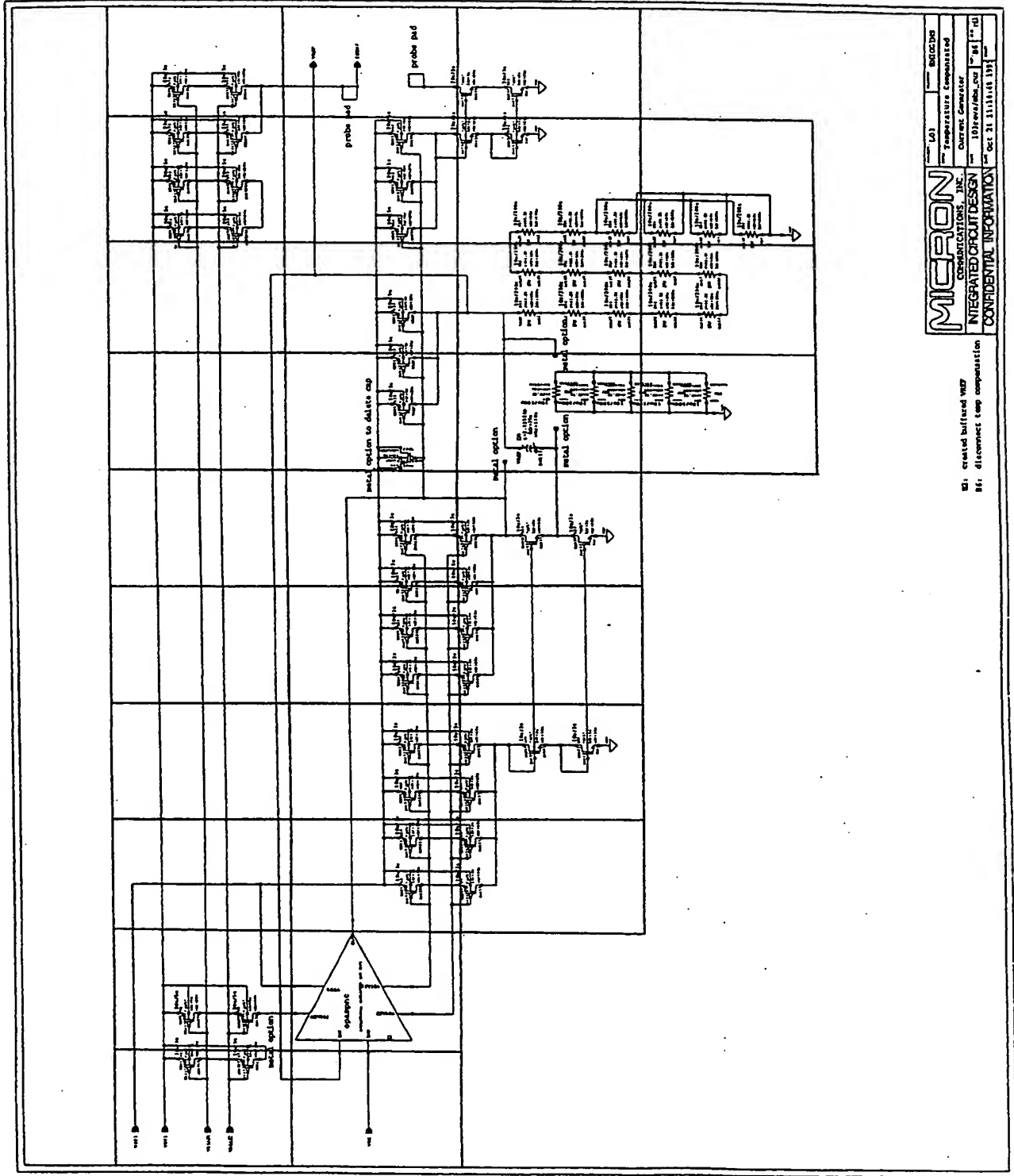

FIG. 9.0301



9.0302AA	9.0302AB	9.0302AC	9.0302AD	9.0302AE	9.0302AF	9.0302AG	9.0302AH	9.0302AI	9.0302AJ
9.0302BA	9.0302BB	9.0302BC	9.0302BD	9.0302BE	9.0302BF	9.0302BG	9.0302BH	9.0302BI	9.0302BJ
		9.0302CC	9.0302CD	9.0302CE	9.0302CF	9.0302CG	9.0302CH	9.0302CI	9.0302CJ
								9.0302DI	

EE 9.0302

FIG. 9.030a



MICRON	
TEMPERATURE COMPENSATED	Current Generator
INTEGRATED CIRCUIT DESIGN	10/27/84/mb_cw
CONFIDENTIAL INFORMATION	Oct 31 11:11:15 1984

Q1, created built and VSP
 Q1, disconnect temp compensation

9.0303AA	9.0303AB	9.0303AC	9.0303AD	9.0303AE	9.0303AF
9.0303BA	9.0303BB	9.0303BC	9.0303BD	9.0303BE	9.0303BF
	9.0303CB	9.0303CC	9.0303CD	9.0303CE	9.0303CF

II II III III III III

9.04AA	9.04AB	9.04AC	9.04AD	9.04AE
9.04BA	9.04BB	9.04BC	9.04BD	9.04BE
9.04CA	9.04CB	9.04CC	9.04CD	9.04CE

EX 9.04

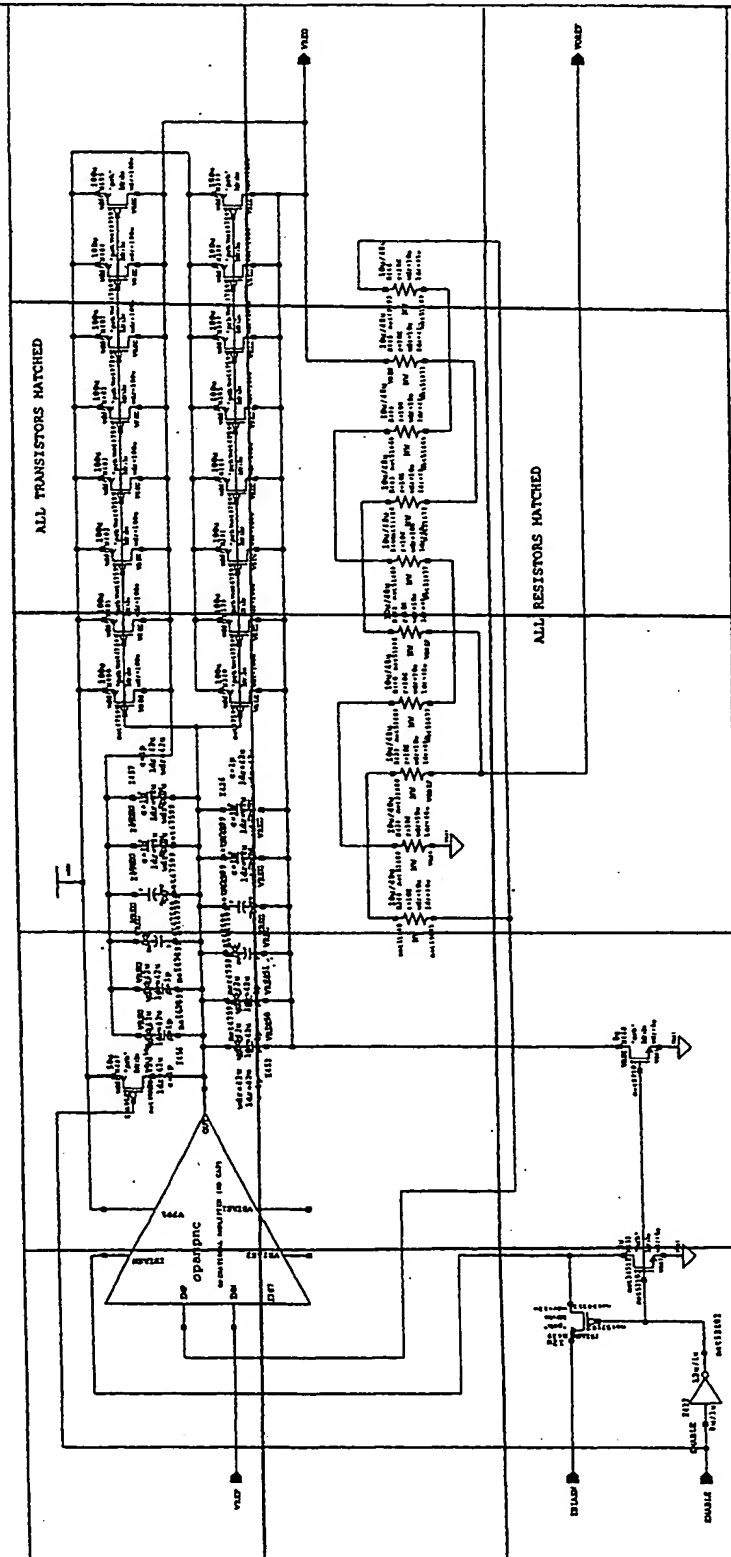


FIG. 9.04

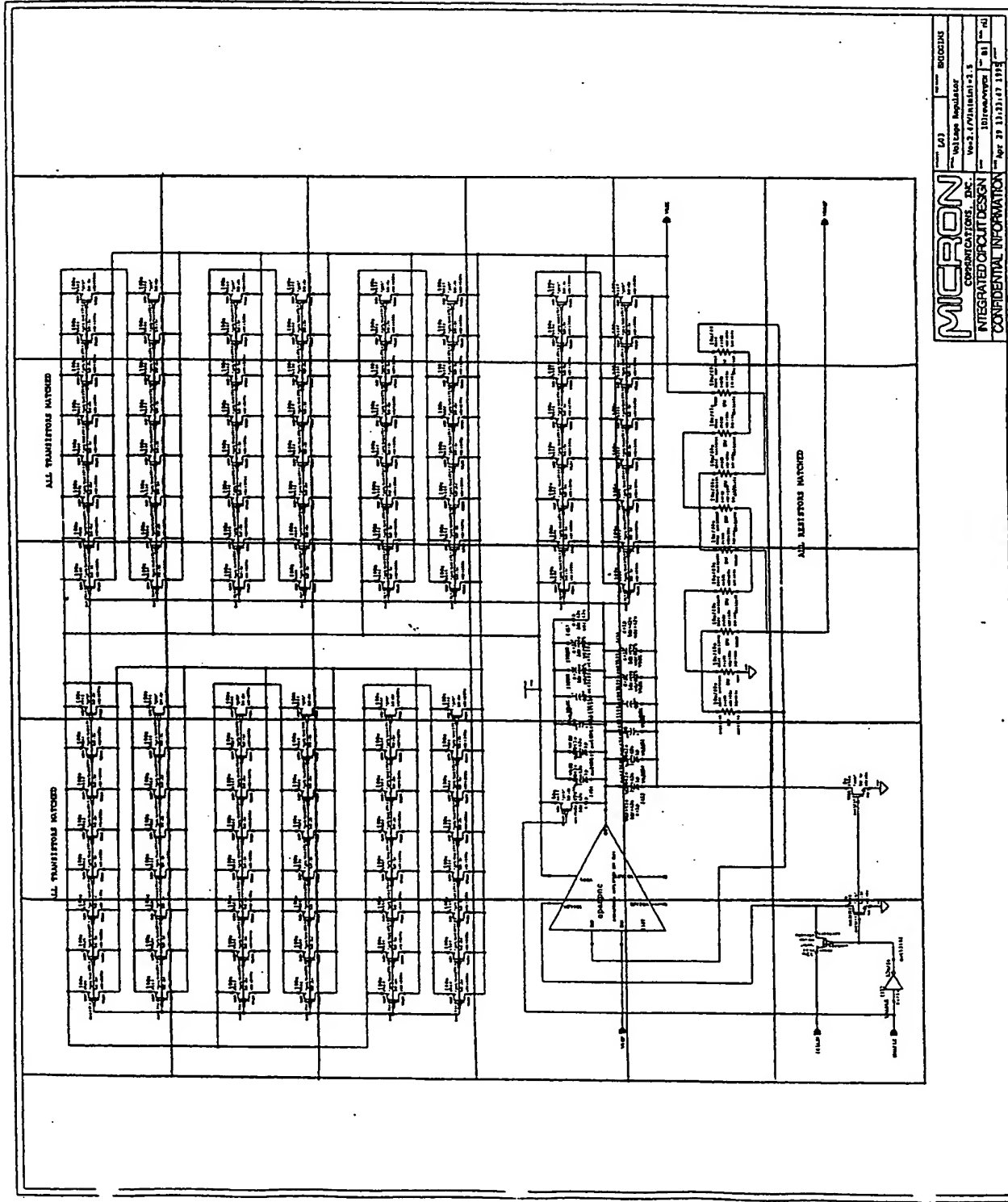
02 : 177

MICROCON, INC.		PRODUCT: L03		REVISION: B10G03INS	
COMMUNICATIONS, INC.		NAME: Voltage Regulator			
INTEGRATED CIRCUIT DESIGN		Vo = 2.4V (min) ± 2.5			
CONFIDENTIAL INFORMATION		DATE:	10/27revd/vrp	BY: B.	ML
CONFIDENTIAL INFORMATION		DATE:	Apr 29 13:22:24 1995		ML

9.05AA	9.05AB	9.05AC	9.05AD	9.05AE
9.05BA	9.05BB	9.05BC	9.05BD	9.05BE
9.05CA	9.05CB	9.05CC	9.05CD	9.05CE
9.05DA	9.05DB	9.05DC	9.05DD	9.05DE
9.05EA	9.05EB	9.05EC	9.05ED	9.05EE
9.05FA	9.05FB	9.05FC	9.05FD	9.05FE

II II 99.005

Fig. 9.05



MICRON
 CORPORATION, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

Doc ID: 101
 Voltage Regulator
 Ver 2.1/04/04/01-1.1
 10/04/04/01-1.1
 Apr 20 11:23:47 1994

9.0501AA	9.0501AB	9.0501AC	9.0501AD
9.0501BA	9.0501BB	9.0501BC	9.0501BD
9.0501CA	9.0501CB	9.0501CC	9.0501CD

IL 9.0501

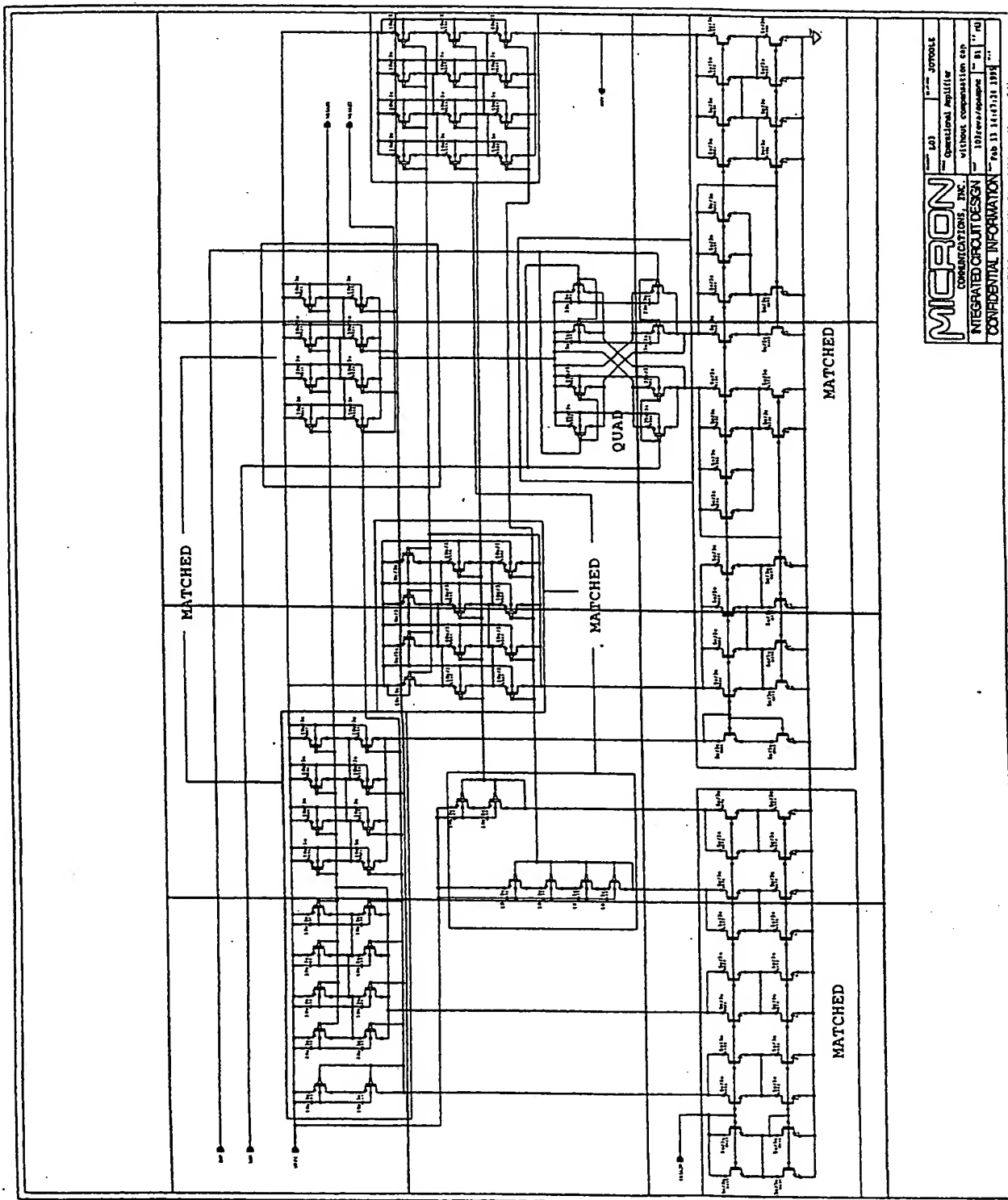


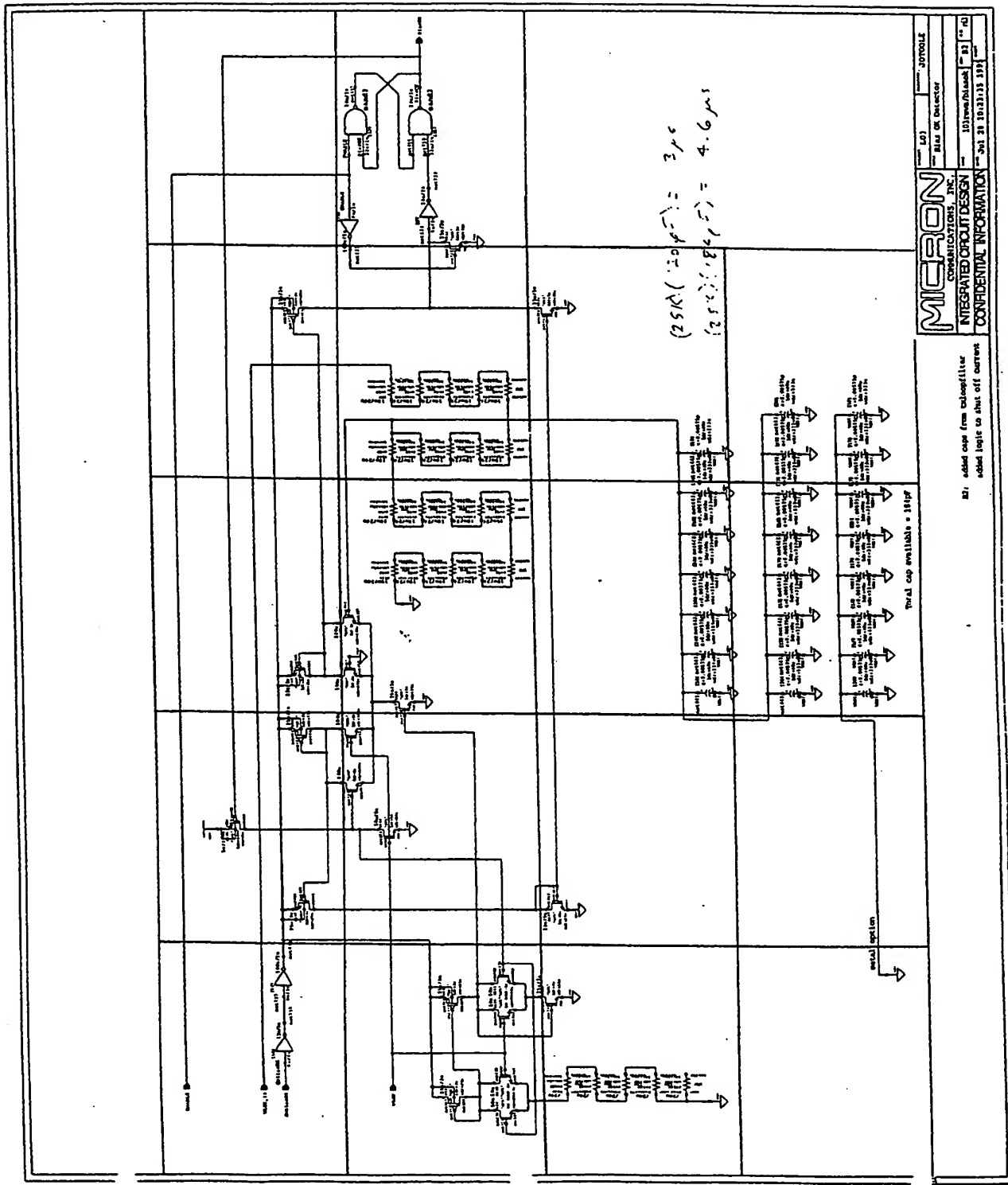
Fig. 9.0501

MICRON INTEGRATED CIRCUIT DESIGN CONFIDENTIAL INFORMATION	30VOLT Operational Amplifier
	without compensation cap
	10/10/68
	10/10/68

9.06AA	9.06AB	9.06AC	9.06AD	9.06AE
9.06BA	9.06BB	9.06BC	9.06BD	9.06BE
9.06CA	9.06CB	9.06CC	9.06CD	
9.06DA	9.06DB	9.06DC	9.06DD	

SECRET

FIG. 9.06



MICRON
 CORPORATION, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

81. added case from calogfilter
 added logic to shut off current

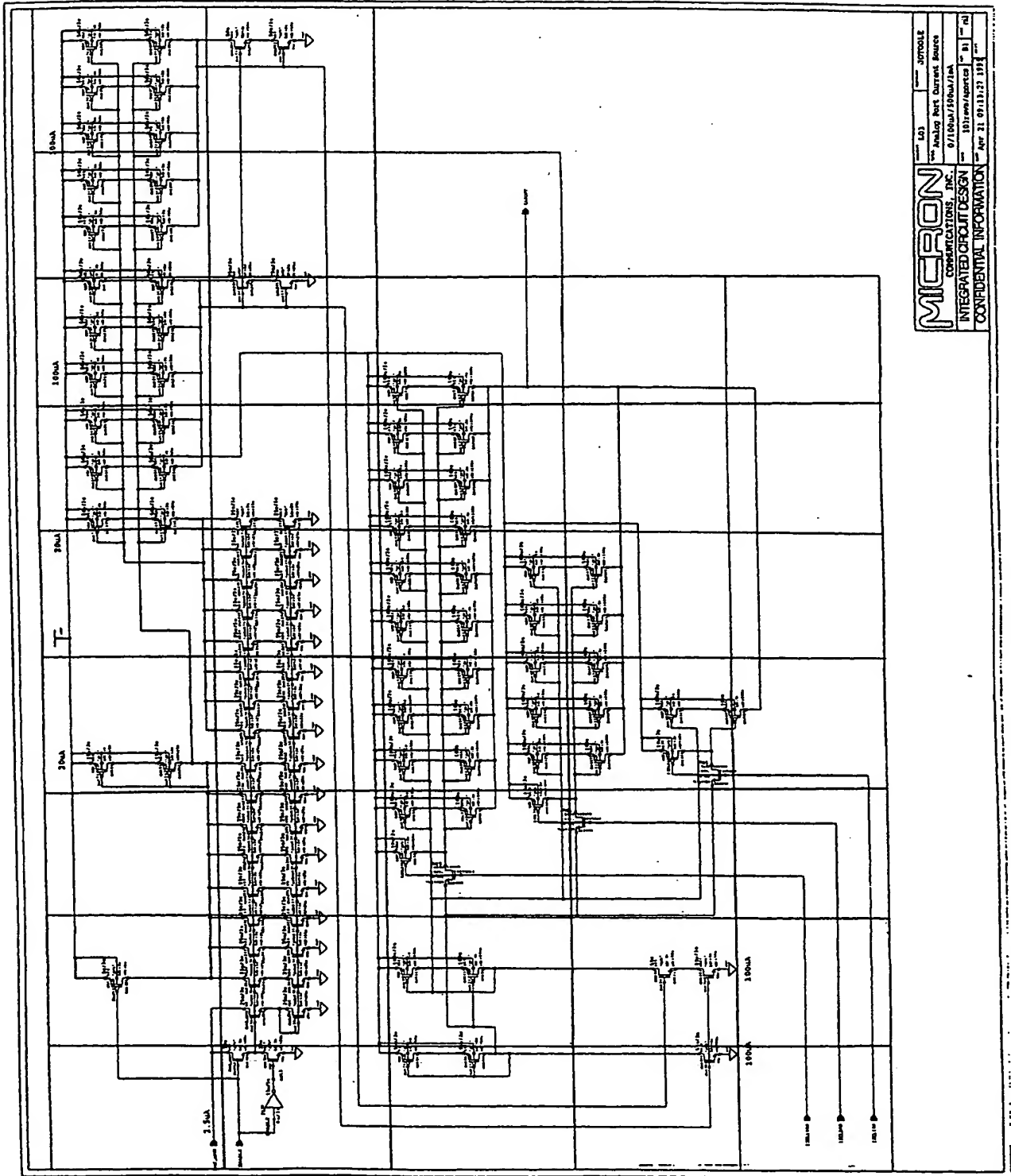
total cap available = 181pf

metal option

9.07AA	9.07AB	9.07AC	9.07AD	9.07AE	9.07AF	9.07AG	9.07AH	9.07AI
9.07BA	9.07BB	9.07BC	9.07BD	9.07BE	9.07BF	9.07BG	9.07BH	9.07BI
9.07CA	9.07CB	9.07CC	9.07CD	9.07CE	9.07CF	9.07CG	9.07CH	
9.07DA	9.07DB	9.07DC	9.07DD	9.07DE	9.07DF	9.07DG		
9.07EA	9.07EB	9.07EC	9.07ED	9.07EE	9.07EF	9.07EG		

SECRET

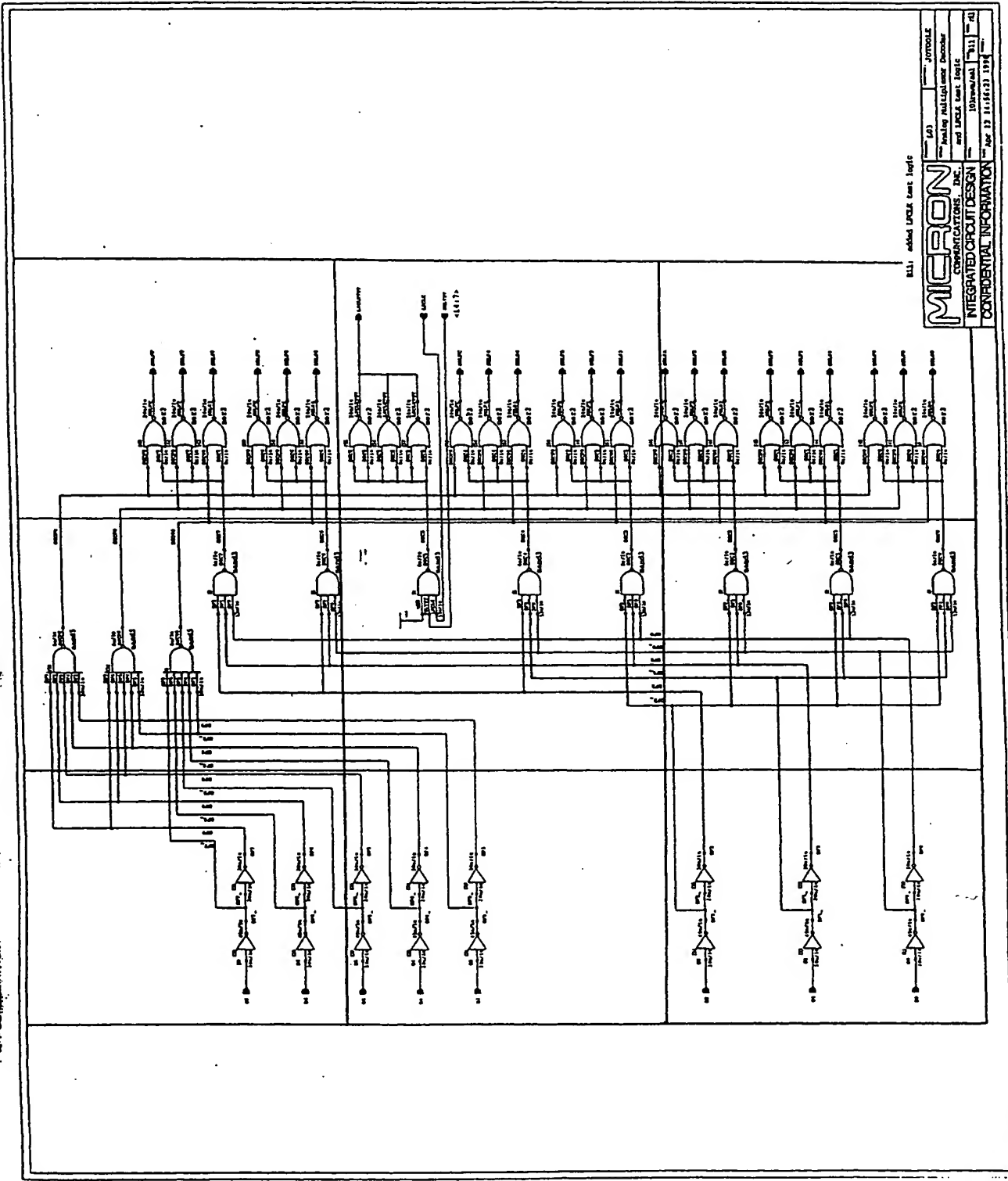
FIG. 9.07



9.08AA	9.08AB	9.08AC
9.08BA	9.08BB	9.08BC
9.08CA	9.08CB	9.08CC

II II 9.08B

F16.9.08



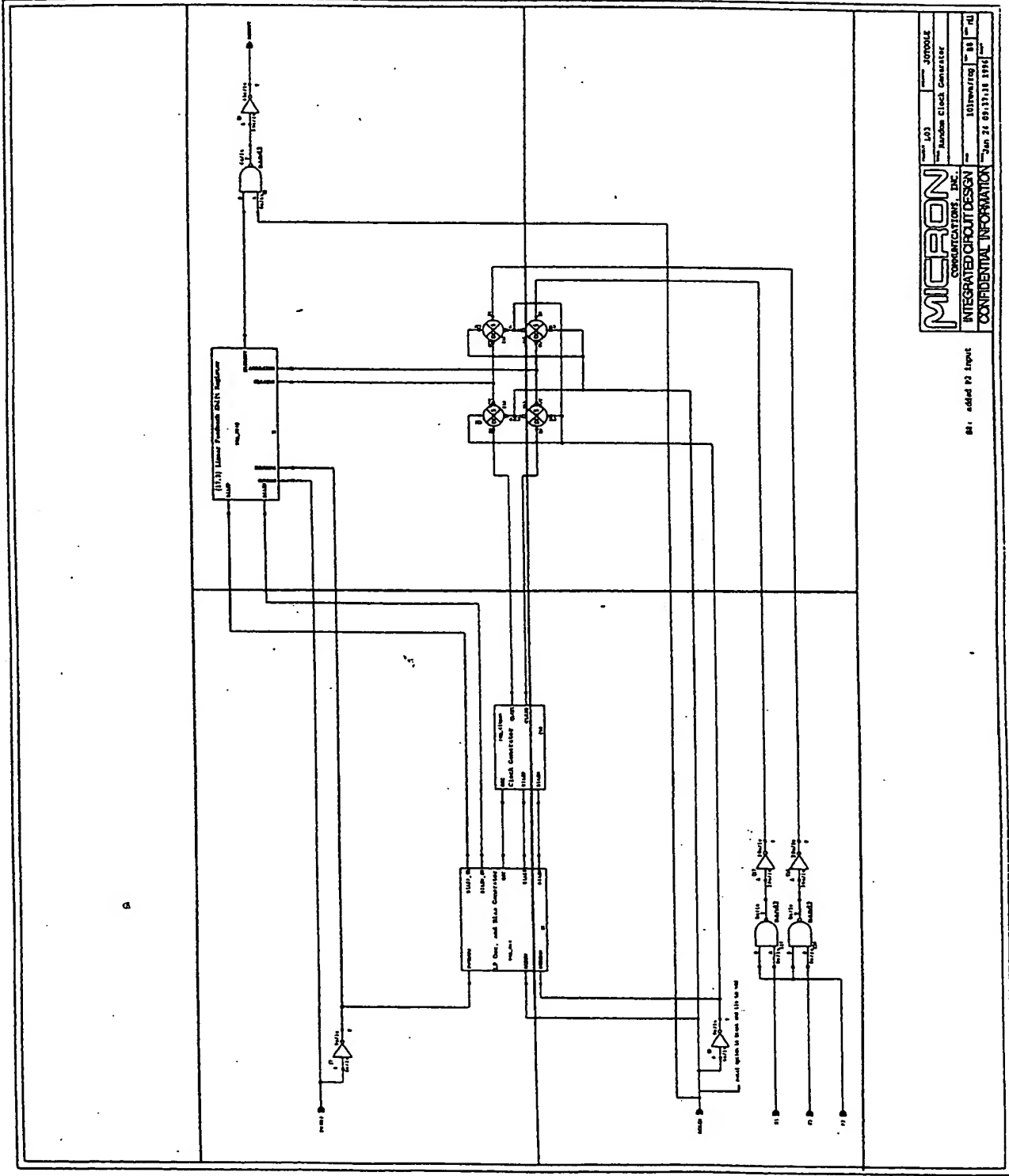
MICRON
CORPORATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

ELL1 added LMC848 test logic
J23 J20404
Timing Multiplier Decoder
and LMC848 test logic
10/26/84
Apr 13 11:56:23 1984

9.09AA	9.09AB
9.09BA	9.09BB

EX-9.09

Fig. 9.09



MICRON		Model 163	Part 1000014
MICRON TECHNOLOGY, INC.		Random Clock Generator	
INTEGRATED CIRCUIT DESIGN		101766707	81
CONFIDENTIAL INFORMATION		Jan 24 09:17:18 1976	70

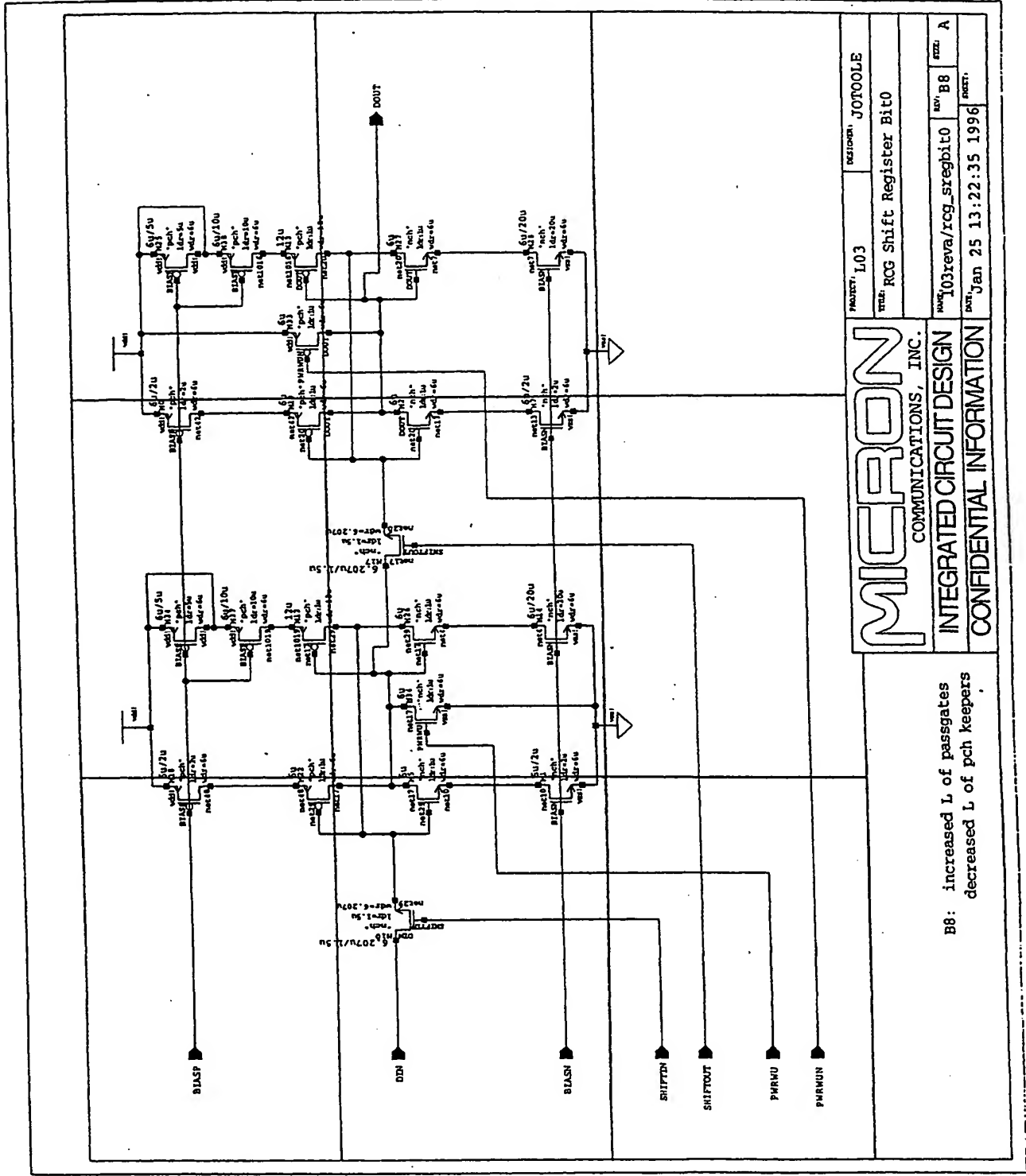
81: added 12 Input

9.0901AA	9.0901AB	9.0901AC	9.0901AD	9.0901AE	9.0901AF	9.0901AG	9.0901AH
9.0901BA	9.0901BB	9.0901BC	9.0901BD	9.0901BE	9.0901BF	9.0901BG	9.0901BH
9.0901CA	9.0901CB	9.0901CC	9.0901CD	9.0901CE	9.0901CF	9.0901CG	9.0901CH

II II 9.0901

9.090101AA	9.090101AB	9.090101AC
9.090101BA	9.090101BB	9.090101BC
9.090101CA	9.090101CB	9.090101CC

II II II II II II II II



MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

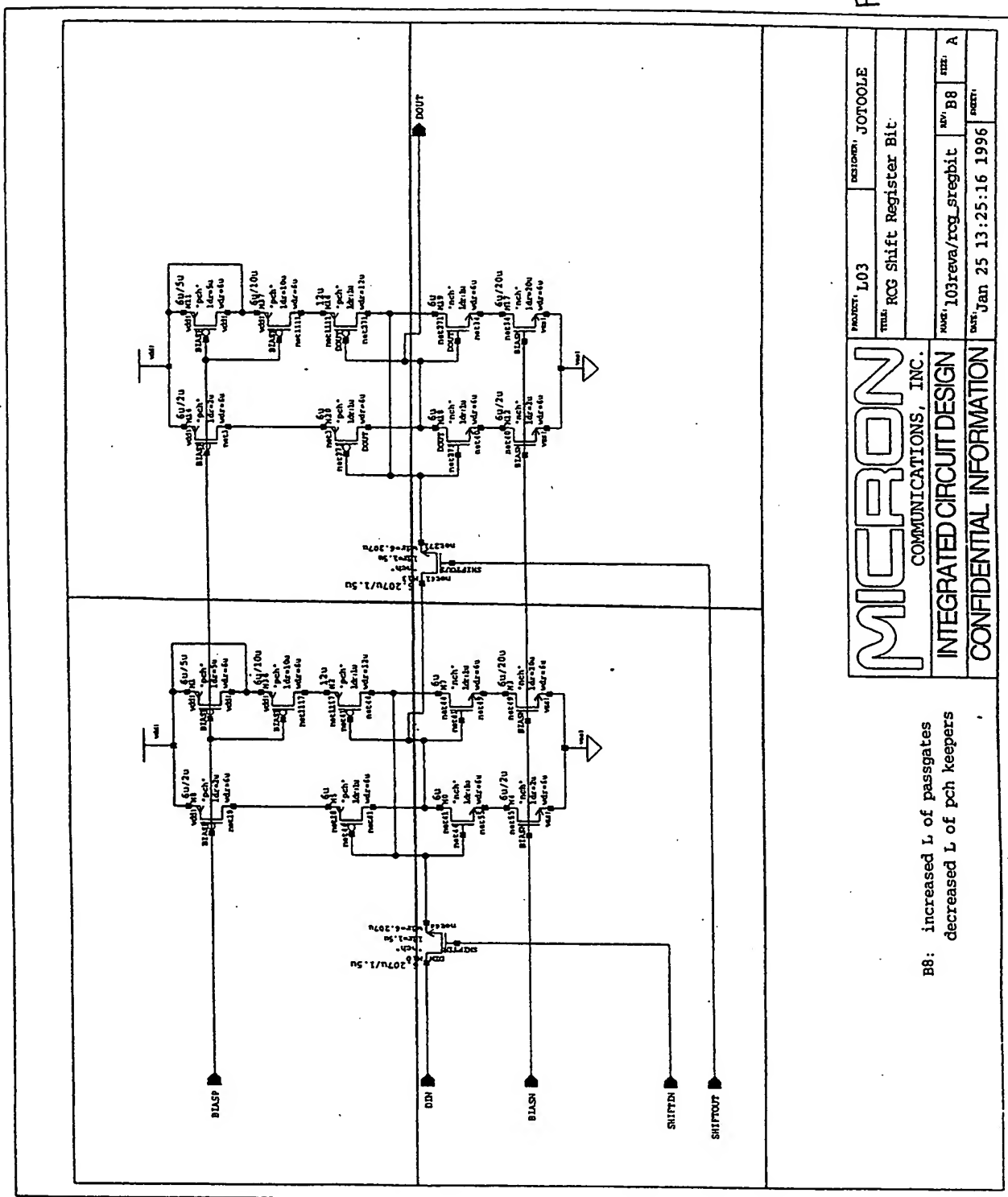
B8: increased L of passgates
 decreased L of pch keepers

PROJECT: L03
 J000001
 TITLE: RCG Shift Register Bit0
 NAME: j03:eva/rcg_sregbit0
 REV: B8
 SIZE: A
 DATE: Jan 25 13:22:35 1996
 DESIGNED BY: J000001

FIG. 9.090101

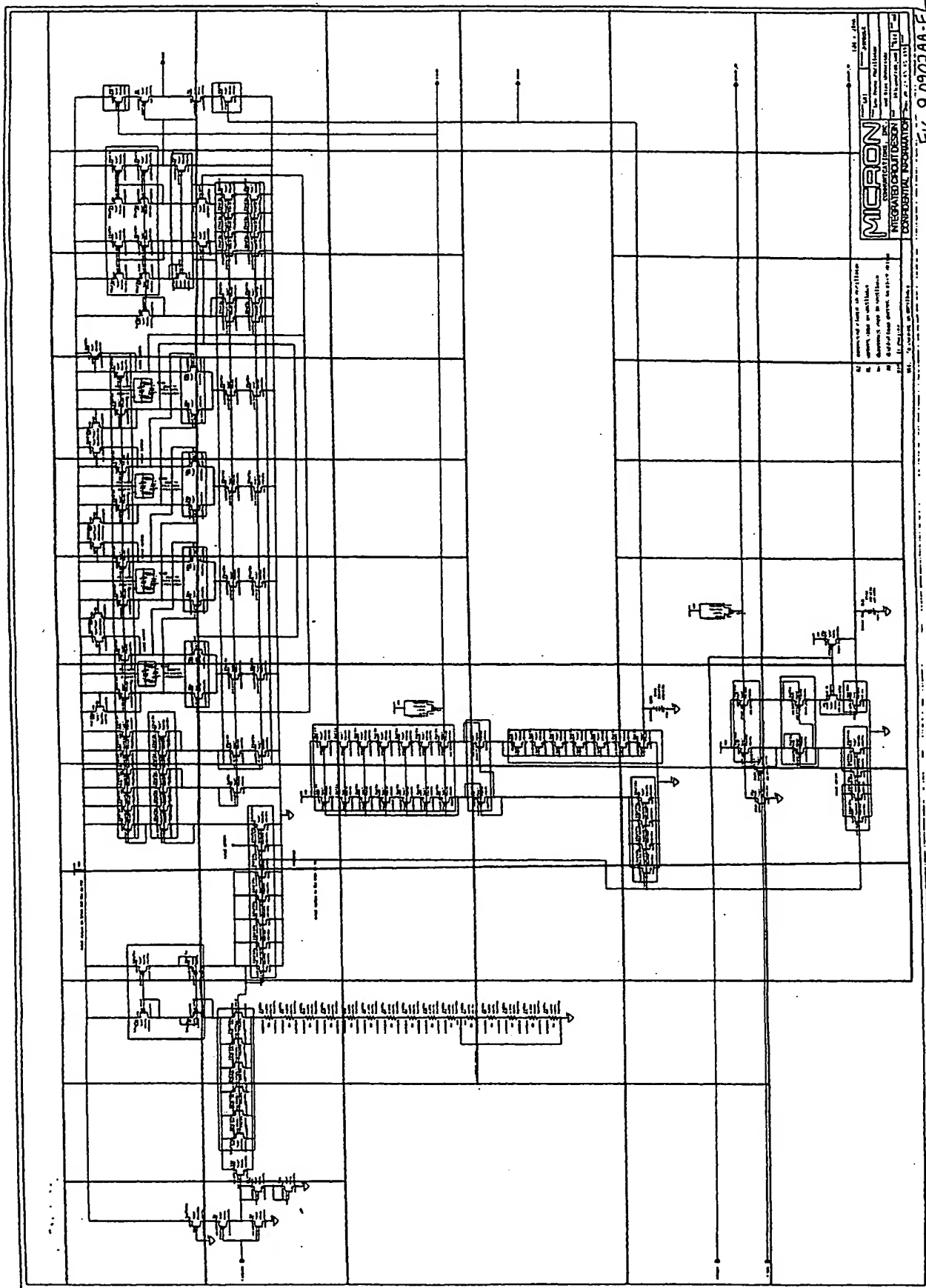
9.090102AA	9.090102AB
9.090102BA	9.090102BB

EE 9.090102



9.0902AA	9.0902AB	9.0902AC	9.0902AD	9.0902AE	9.0902F	9.0902AG	9.0902AH	9.0902AI	9.0902AJ	9.0902AK	9.0902AL
9.0902BA	9.0902BB	9.0902BC	9.0902BD	9.0902BE	9.0902F	9.0902BG	9.0902BH	9.0902BI	9.0902BJ	9.0902BK	9.0902BL
		9.0902CC	9.0902CD	9.0902CE	9.0902F	9.0902CG	9.0902CH	9.0902CI	9.0902CJ	9.0902CK	9.0902CL
		9.0902DC	9.0902DD	9.0902DE	9.0902F						9.0902DL
9.0902EA	9.0902EB	9.0902EC	9.0902ED	9.0902EE	9.0902F	9.0902EG	9.0902EH	9.0902EI	9.0902EJ	9.0902EK	9.0902EL
			9.0902FD	9.0902FE	9.0902F	9.0902FG	9.0902FH	9.0902FI	9.0902FJ	9.0902FK	9.0902FL

II II 9.0902



MICRON
 CORPORATION
 10000 N. 10th Ave.
 Denver, Colorado 80231
 (303) 751-1000

1. All components are in accordance with the specifications of the manufacturer.
 2. All components are in accordance with the specifications of the manufacturer.
 3. All components are in accordance with the specifications of the manufacturer.

Fig. 9.0902AA-F

9.0903AA	9.0903AB	9.0903AC
9.0903BA	9.0903BB	9.0903BC
9.0903CA	9.0903CB	9.0903CC

EX 9.0903

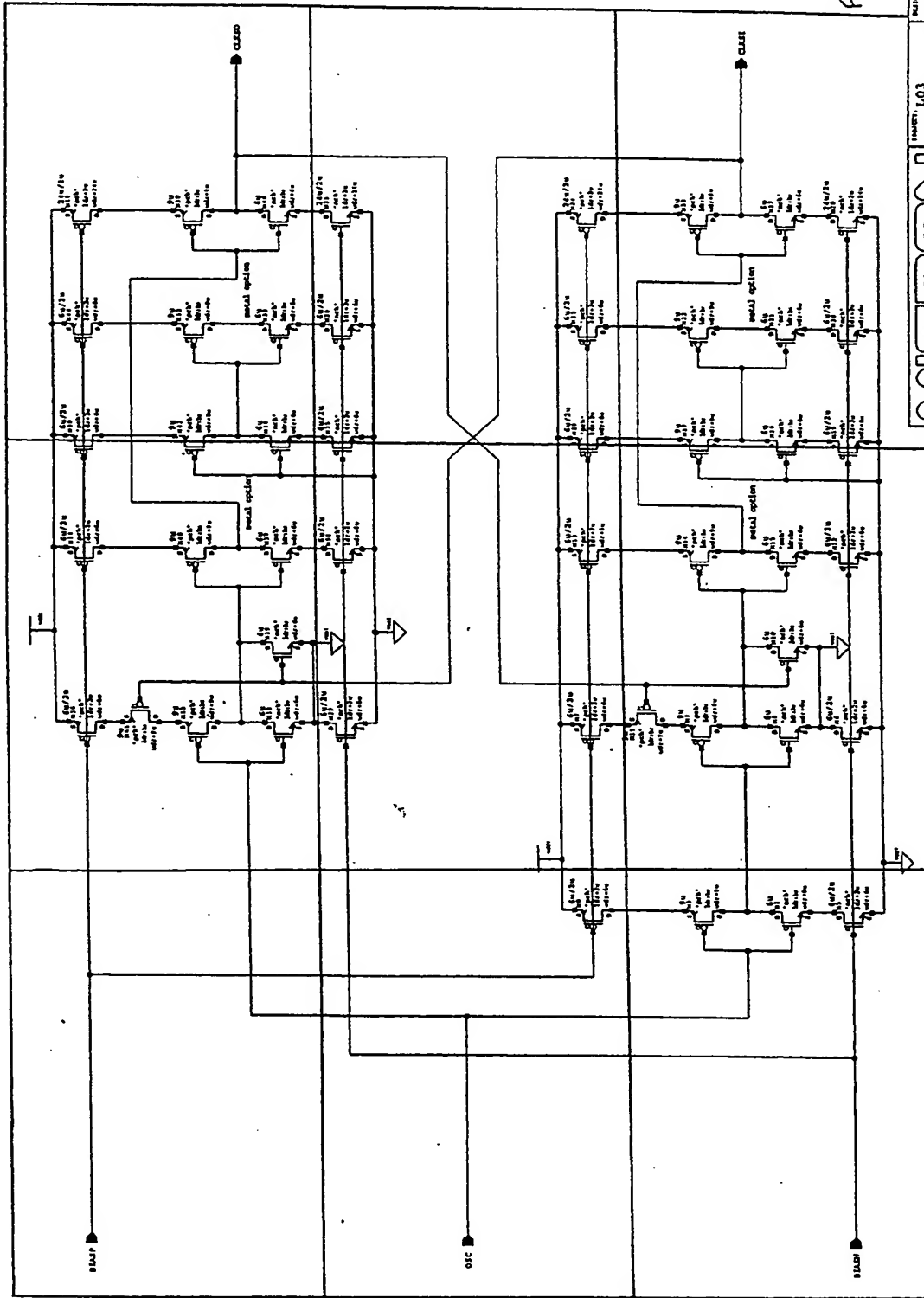


Fig. 9.0903

MICRON
COMMUNICATIONS, INC.

REVISION: L03
NAME: Clock Generator
J07000LE

INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

88: wired cross-couples to ground

DATE: 103revA/rcg.clkgen
REV: B8
JAN 24 09:56:43 1996

10AA	10AB	10AC	10AD
10BA	10BB	10BC	10BD
10CA	10CB	10CC	10CD
10DA	10DB	10DC	10DD

11.11.11

10.01AA	10.01AB	10.01AC	10.01AD	10.01AE	10.01AF	10.01AG			
10.01BA	10.01BB	10.01BC	10.01BD	10.01BE	10.01BF	10.01BG	10.01BH	10.01BI	10.01BJ
10.01CA	10.01CB	10.01CC	10.01CD	10.01CE	10.01CF	10.01CG	10.01CH	10.01CI	10.01CJ
	10.01DB	10.01DC	10.01DD	10.01DE	10.01DF	10.01DG	10.01DH	10.01DI	10.01DJ

II II II II II

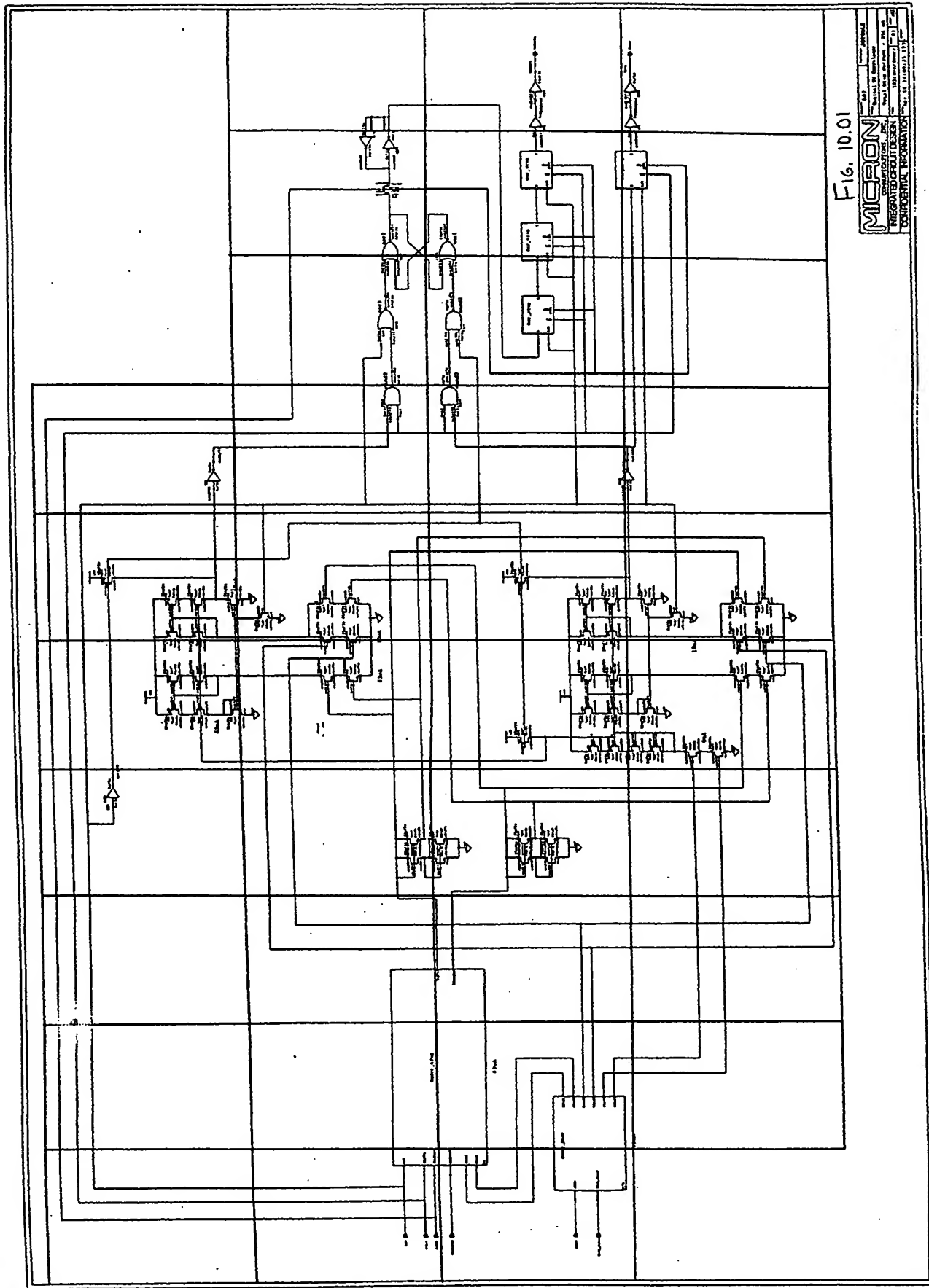


Fig. 10.01

10.0101AA	10.0101AB	10.0101AC	10.0101AD	10.0101AE	10.0101AF	10.0101AG
10.0101BA	10.0101BB	10.0101BC	10.0101BD	10.0101BE	10.0101BF	10.0101BG

IL IL IL IL IL IL IL IL

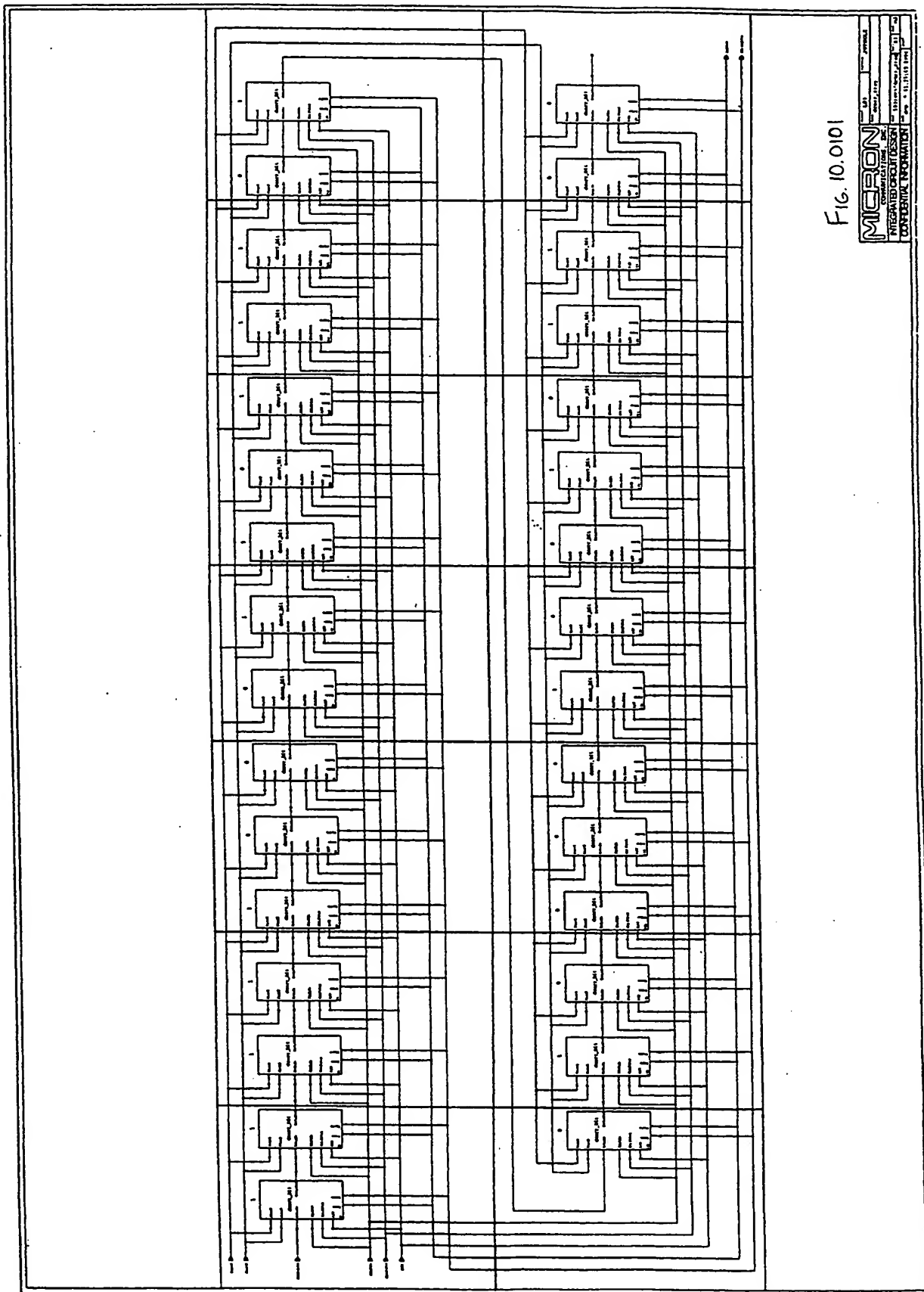


Fig. 10.0101

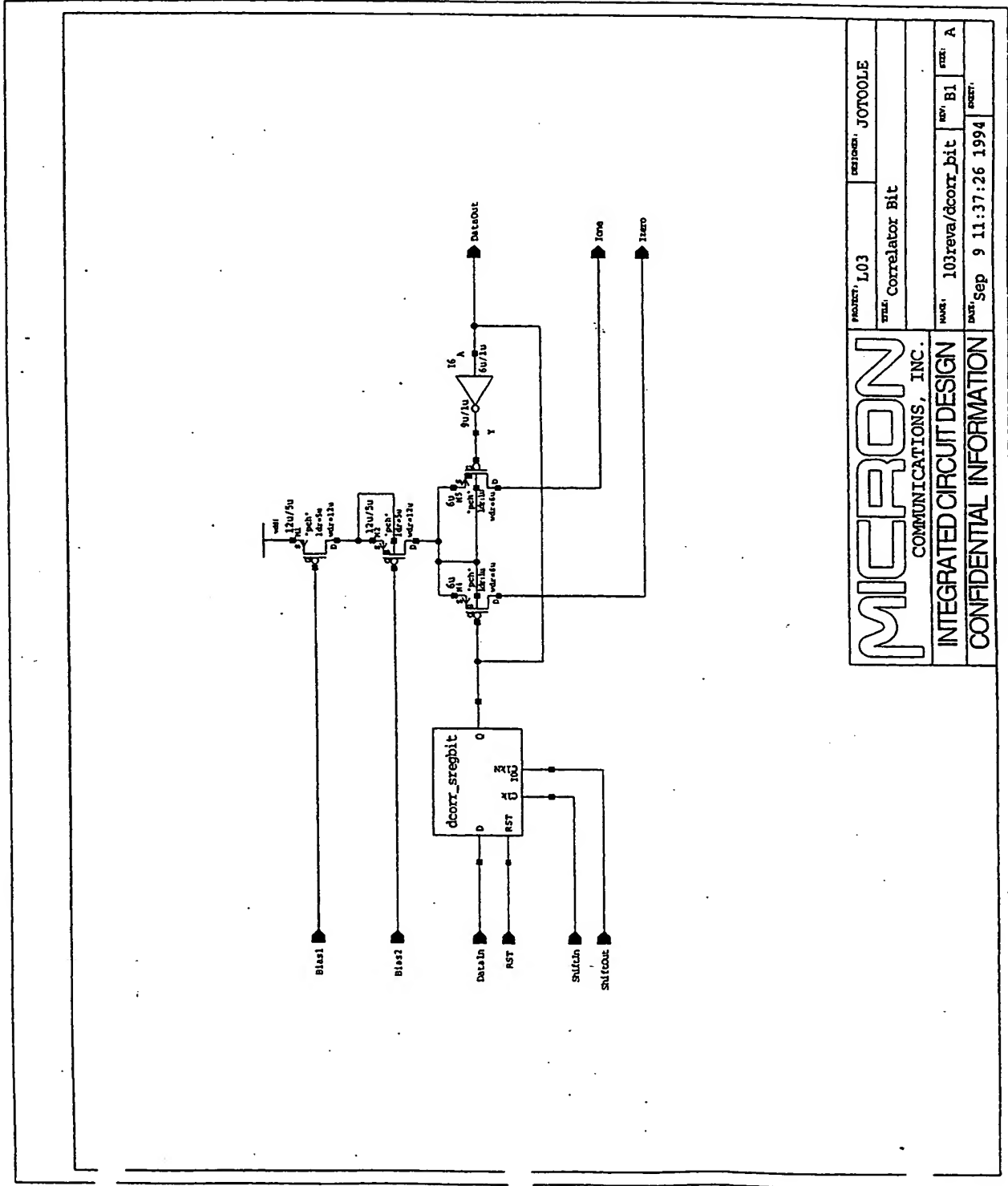


FIG. 10.010101

MICRON		PROJECT: L03	DESIGNED: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Correlator Bit	
INTEGRATED CIRCUIT DESIGN		DATE: 10/26/94	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 9 11:37:26 1994	REV: A

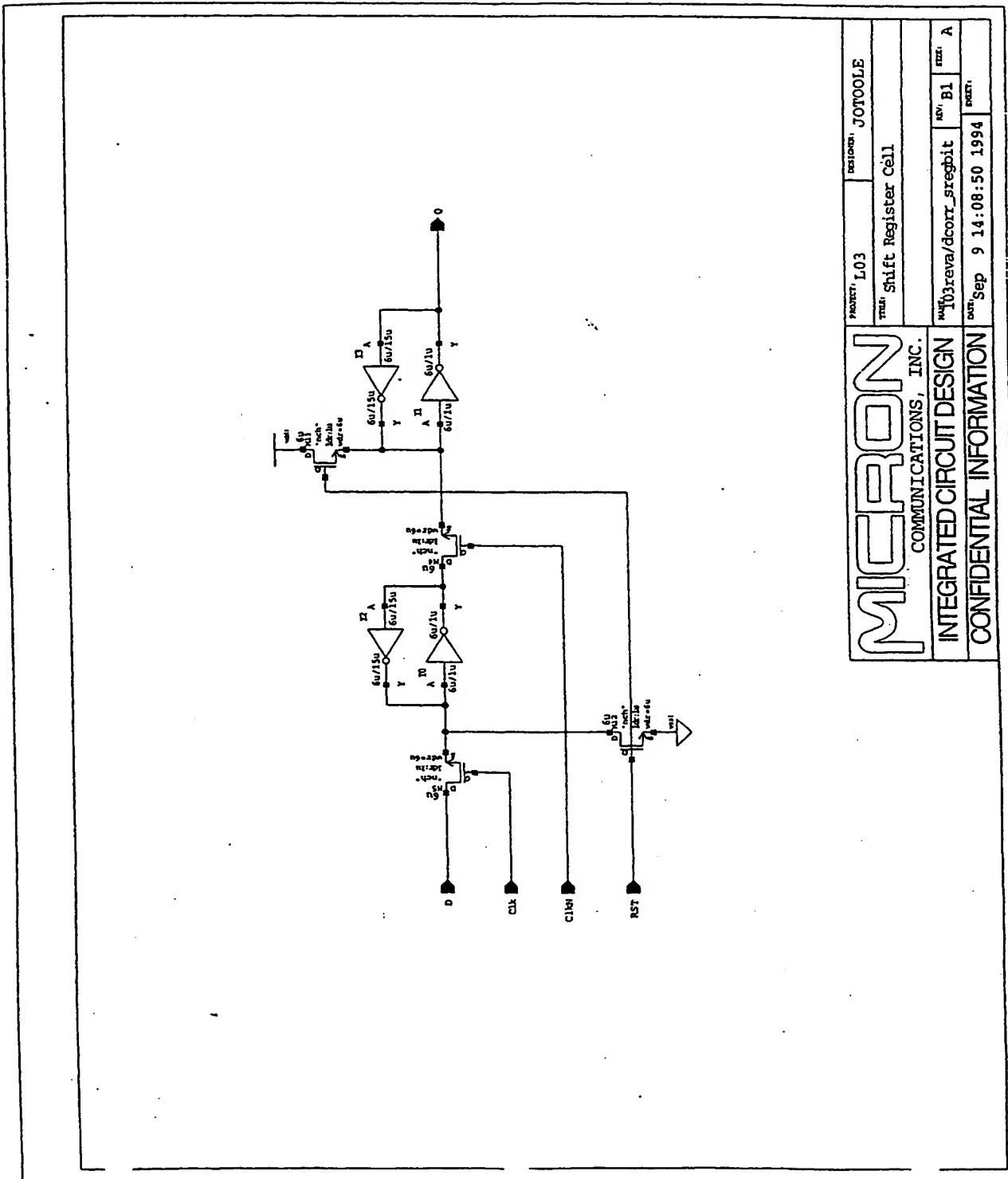


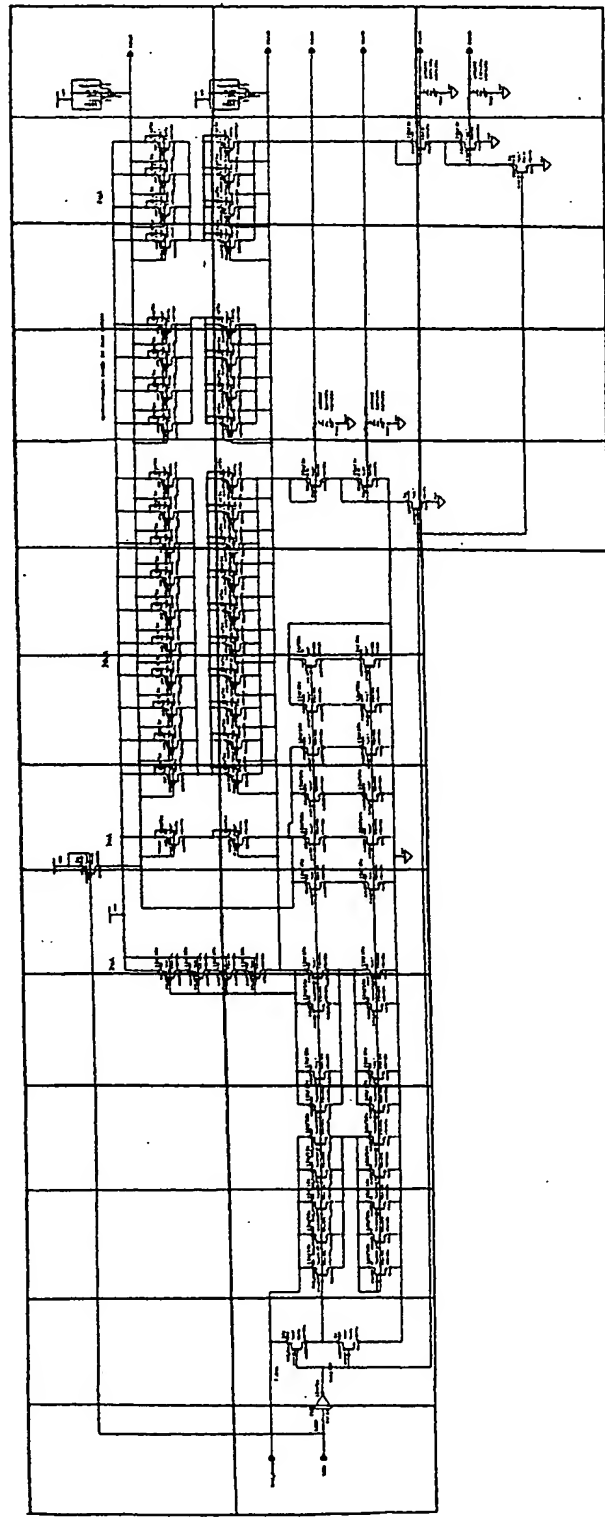
FIG. 10.01010101

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Shift Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: J33reva/dcorr_sregbit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 9 14:08:50 1994	SIZE: A

10.0102AA	10.0102AB	10.0102AC	10.0102AD	10.0102AE	10.0102AF	10.0102AG	10.0102AH	10.0101AI	10.0102AJ	10.0102AK	10.0102AL	10.0102AM	10.0102AN
10.0102BA	10.0102BB	10.0102BC	10.0102BD	10.0102BE	10.0102BF	10.0102BG	10.0102BH	10.0101AI	10.0102BJ	10.0102BK	10.0102BL	10.0102BM	10.0102BN
									10.0102CJ	10.0102CK	10.0102CL	10.0102CM	10.0102CN

10.000 10.000

Fig. 10, 0102



10.02AA	10.02AB	10.02AC	10.02AD	10.02AE
10.02BA	10.02BB	10.02BC	10.02BD	10.02BE

EE 10.02

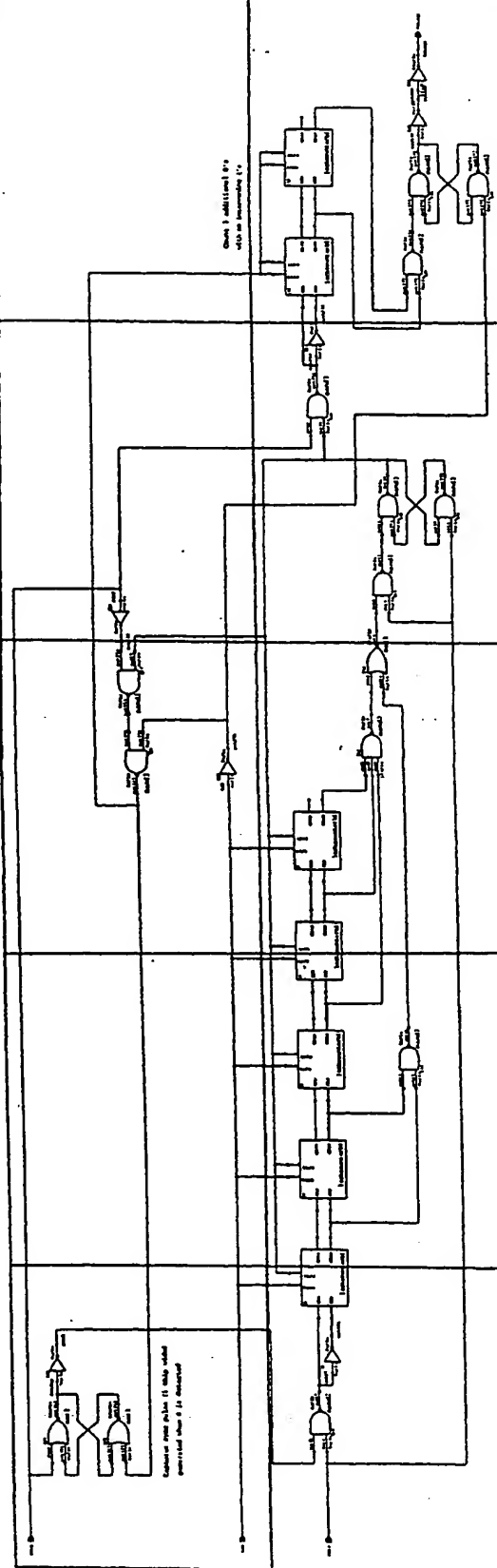
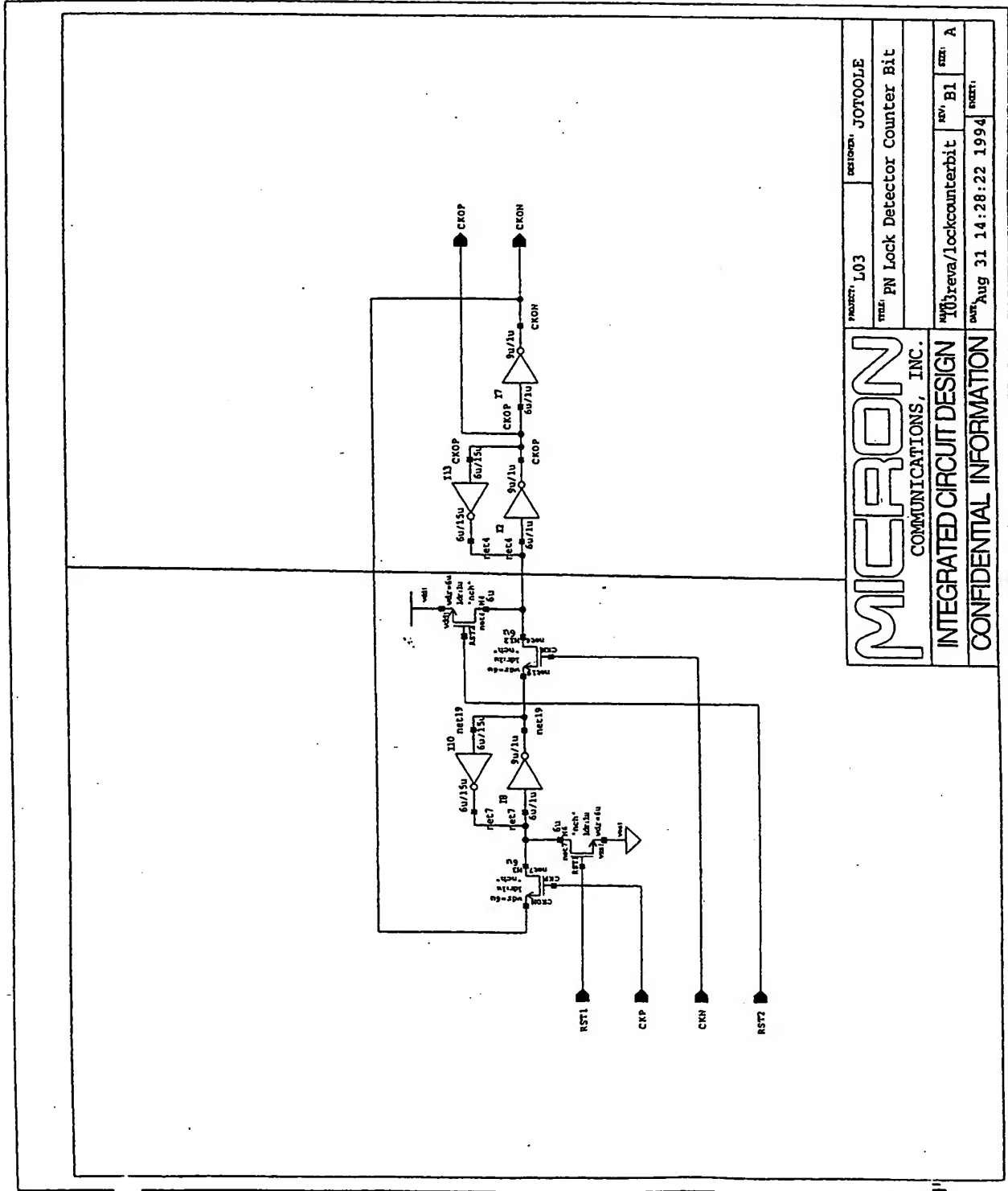


Fig. 10.02

И И 10.02.01



MICRON
COMMUNICATIONS, INC.

PROJECT: L03
TITLE: PN Lock Detector Counter Bit

DESIGNER: JOTOOLE
REV: B1
DATE: Aug 31 14:28:22 1994

INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

FIG. 10.0201

10.03AA	10.03AB
---------	---------

EX-100

10.04AA	10.04AB	10.04AC	10.04AD	10.04AE
10.04BA	10.04BB	10.04BC	10.04BD	10.04BE
10.04CA	10.04CB	10.04CC	10.04CD	10.04CE

И. И. И.

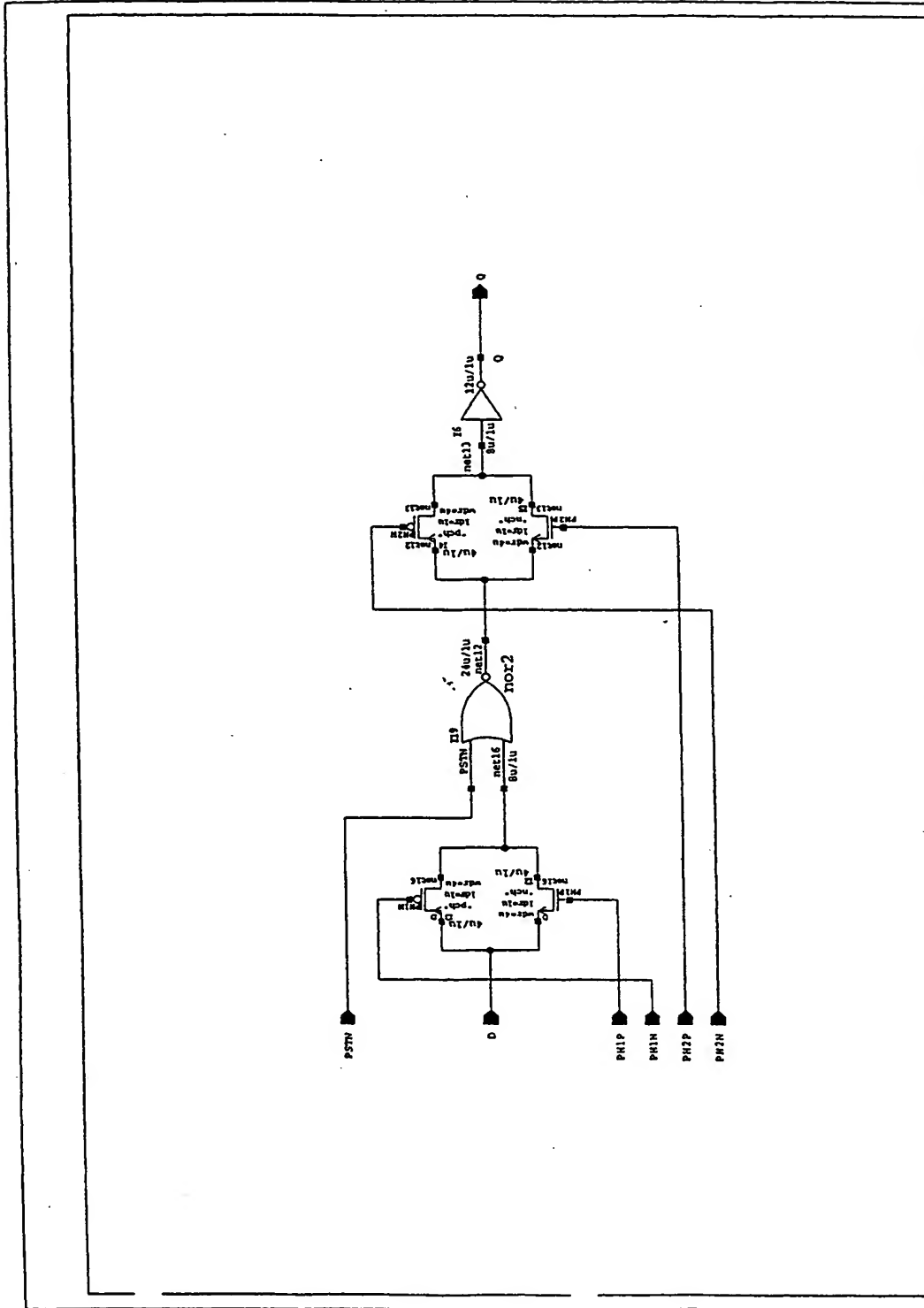


Fig. 10.0401

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: PN Generator Shift Register Cell		REV: -	
NAME: 103reva/pnsgreg		SIZE: A	
DATE: Nov 20 21:22:37 1993		SHEET: 1	

MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

10.0402AA	10.0402AB
10.0402BA	10.0402BB
10.0402CA	10.0402CB

EX-100402

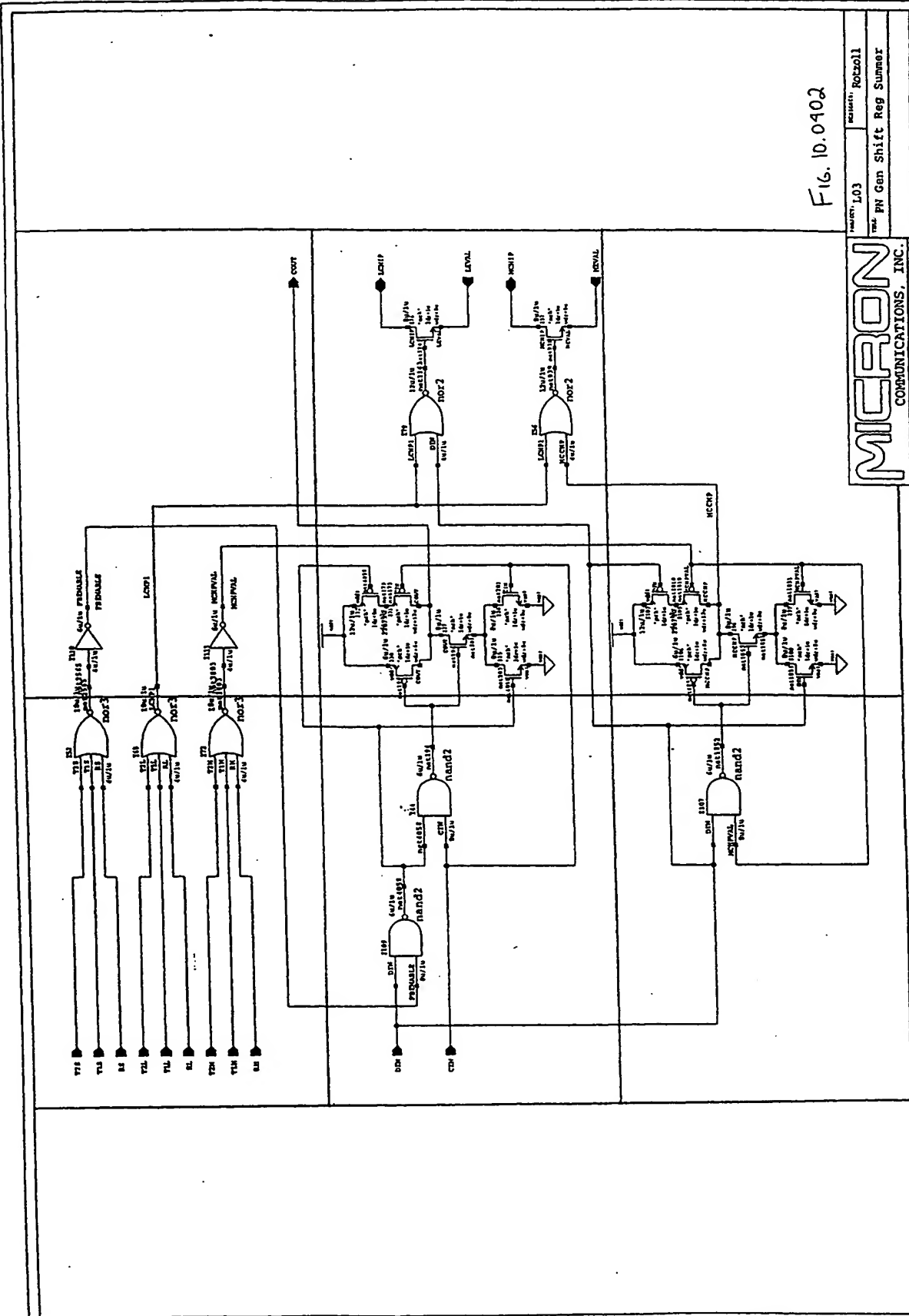
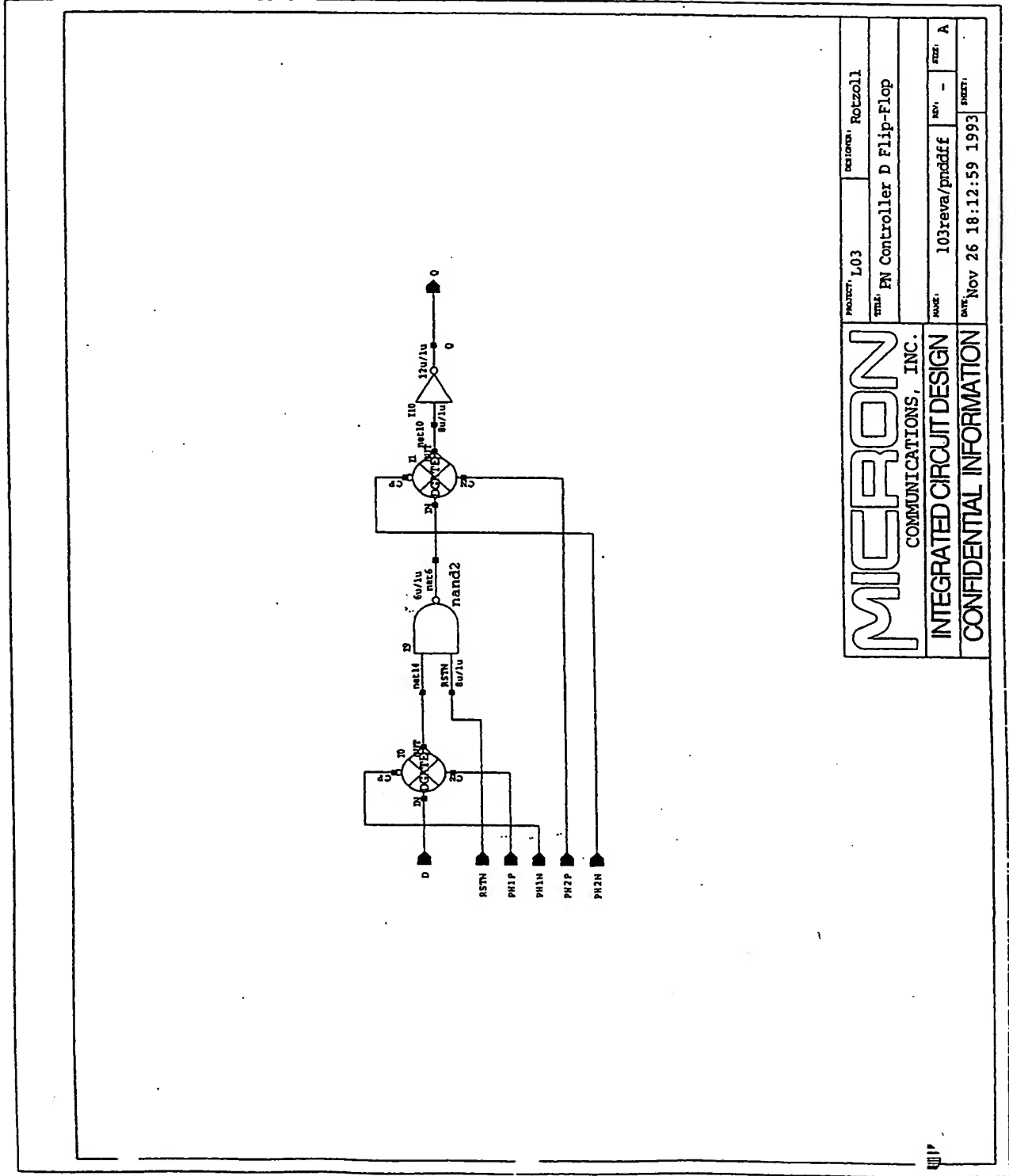


FIG. 10.0402

MICRON		PROPERTY: 103	REVISION: 1030011
COMMUNICATIONS, INC.		TITLE: PN Gen Shift Reg Summer	
INTEGRATED CIRCUIT DESIGN		DATE: 103revA/pngssum	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 20 21:23:00 1993	BY: nll



MICRON		PROJECT: L03		DESIGNER: Rotzoll	
		TITLE: FN Controller D Flip-Flop			
		NAME: 103reva/pnddfff		REV: -	
				SIZE: A	
		DATE: Nov 26 18:12:59 1993		SHEET: 1	

MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

Fig. 10.05

10.06AA	10.06AB	10.06AC	10.06AD								
10.06BA	10.06BB	10.06BC	10.06BD	10.06BE	10.06BF	10.06BG	10.06BH	10.06BI	10.06BJ	10.06BK	
10.06CA	10.06CB	10.06CC	10.06CD	10.06CE	10.06CF	10.06CG	10.06CH	10.06CI	10.06CJ	10.06CK	
10.06DA	10.06DB	10.06DC	10.06DD	10.06DE	10.06DF	10.06DG	10.06DH				

10.005
II II

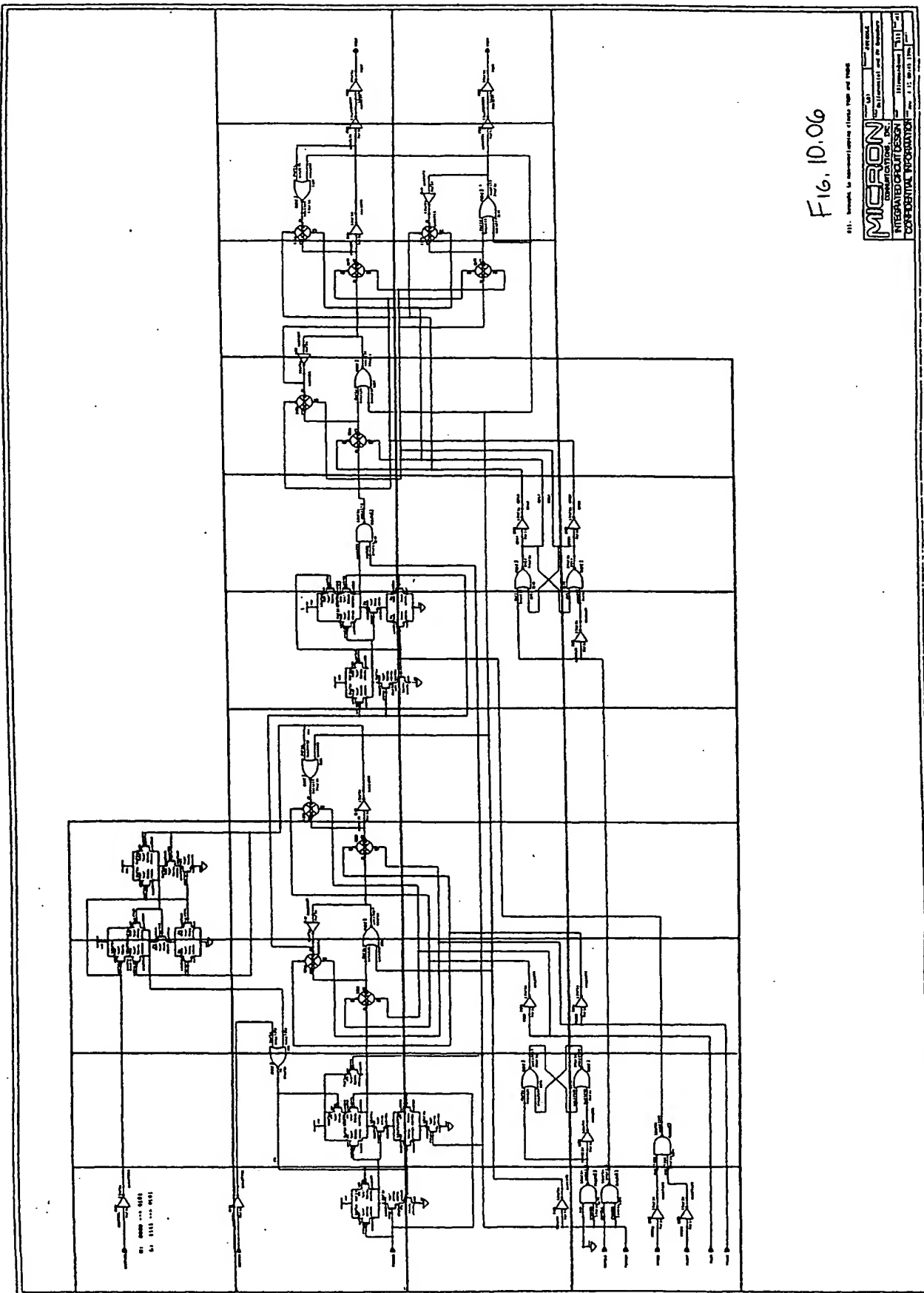


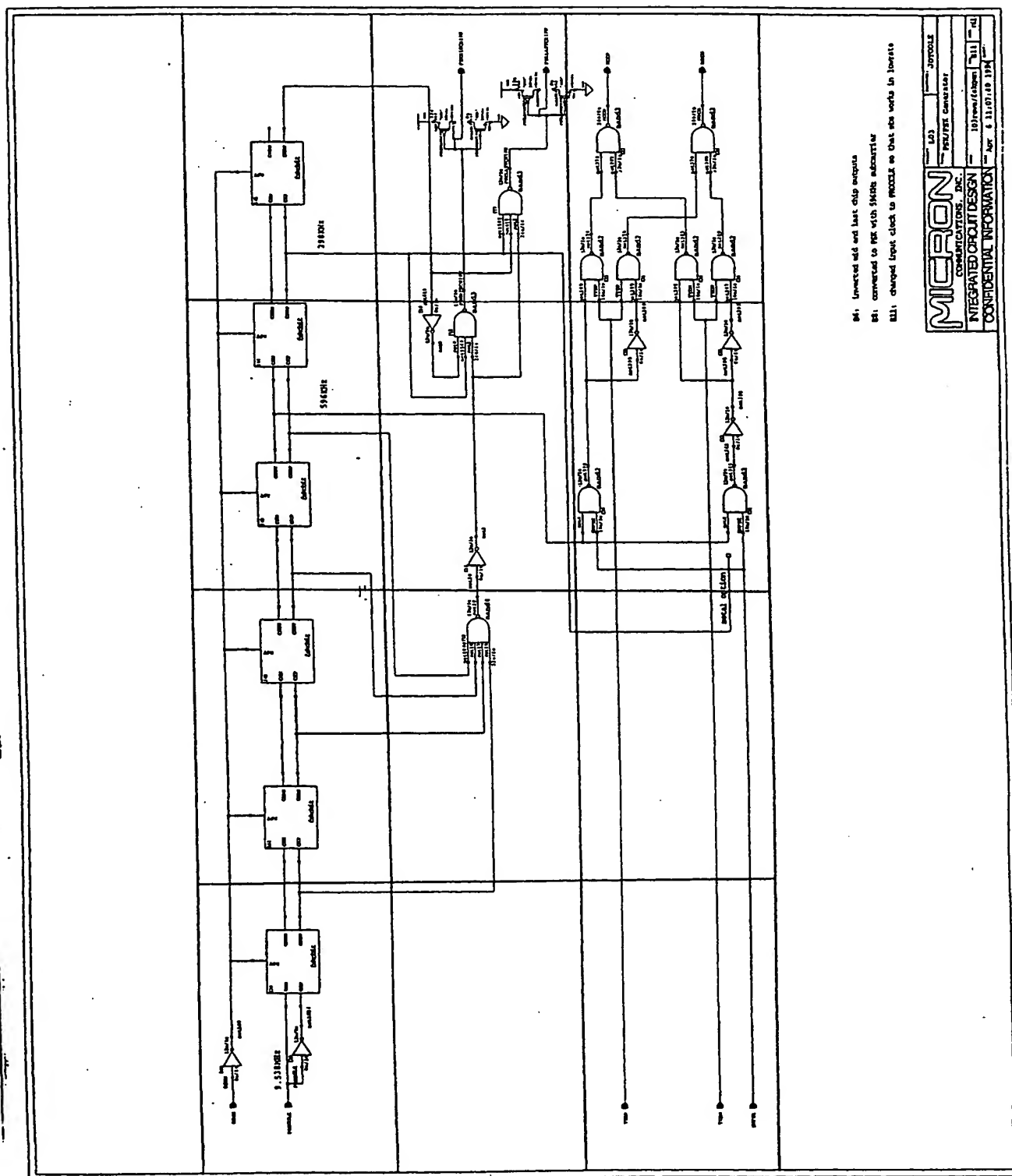
Fig. 10.06

101. Standard. In accordance with the requirements of the standard.

MICRON	
DESIGNATION	101
REVISION	1.0
DATE	10/10/06
BY	101
CHECKED	101
APPROVED	101

10.07AA	10.07AB	10.07AC	10.07AD
10.07BA	10.07BB	10.07BC	10.07BD
10.07CA	10.07CB	10.07CC	10.07CD

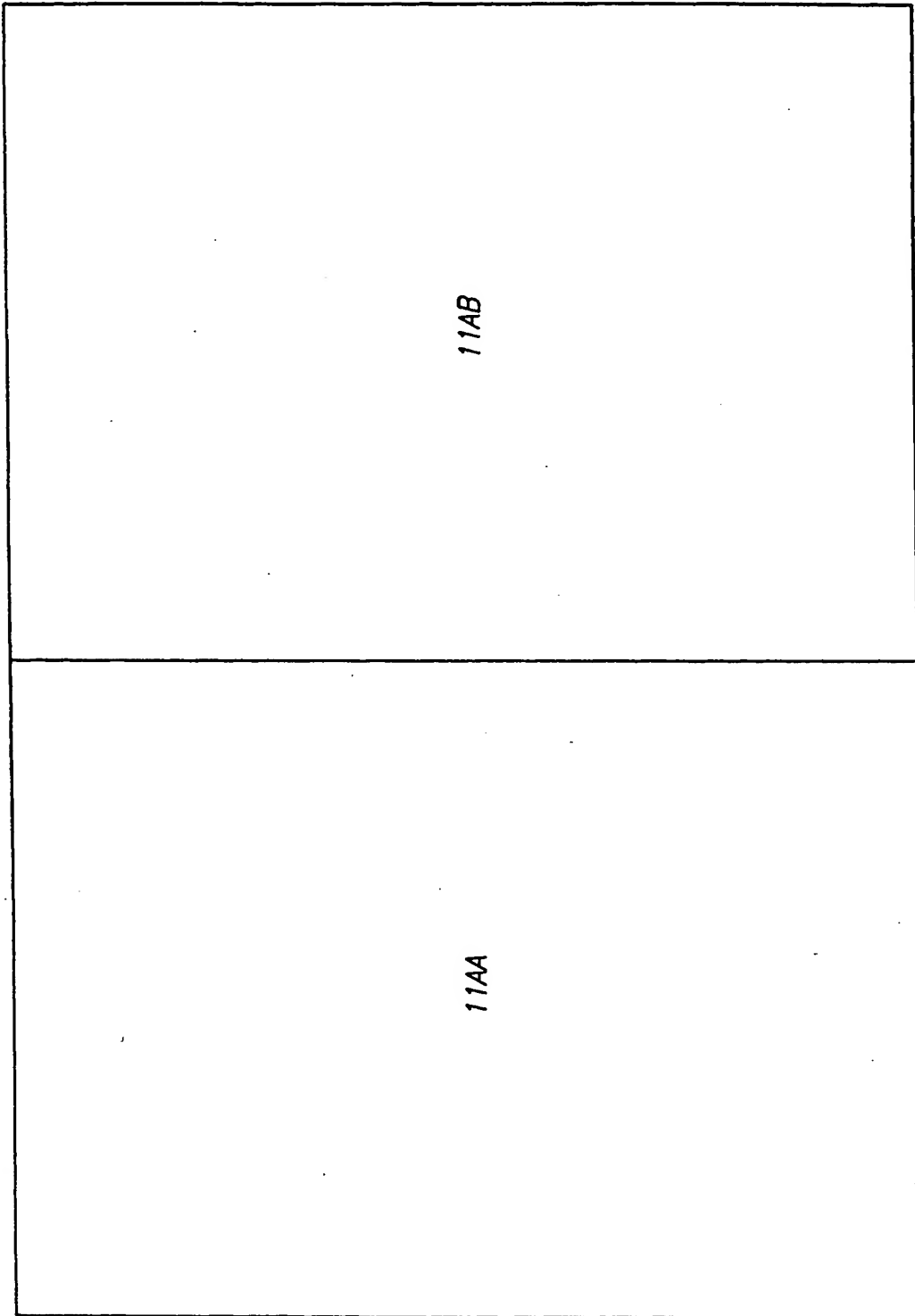
II II II II II



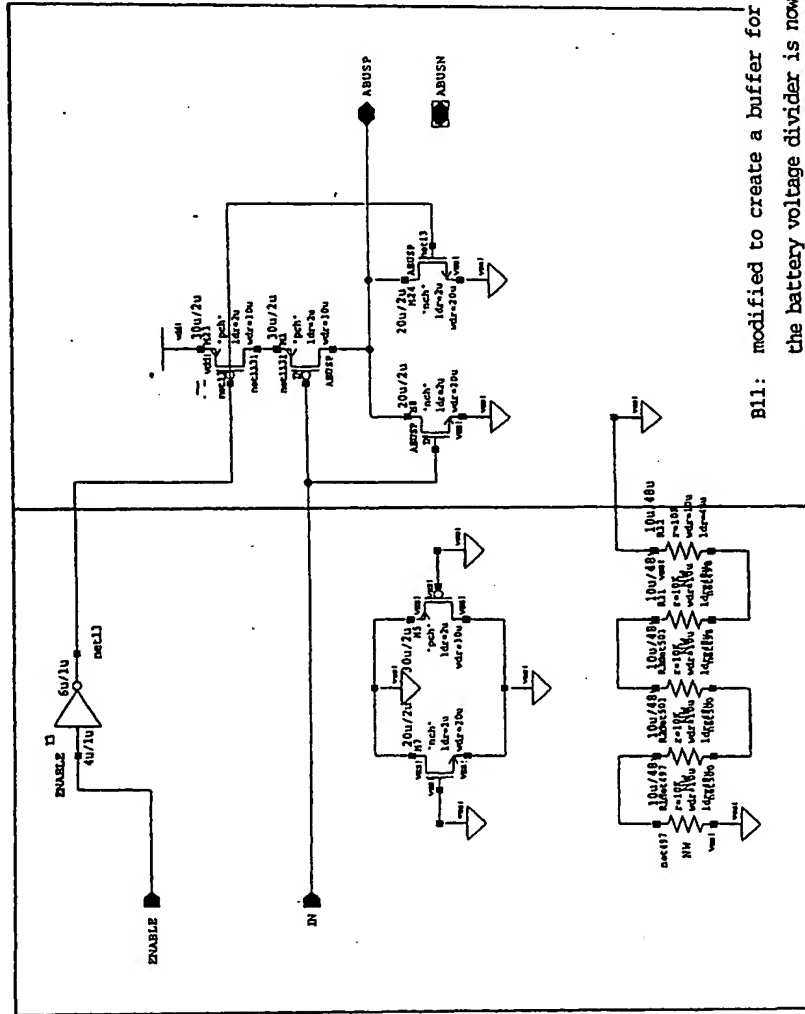
10.0701AB

10.0701AA

EX 10.0701



11 11



B11: modified to create a buffer for the opamp output
the battery voltage divider is now part of tsn

MICRON
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

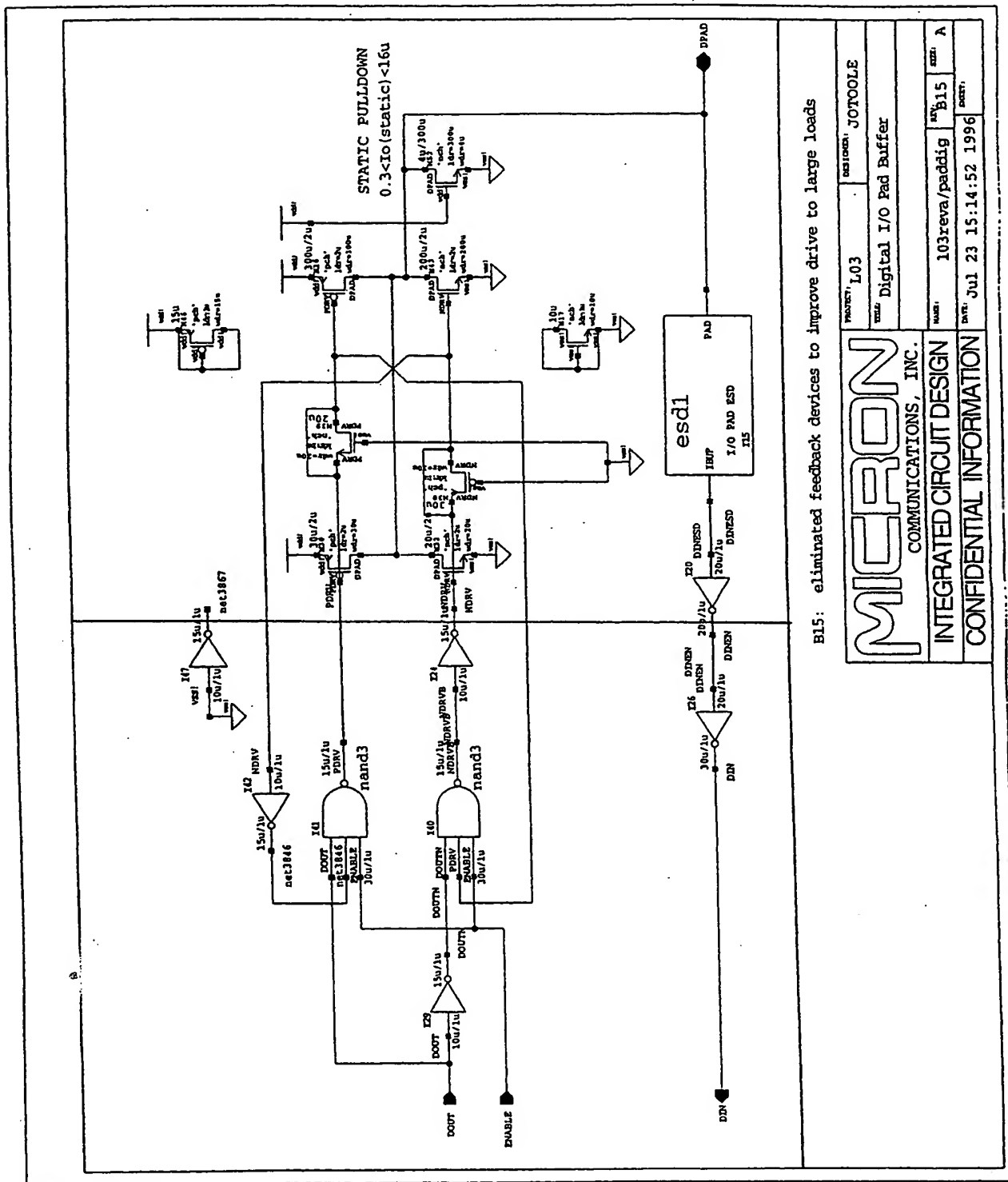
PROJECT: L03	DESIGNER: JOTOOLE
NAME: Battery Analog I/O Buffer	
DATE: Apr 8 10:19:56 1996	REV: B11
	REV: A
	SHEET: 1

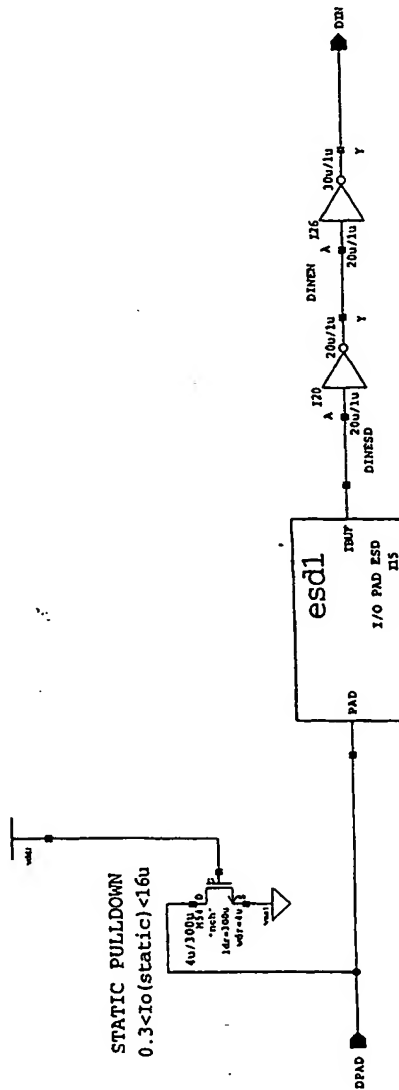
Fig. 11

12AB

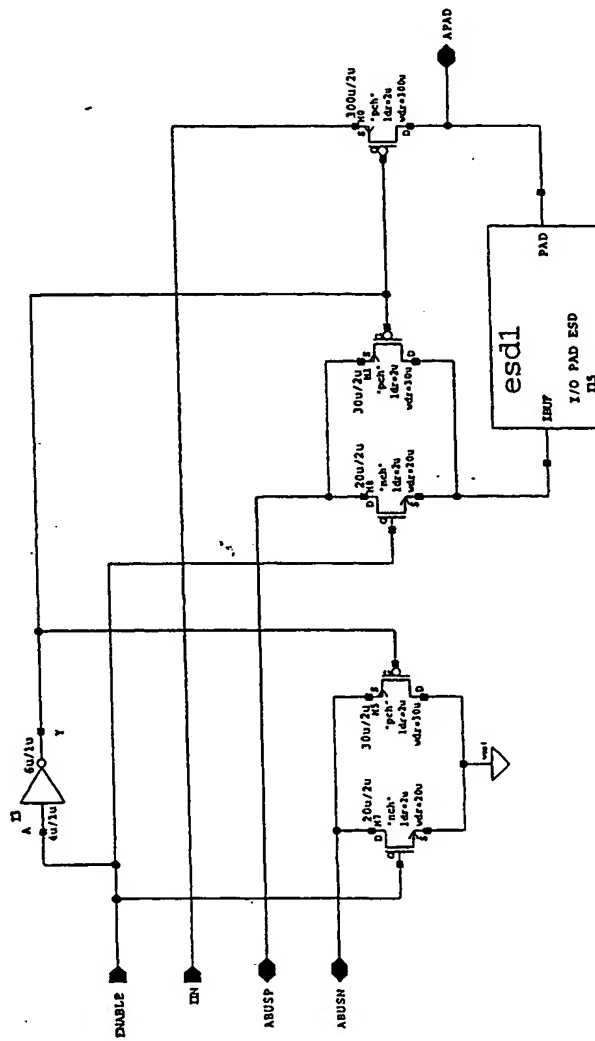
12AA

112





MICROON		PROJECT: L03		DESIGNER: JOTOOLE	
		TITLE: Digital Input Pad Buffer			
		NAME: 103reva/padigin		REV: B1	SIZE: A
		DATE: Apr 11 11:10:35 1995		PAGES: 1	



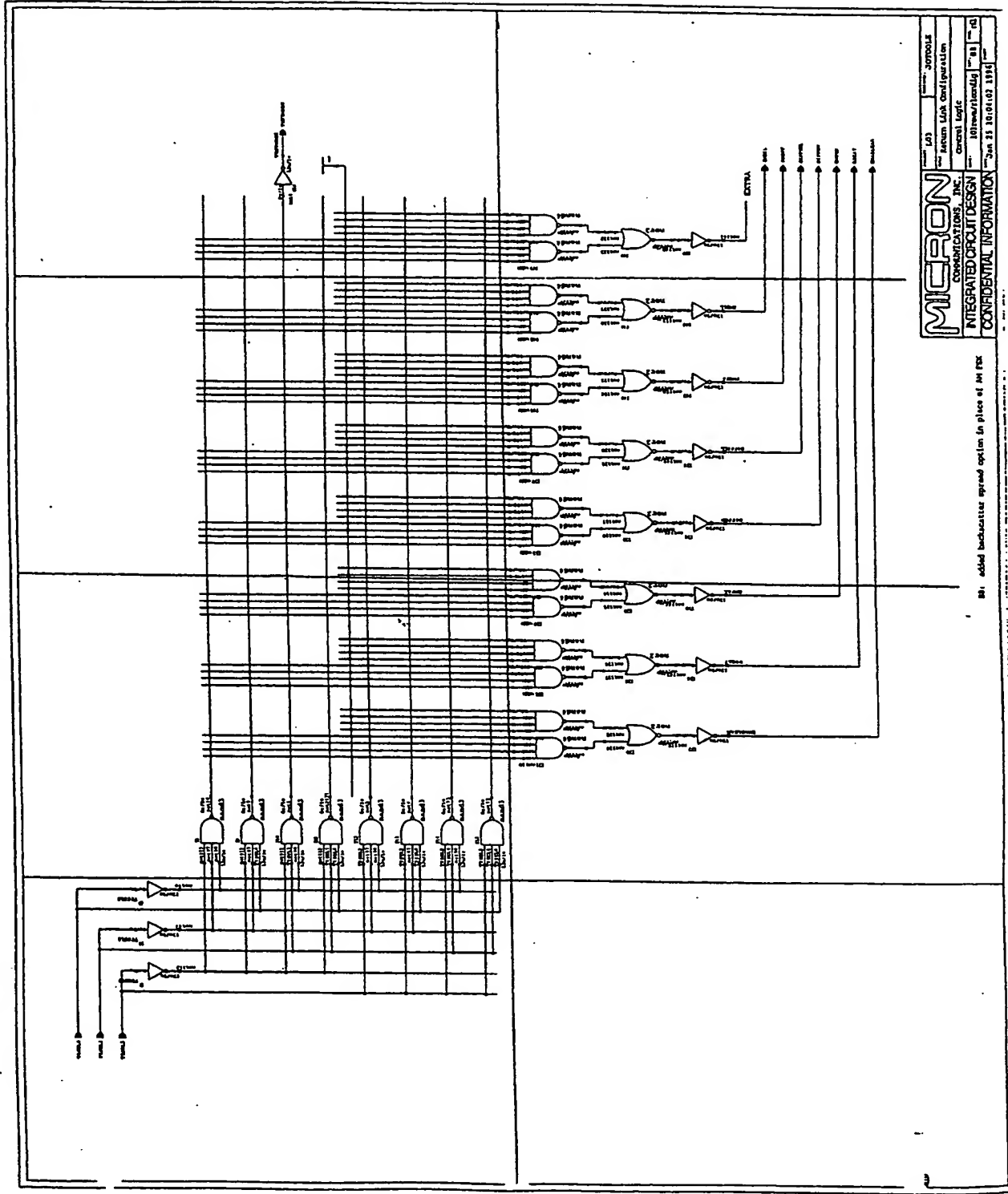
MICRON		PRODUCT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Analog I/O Pad Buffer	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/padalg	REV: -
CONFIDENTIAL INFORMATION		DATE: Dec 12 21:55:41 1993	SHEET: A

FIG. 19

15AD	15BC
15AC	15BB
15AB	15BA
15AA	

EX-15

FIG. 15



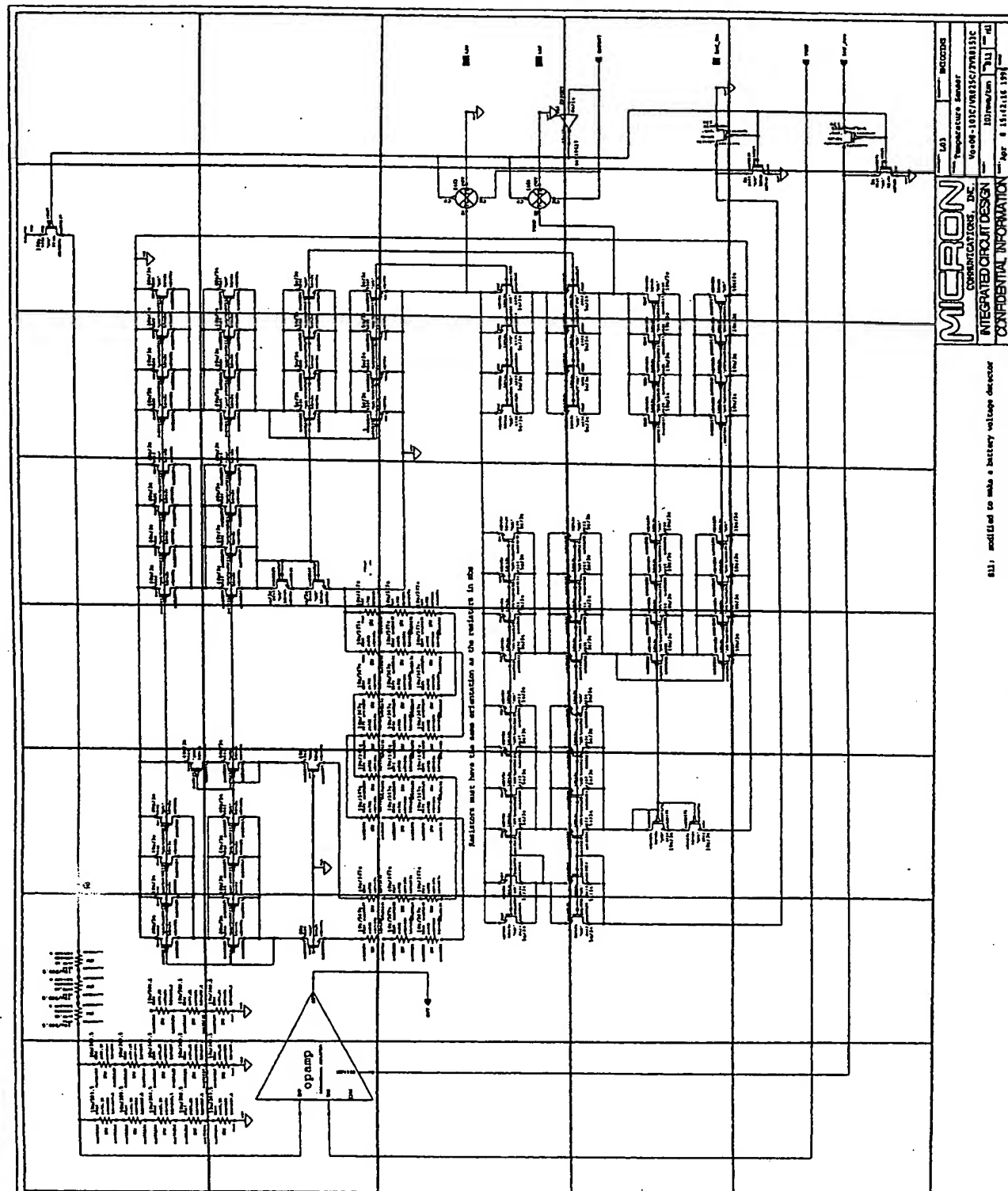
MICRON
COMMUNICATIONS, INC.
INTEGRATED CIRCUIT DESIGN
CONFIDENTIAL INFORMATION

161 added backscatter spread option in place of 161 psc

16AA	16AB	16AC	16AD	16AE	16AF	16AG	16AH
16BA	16BB	16BC	16BD	16BE	16BF	16BG	16BH
16CA	16CB	16CC	16CD	16CE	16CF	16CG	16CH
16DA	16DB	16DC	16DD	16DE	16DF	16DG	16DH
16EA	16EB	16EC	16ED	16EE	16EF	16EG	16EH

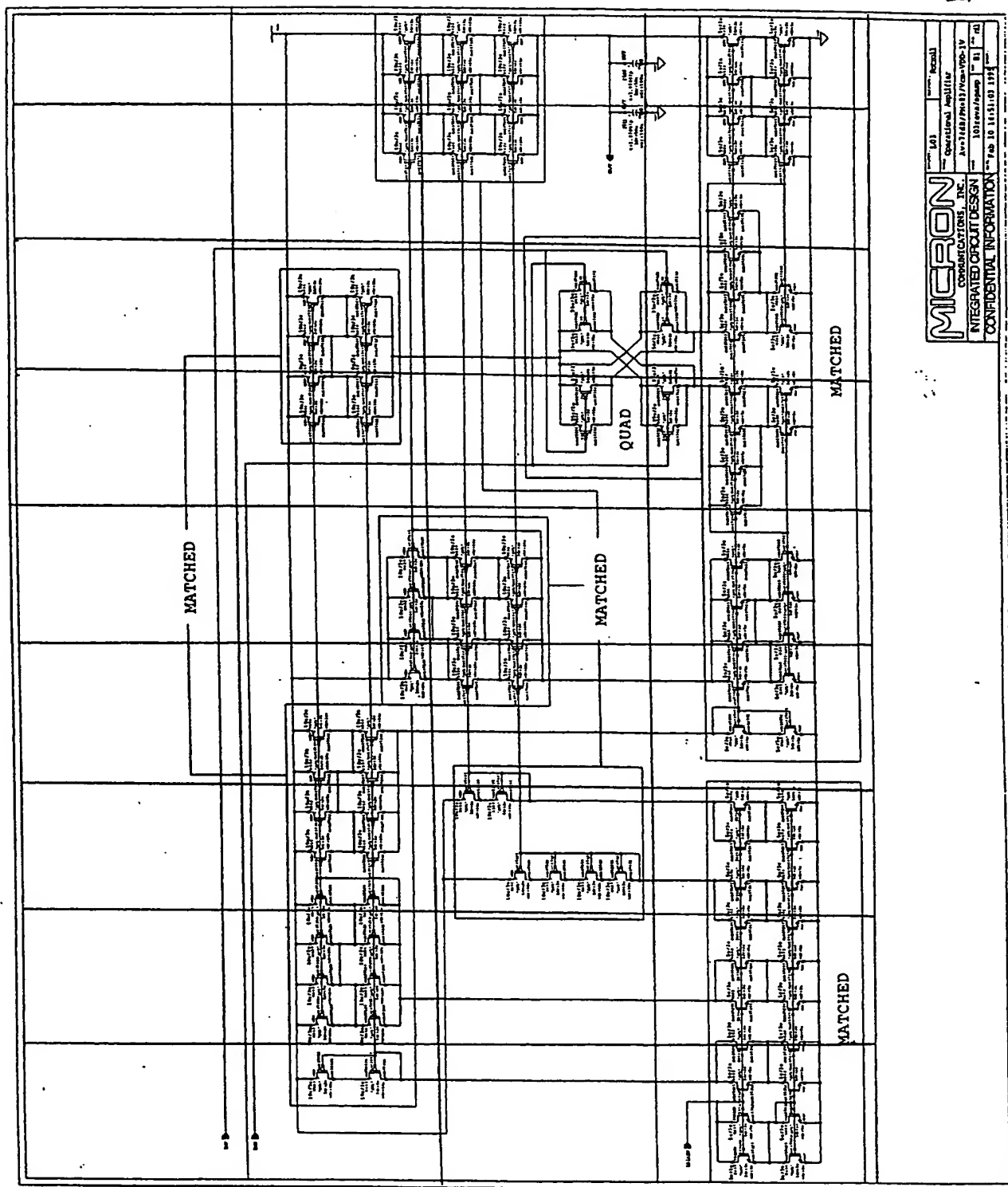
IL 15

Fig. 16



16.01AA	16.01AB	16.01AC	16.01AD	16.01AE	16.01AF	16.01AG		
16.01BA	16.01BB	16.01BC	16.01BD	16.01BE	16.01BF	16.01BG	16.01BH	16.01BI
16.01CA	16.01CB	16.01CC	16.01CD	16.01CE	16.01CF	16.01CG	16.01CH	16.01CI
16.01DA	16.01DB	16.01DC	16.01DD	16.01DE	16.01DF	16.01DG	16.01DH	16.01DI

II II II II II II II II II



MICRON
 CORPORATION, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION

Part 141
 Operation: Additive
 A=7448/7449/7450-1V
 101rev/June 74
 Feb 10 11:51:03 1974

Fig. 16.01

17AB	17BB
17AA	17BA

II II

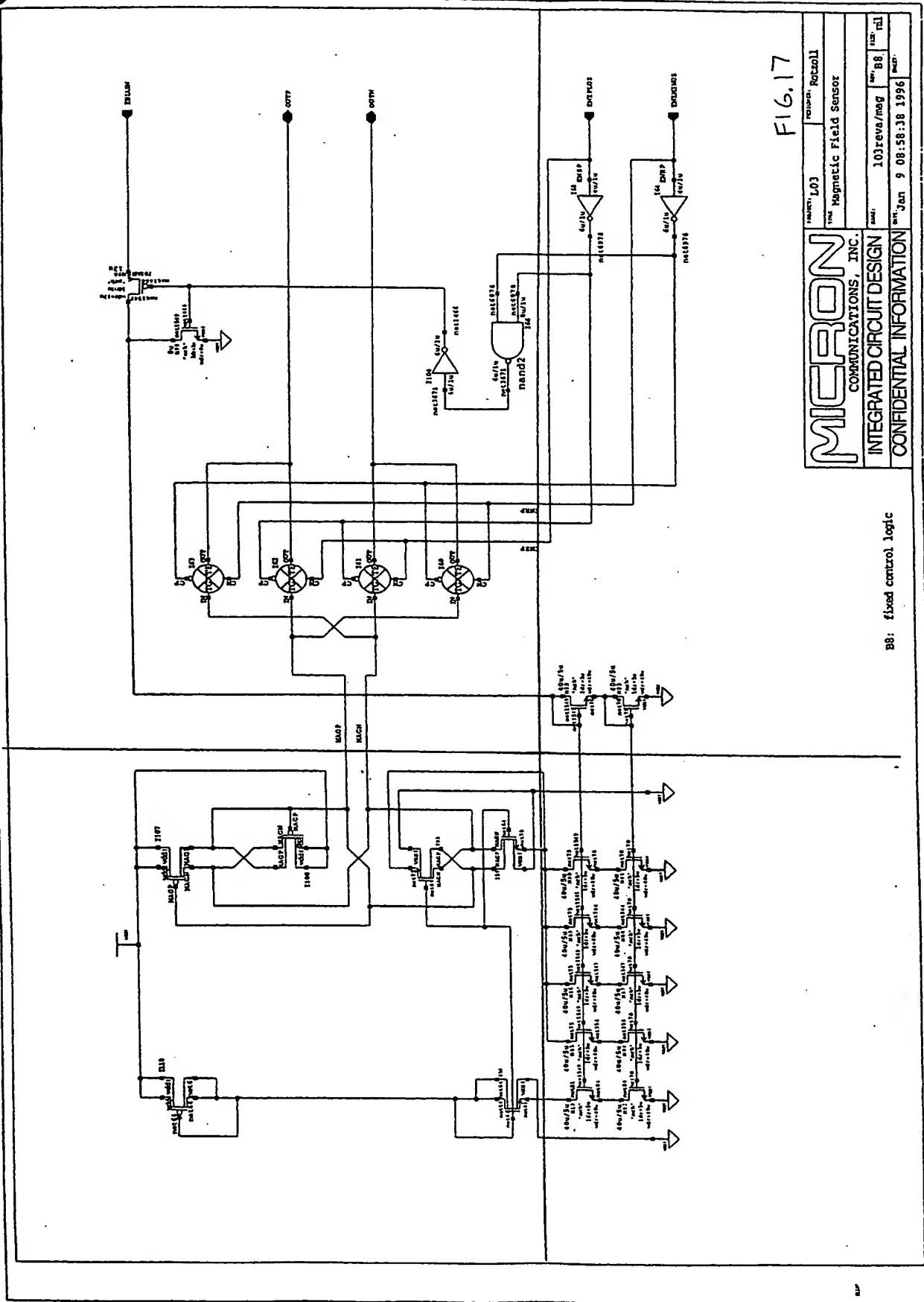
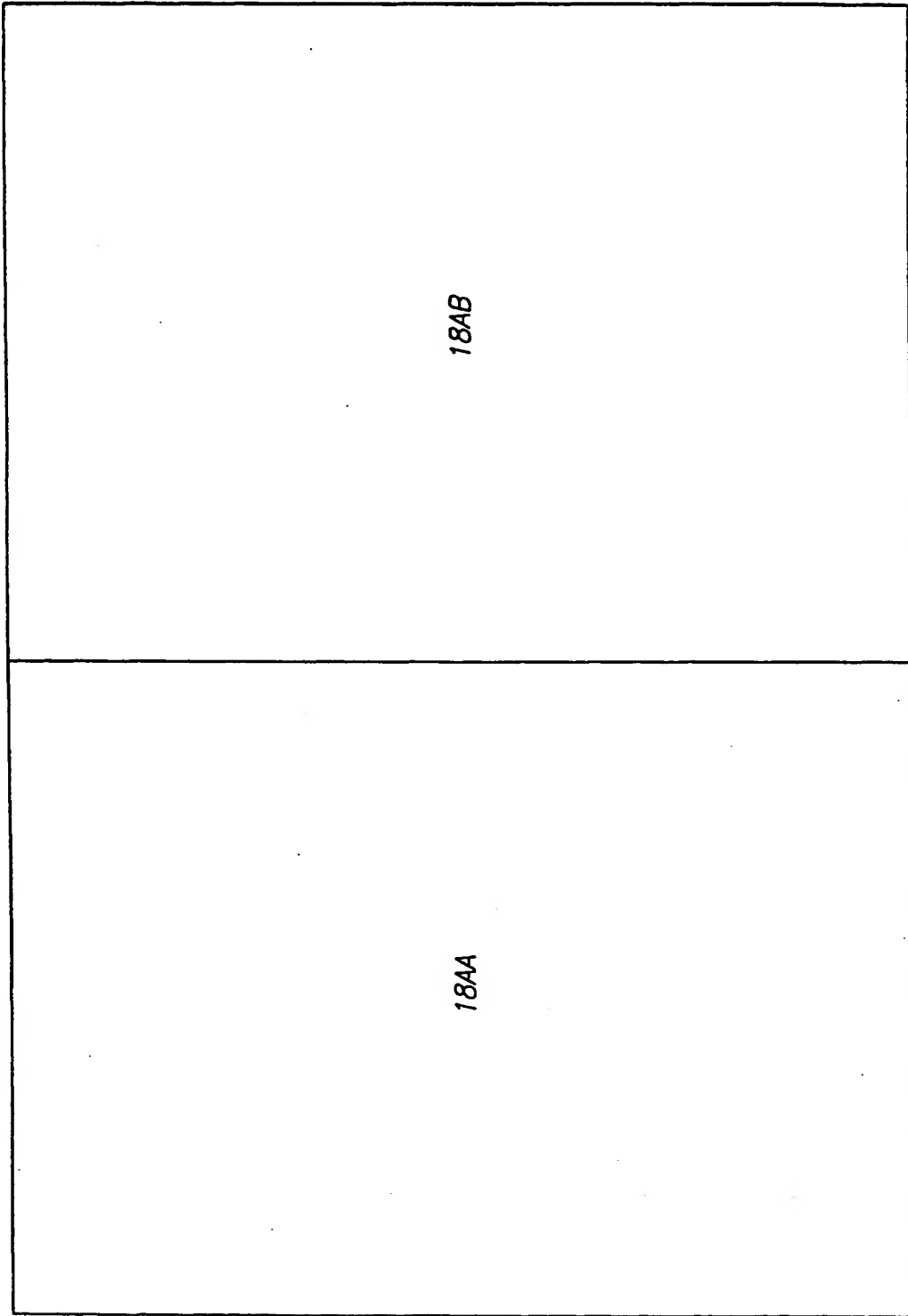


FIG. 17

MICRON COMMUNICATIONS, INC.	DESIGN: L03	DESIGNED: Retzoll
	Magnetic Field Sensor	
	DATE: 10/28/88	REV: B8
	CONFIDENTIAL INFORMATION	

B8: fixed control logic

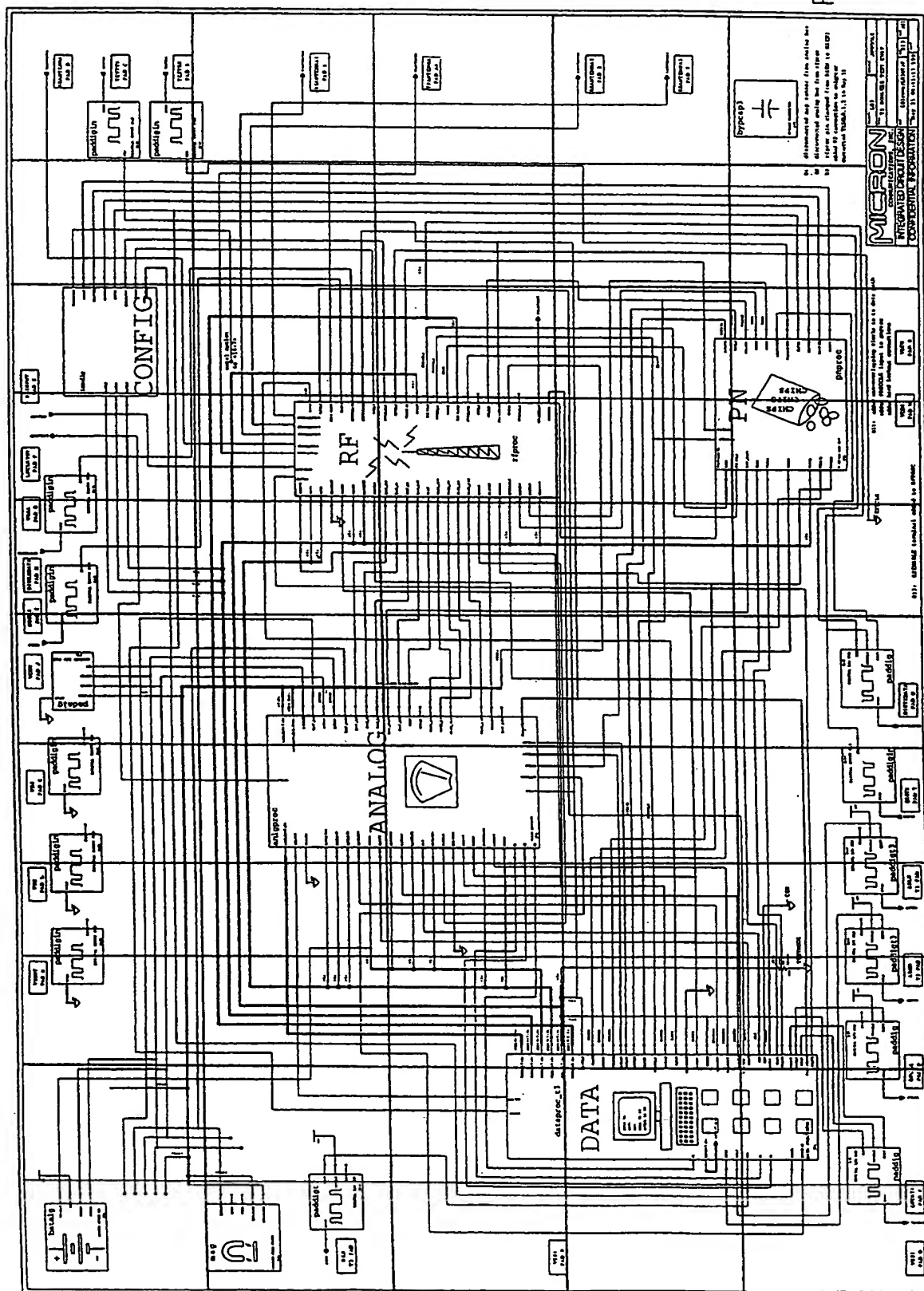


18A 18B

19AA	19AB	19AC	19AD	19AE	19AF	19AG	19AH	19AI	19AJ	19AK
19BA	19BB	19BC	19BD	19BE	19BF	19BG	19BH	19BI	19BJ	19BK
19CA	19CB	19CC	19CD	19CE	19CF	19CG	19CH	19CI	19CJ	19CK
19DA	19DB	19DC	19DD	19DE	19DF	19DG	19DH	19DI	19DJ	19DK
19EA	19EB	19EC	19ED	19EE	19EF	19EG	19EH	19EI	19EJ	19EK

11 11 11

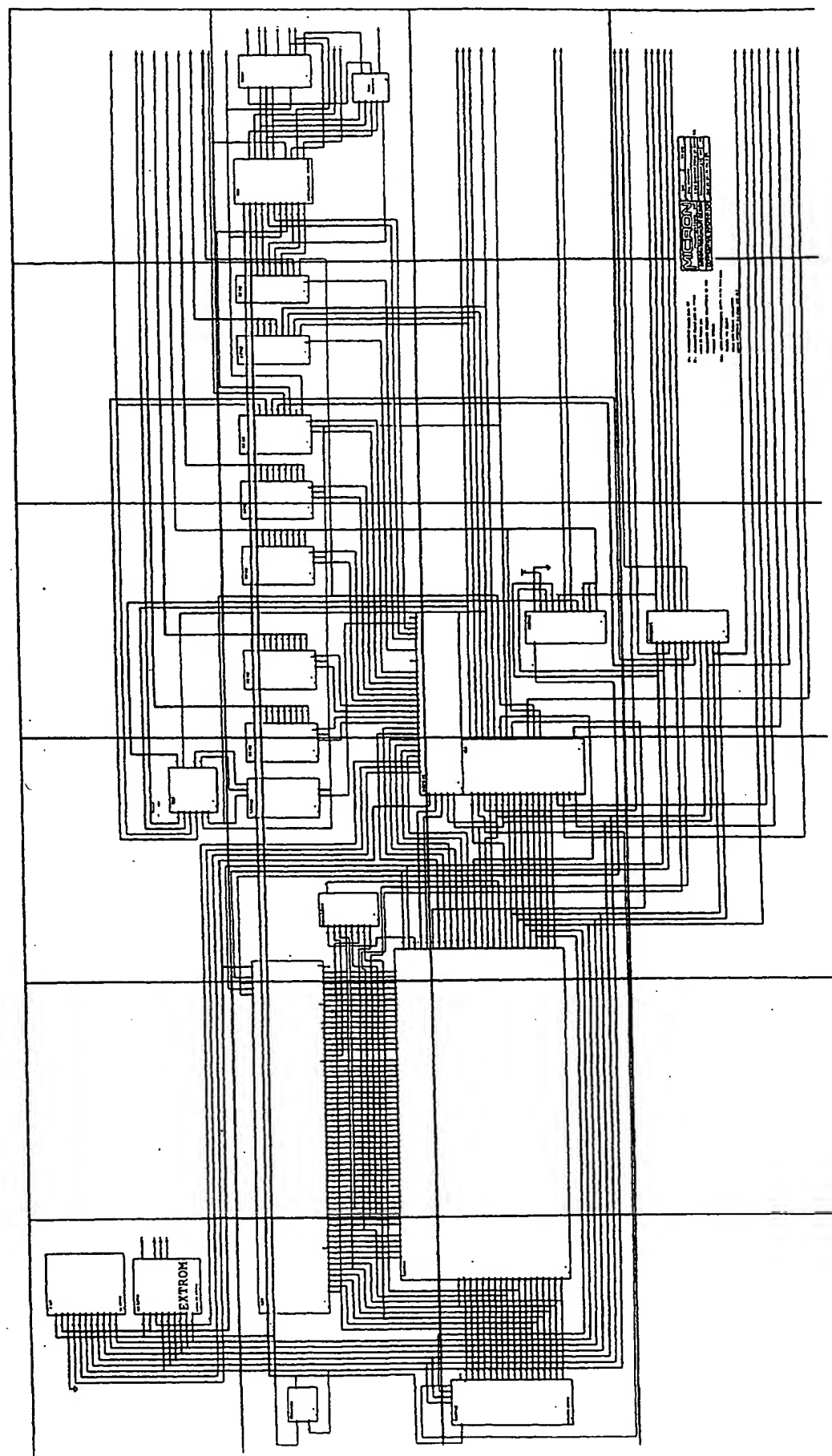
FIG. 19. AA-EK



20AA	20AB	20AC	20AD	20AE	20AF
20BA	20BB	20BC	20BD	20BE	20BF
20CA	20CB	20CC	20CD	20CE	20CF
		20DC	20DD	20DE	20DF

EX-200

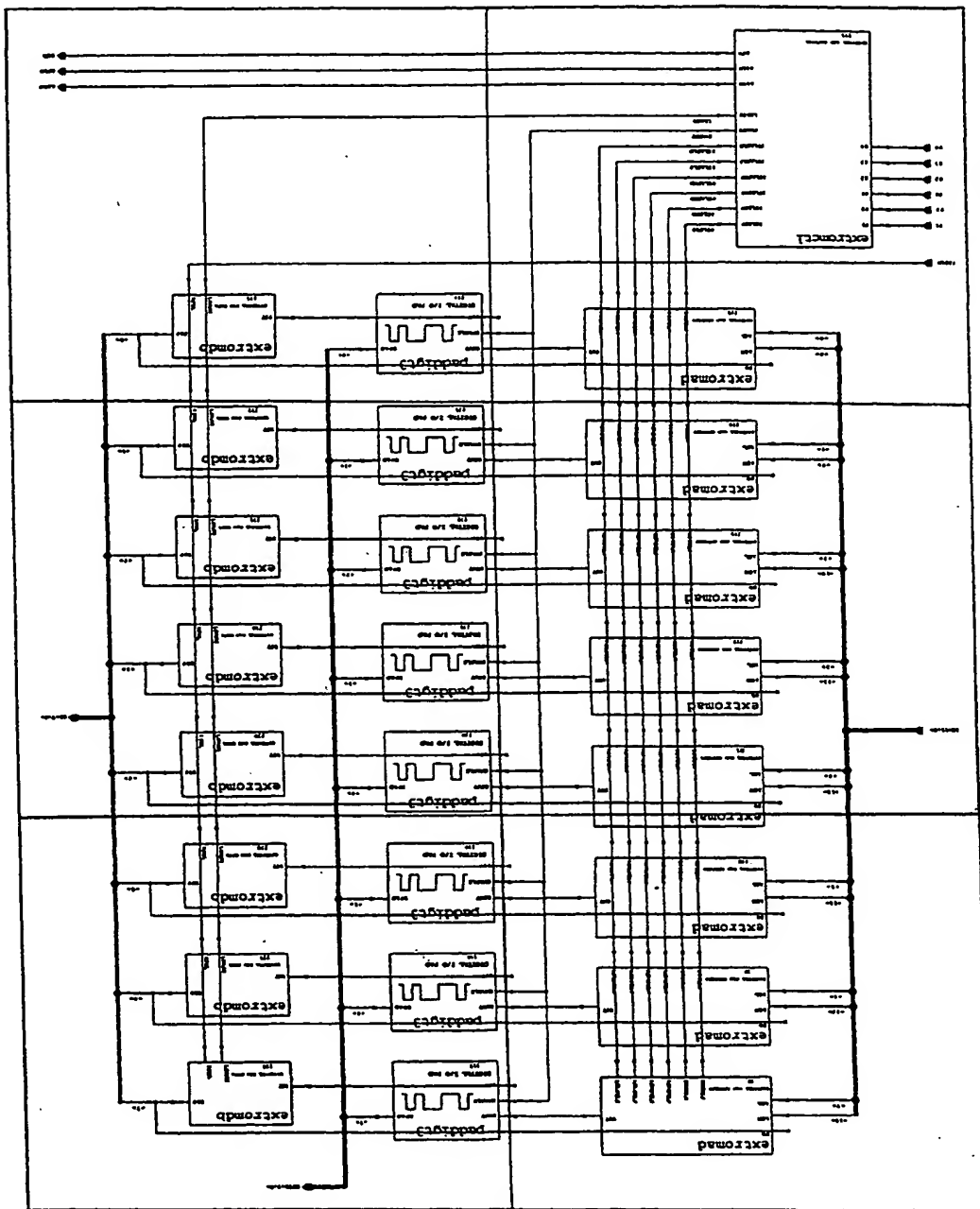
FIG. 20



20.01AA	20.01AB
20.01BA	20.01BB
20.01CA	20.01CB

Fig 20.01

Fig. 20.01



20.0101AA	20.0101AB
20.0101BA	20.0101BB

II II II II II II II II

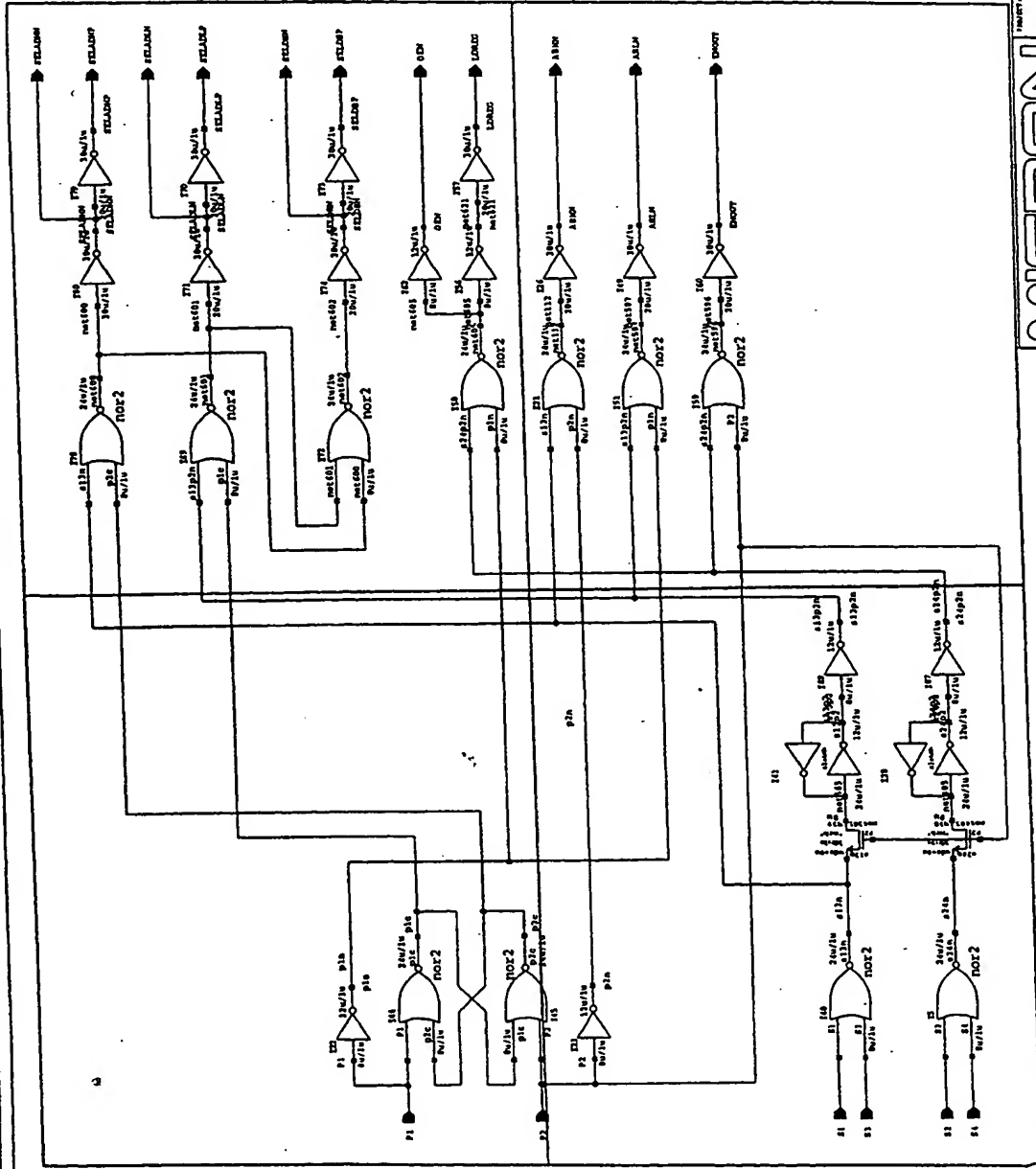


FIG. 20.0101

MICRON		MARKET	L03	REVISION	Rev 001
COMMUNICATIONS, INC.		External ROM Control Logic			
INTEGRATED CIRCUIT DESIGN		DATE	10/19/84	EXTENDED	REV - 001
CONFIDENTIAL INFORMATION		DATE	Dec 11 21:56:41 1993	EXTENDED	REV - 001

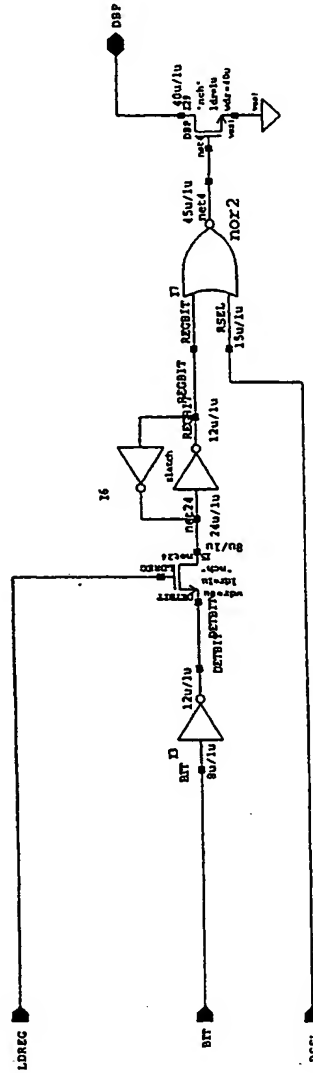
20.0103AA	20.0103AB	20.0103AC
-----------	-----------	-----------

EX 20.0103



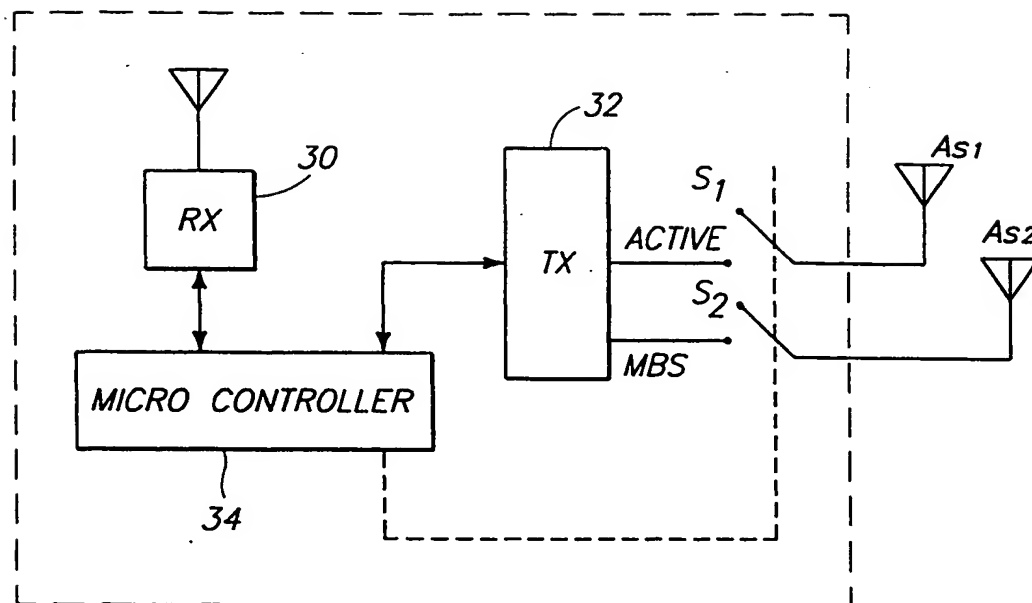
Fig 20.0103M-AC

FIG. 20.0104

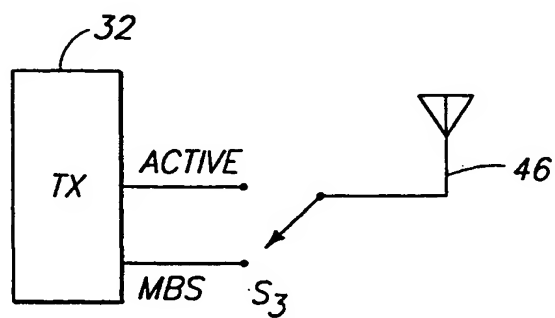


MICRON		PROJECT: L03		DESIGNER: Rotzoll	
COMMUNICATIONS, INC.		TITLE: External ROM Databus Interface			
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/extromdb		REV: -	SHEET: A
CONFIDENTIAL INFORMATION		DATE: Dec 11 01:01:13 1993		PAGE: 1	

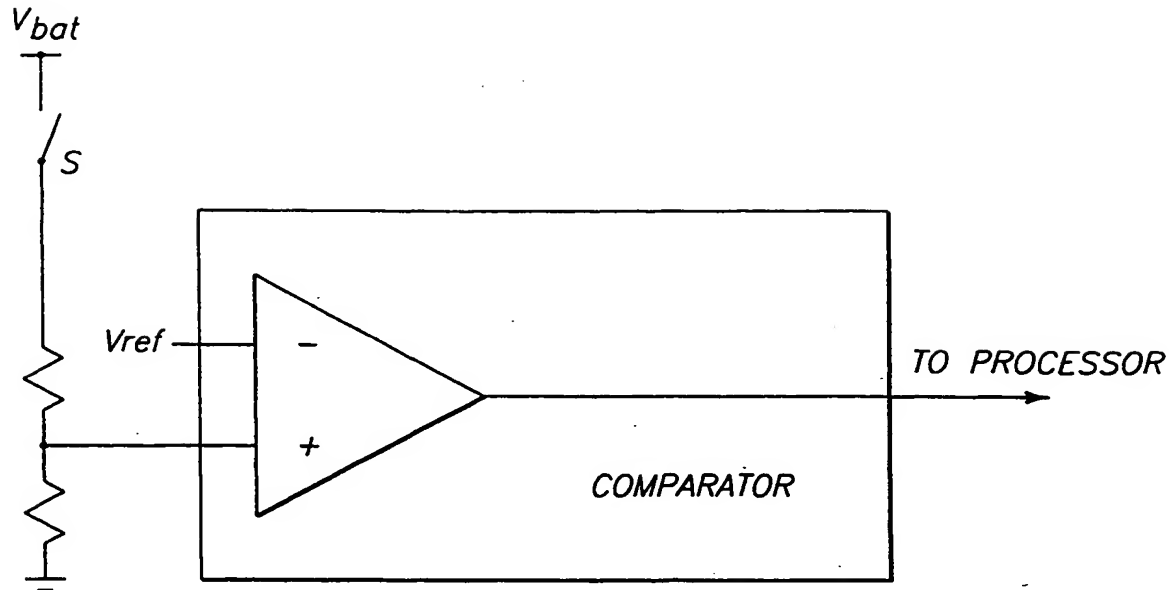
MICRON
 COMMUNICATIONS, INC.
 INTEGRATED CIRCUIT DESIGN
 CONFIDENTIAL INFORMATION



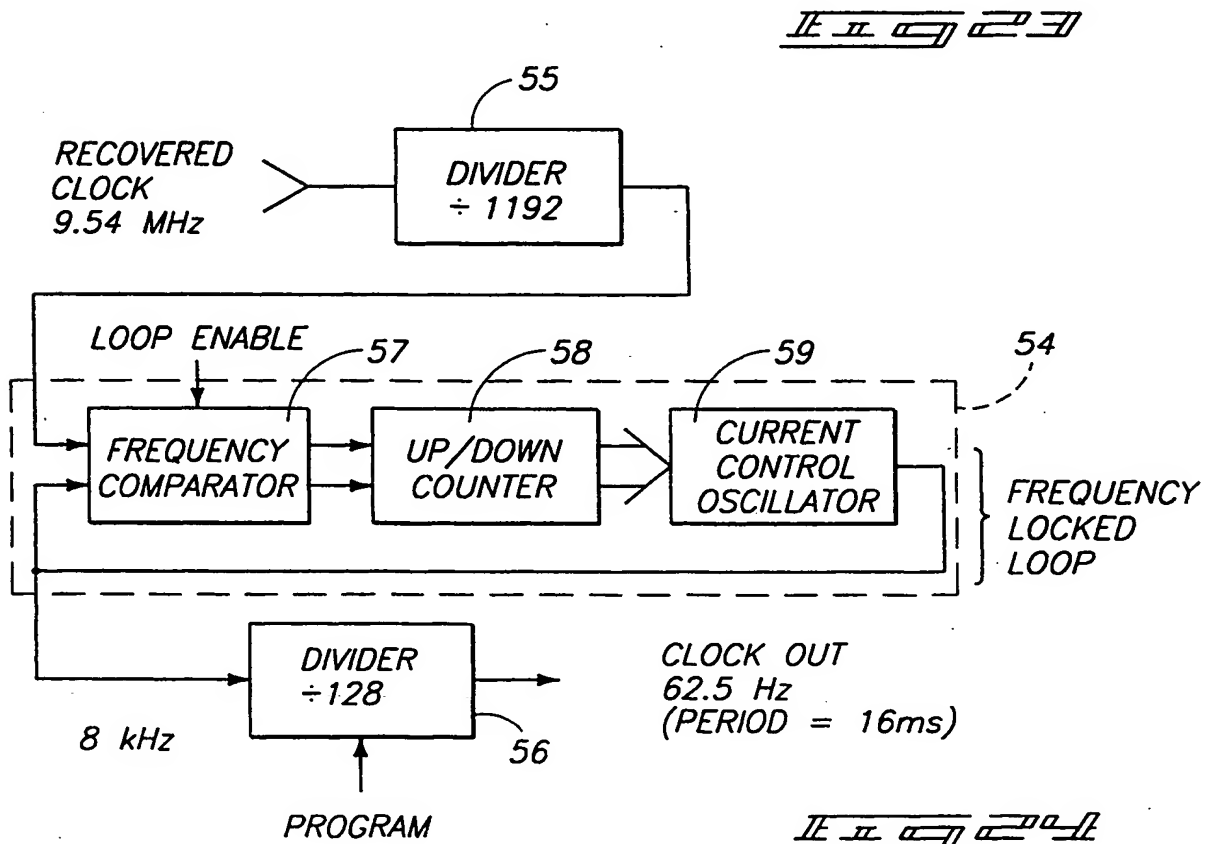
II II II II II

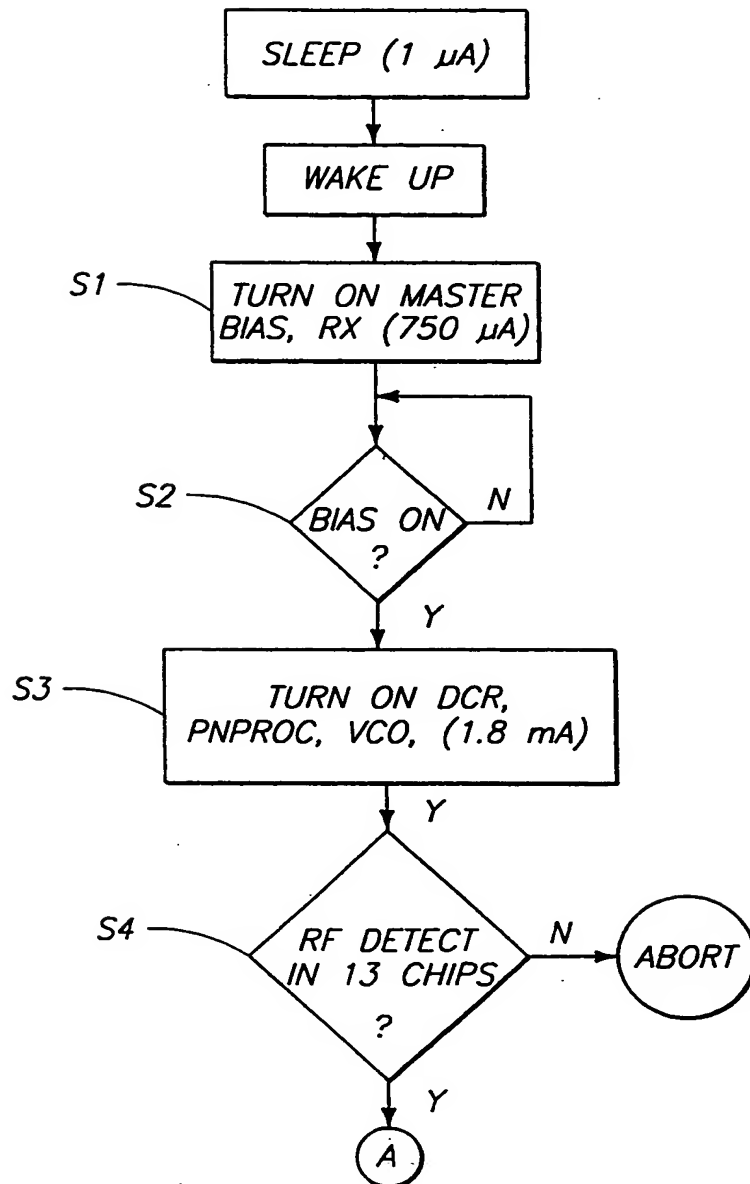


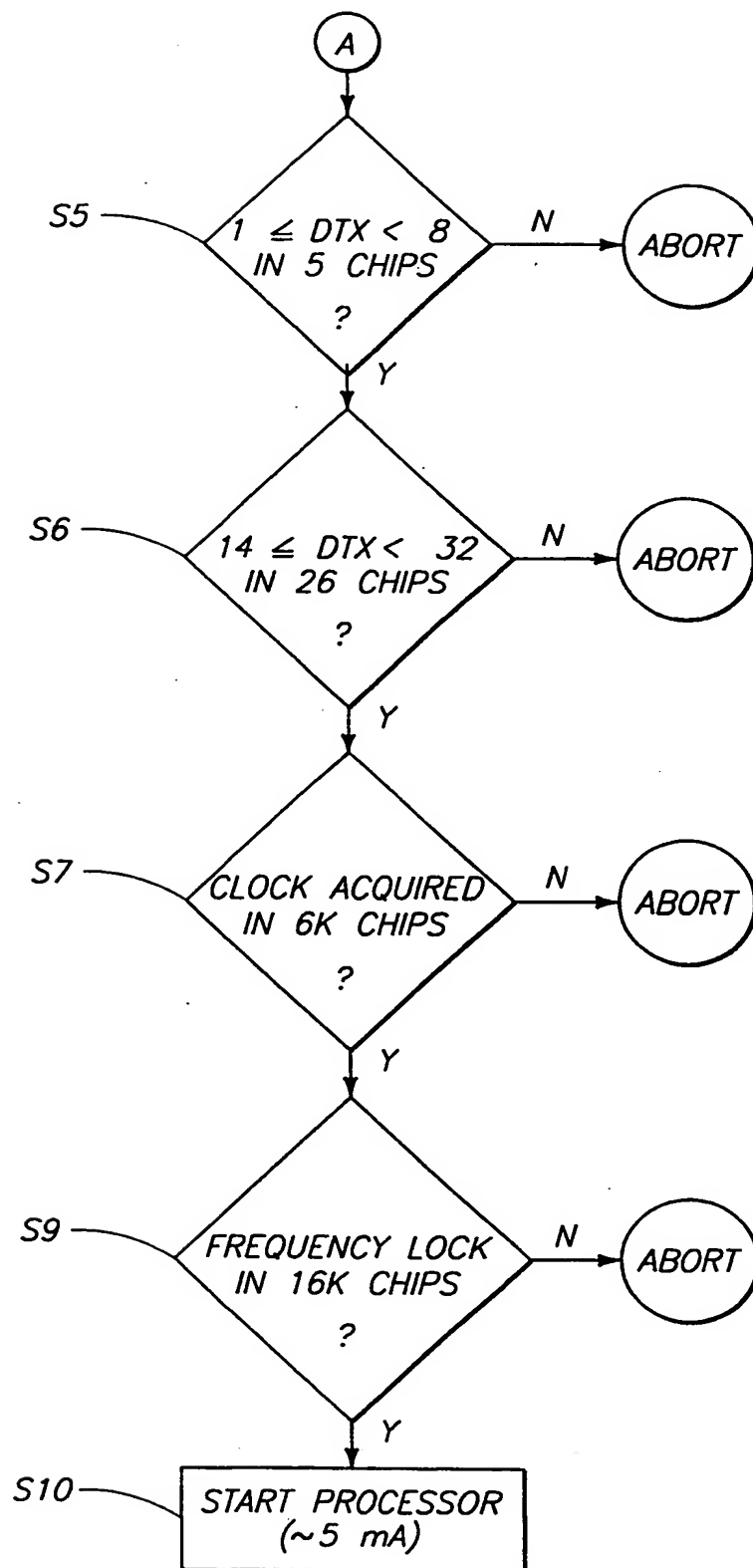
II II II II II

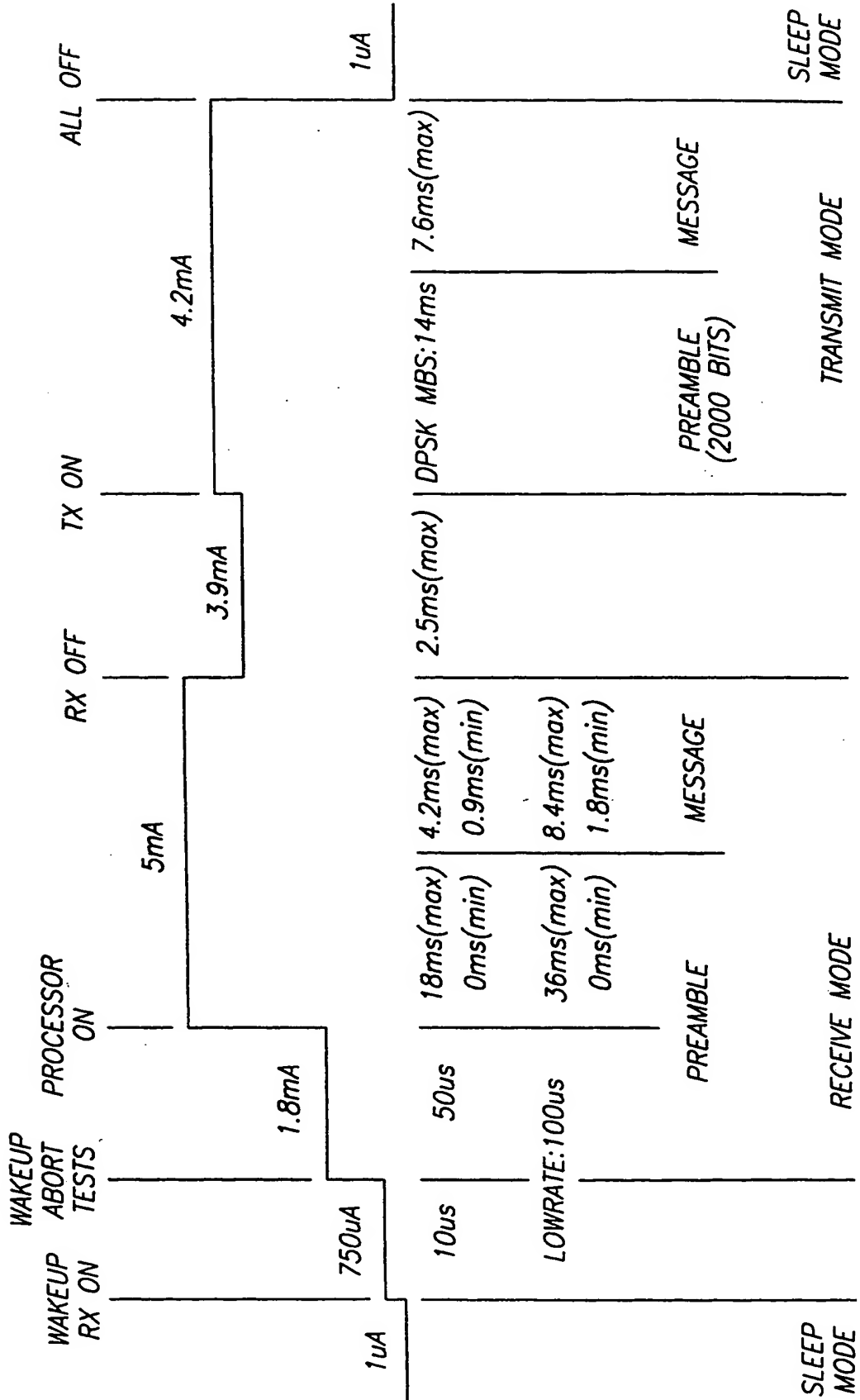


V_{ref} = bandgap voltage ≈ 1.2 V for silicon



WAKEUP SEQUENCE





II 9 27

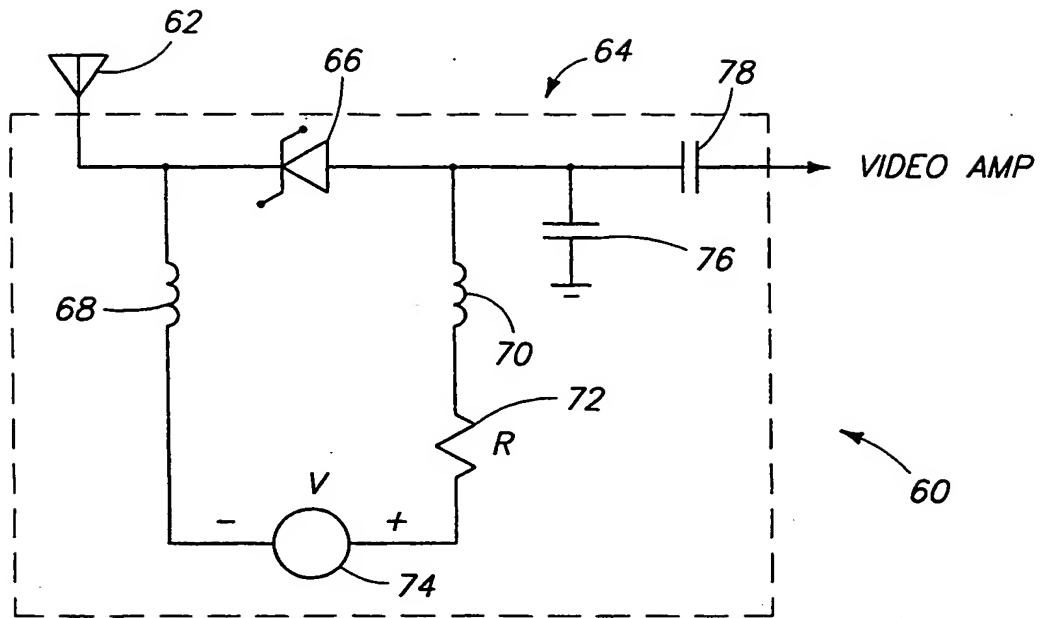


FIG. 28

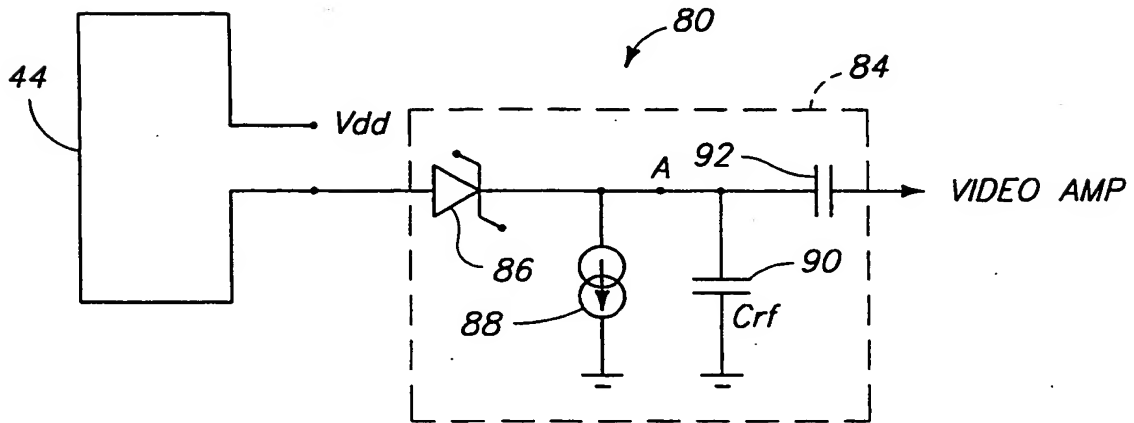
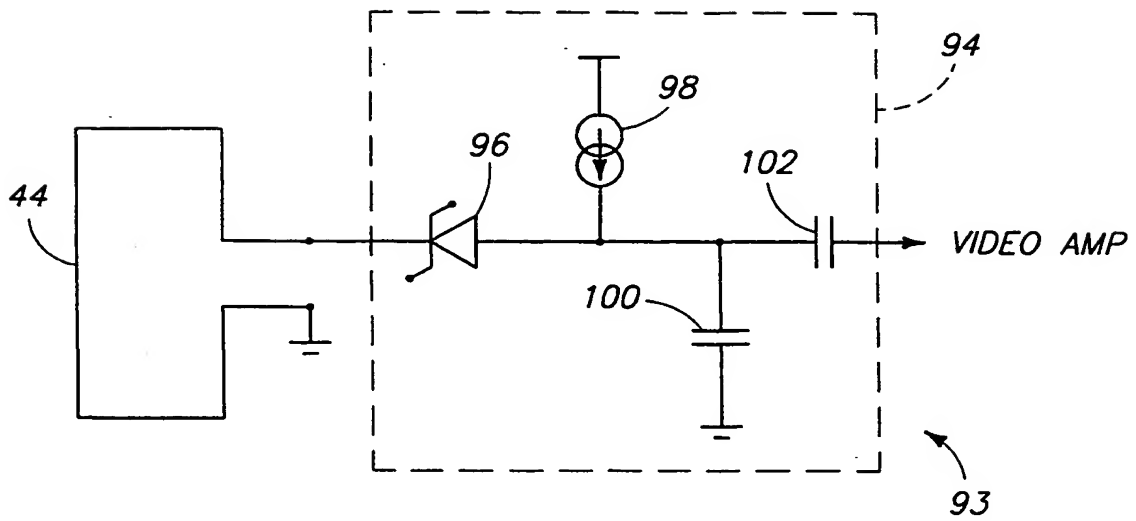
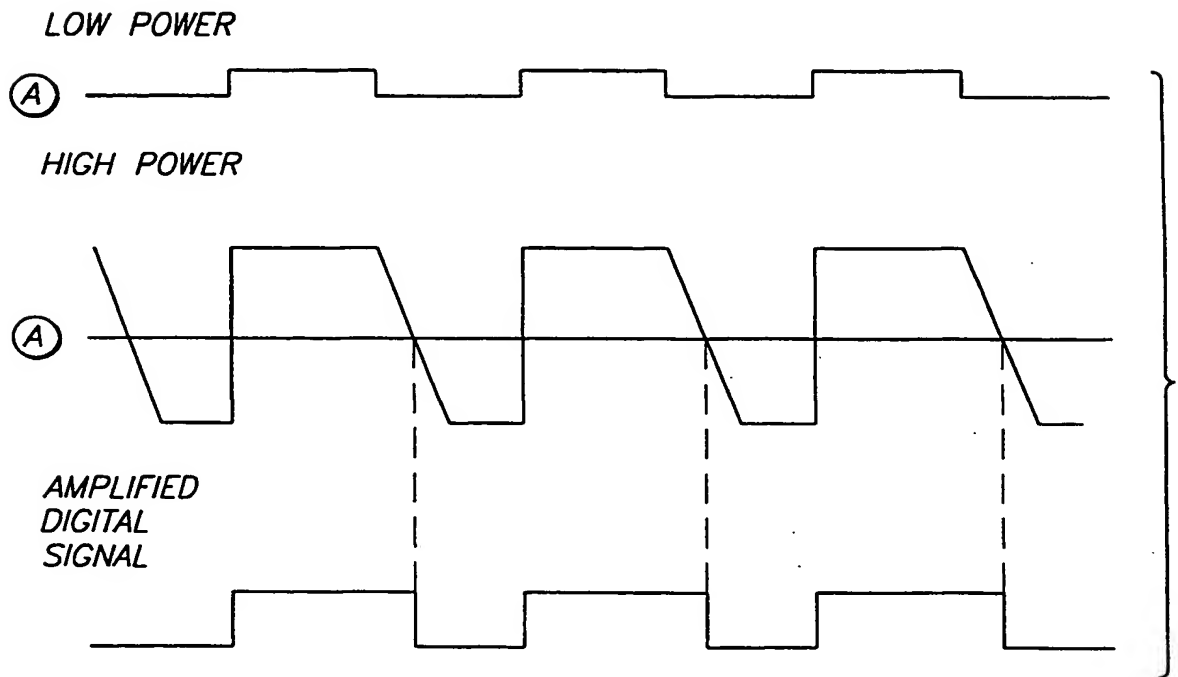


FIG. 29



II II II II



II II II II

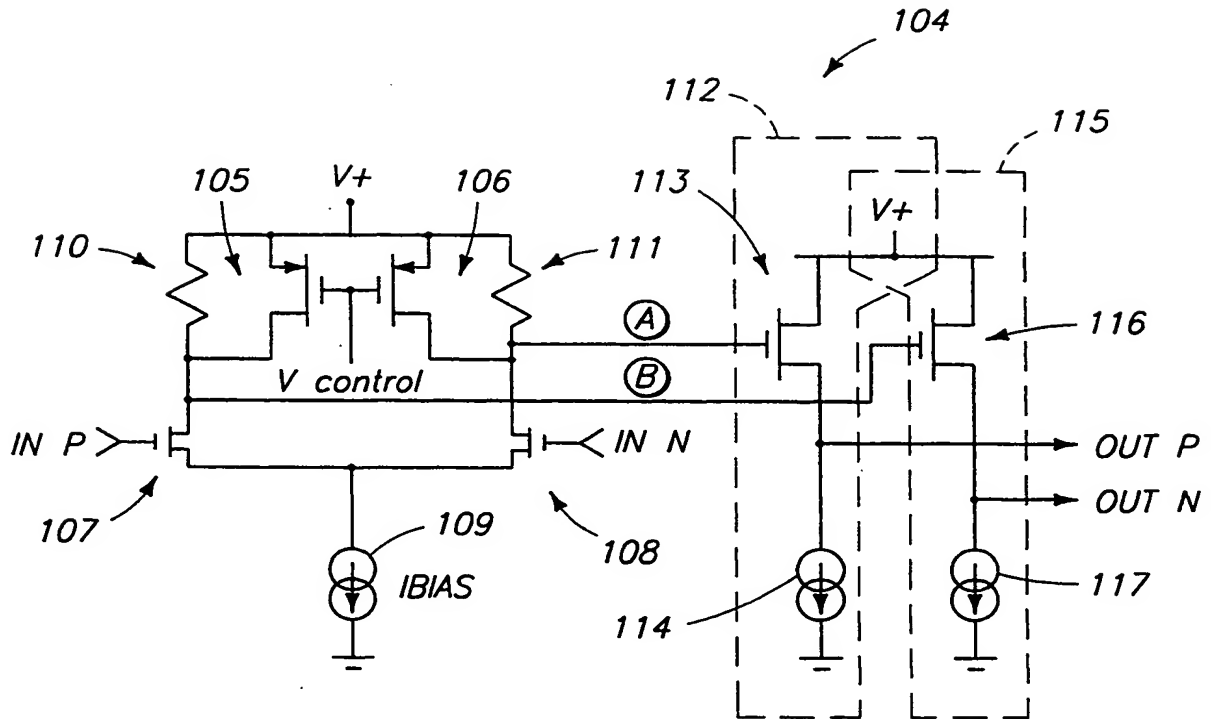


FIG. 32

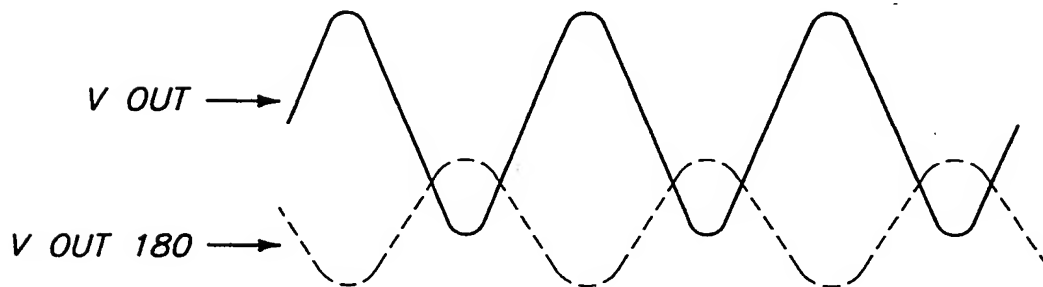
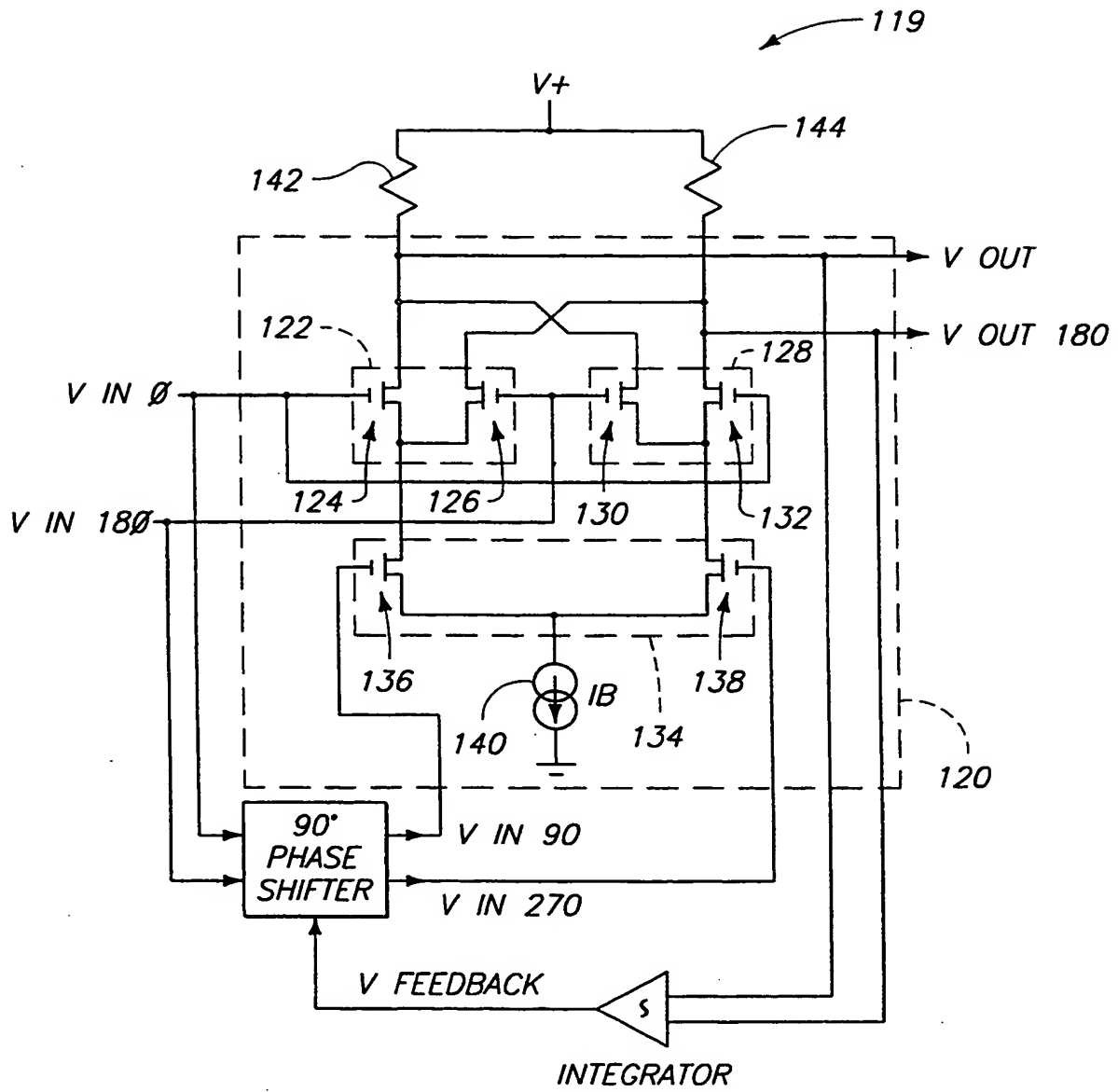
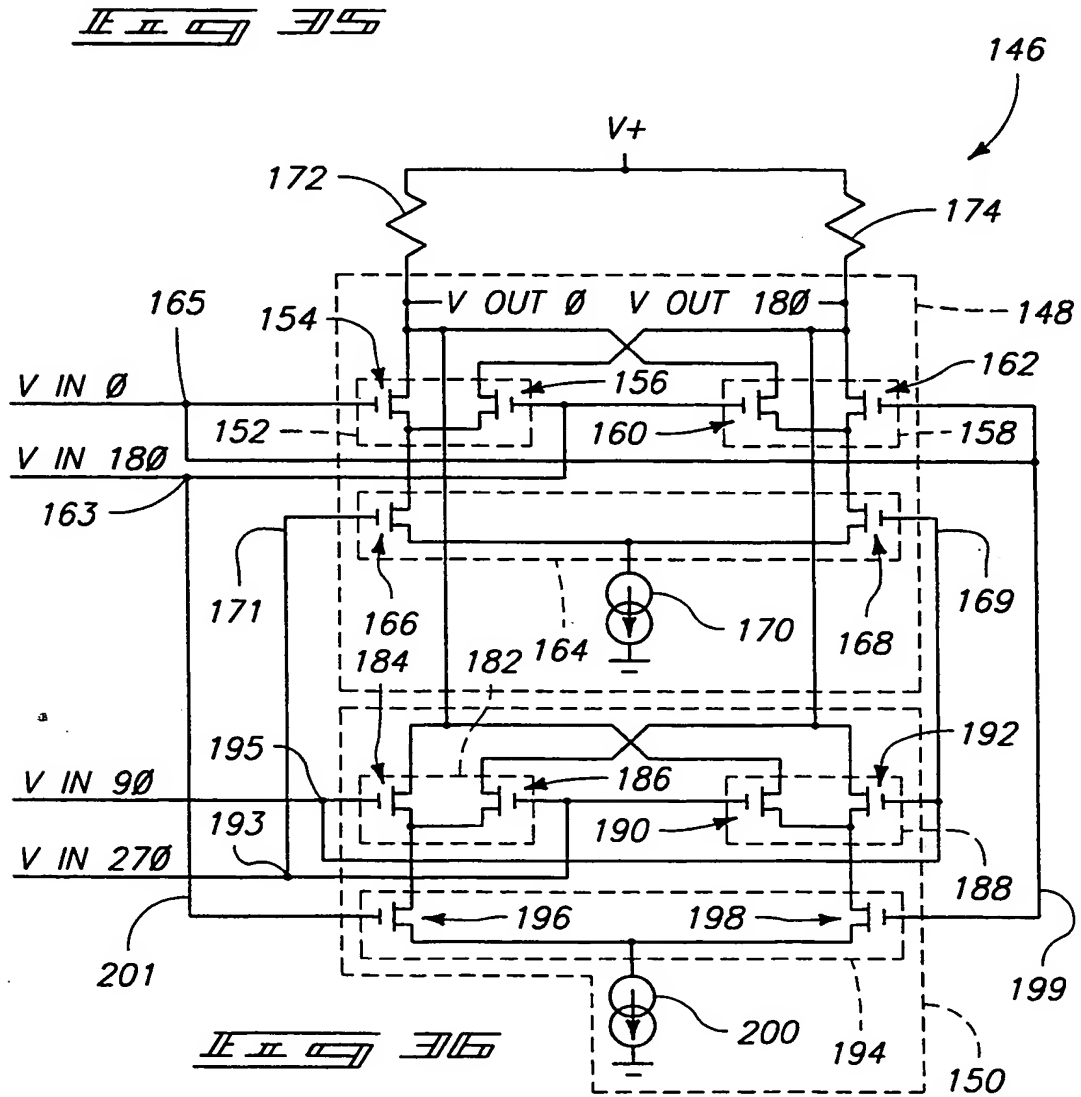
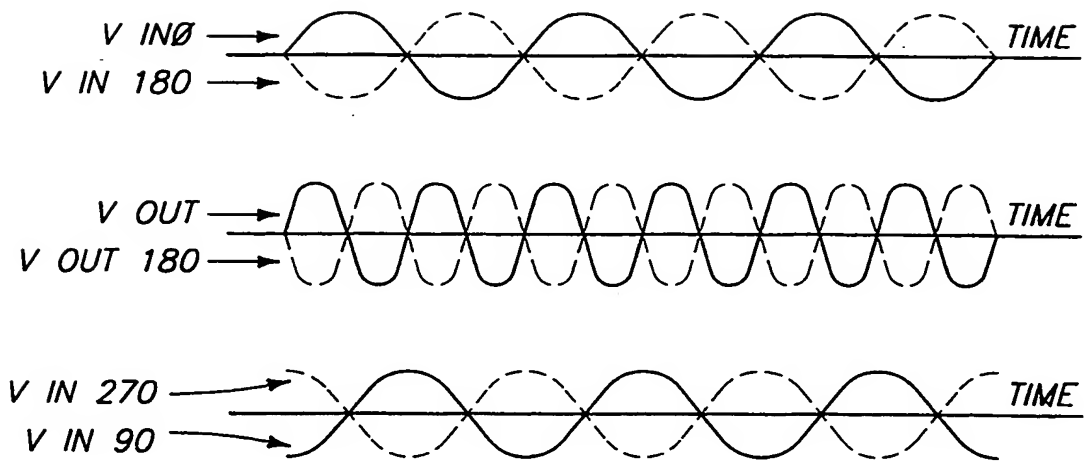
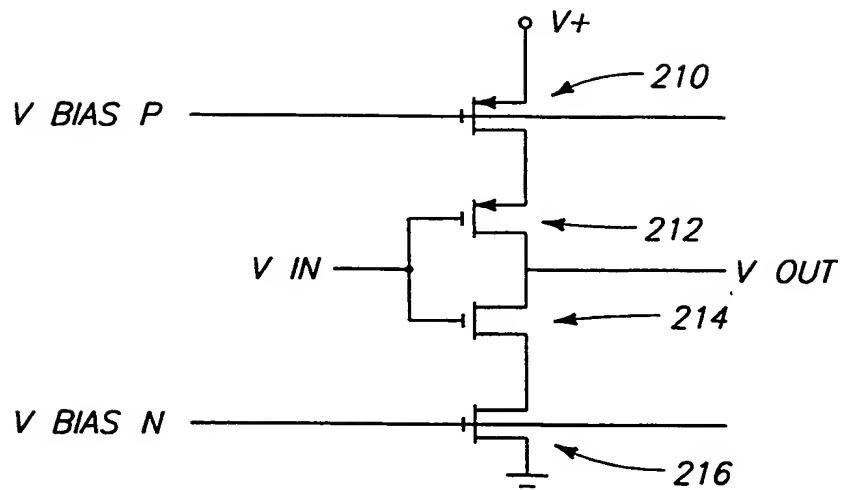


FIG. 33

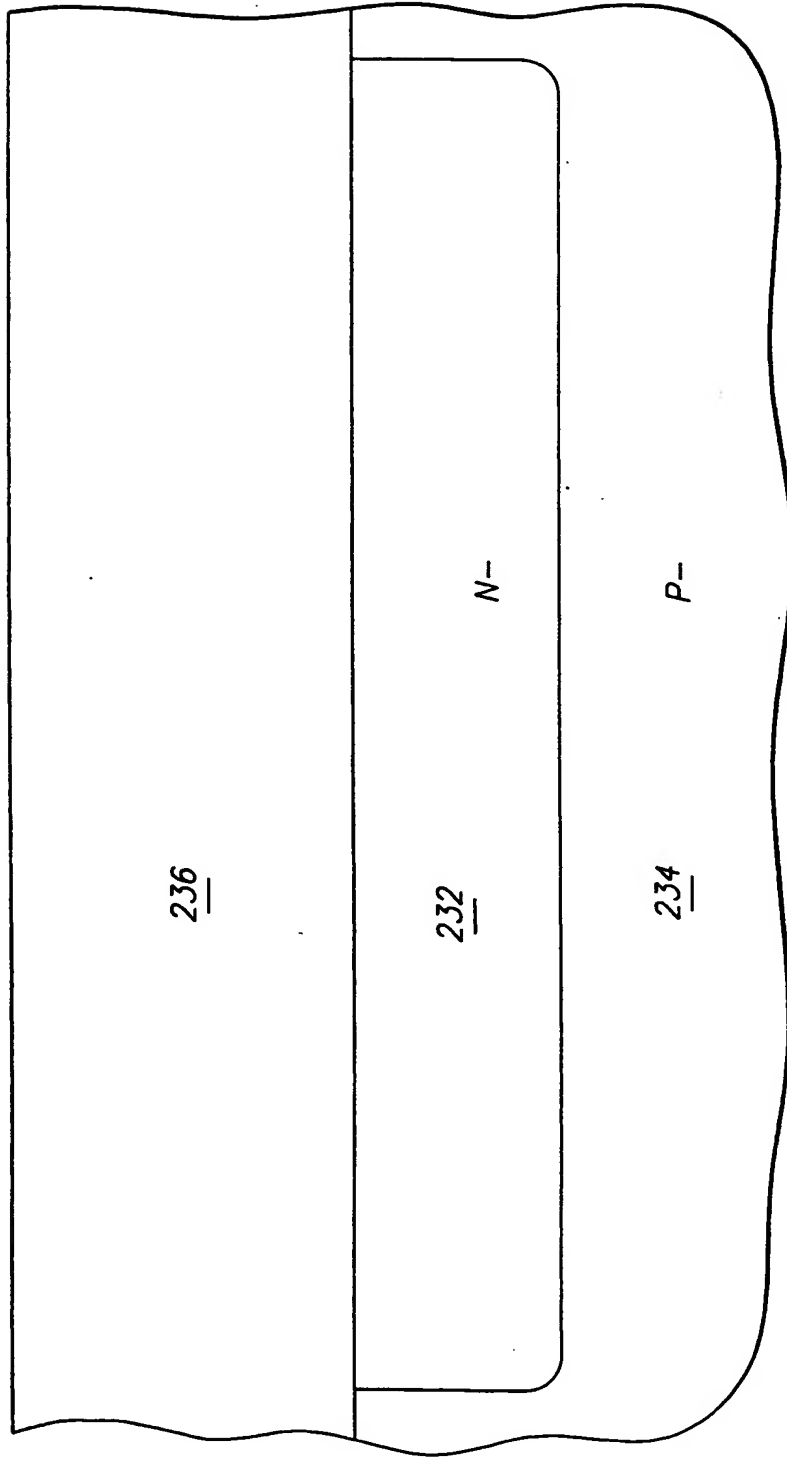


IEEE Std

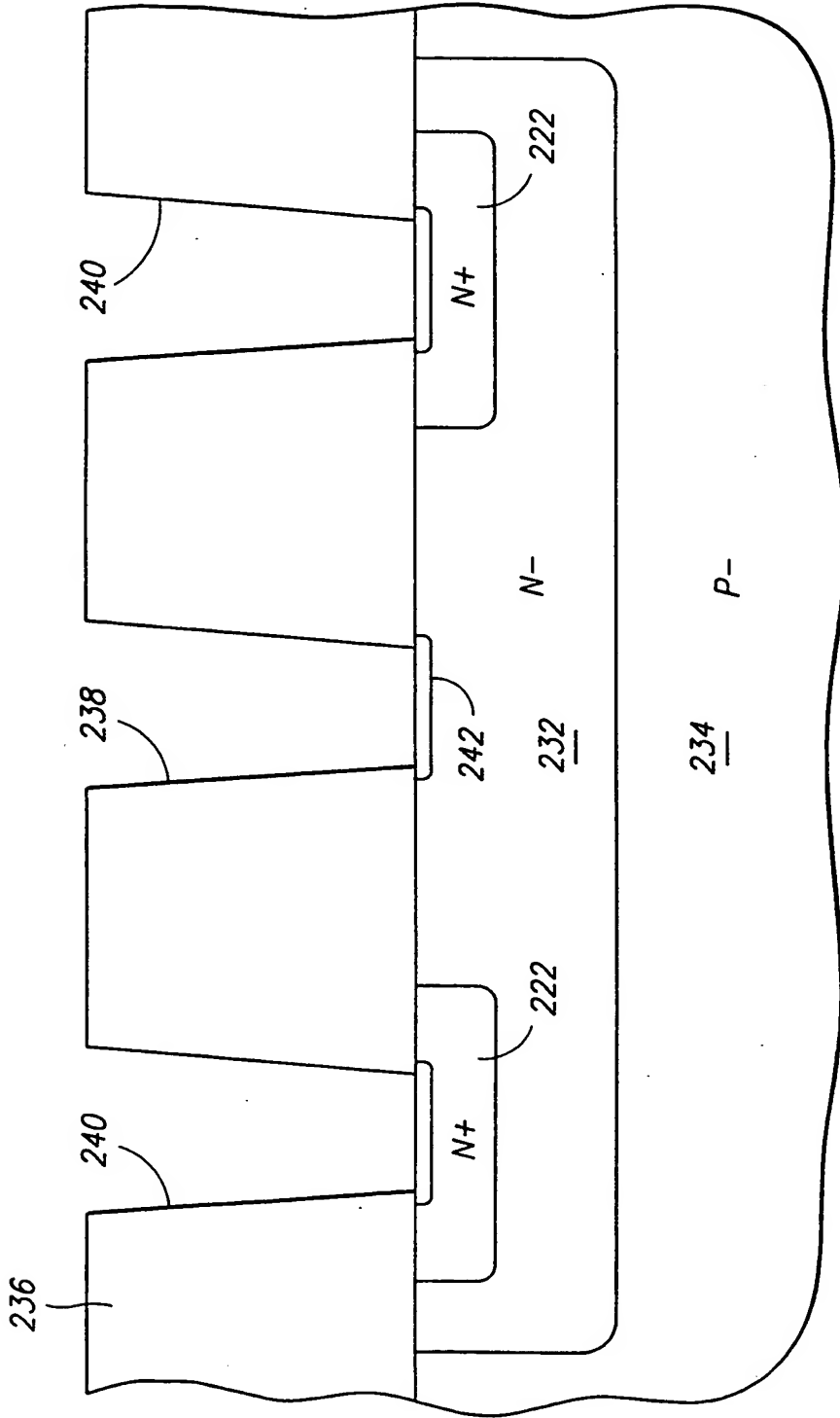




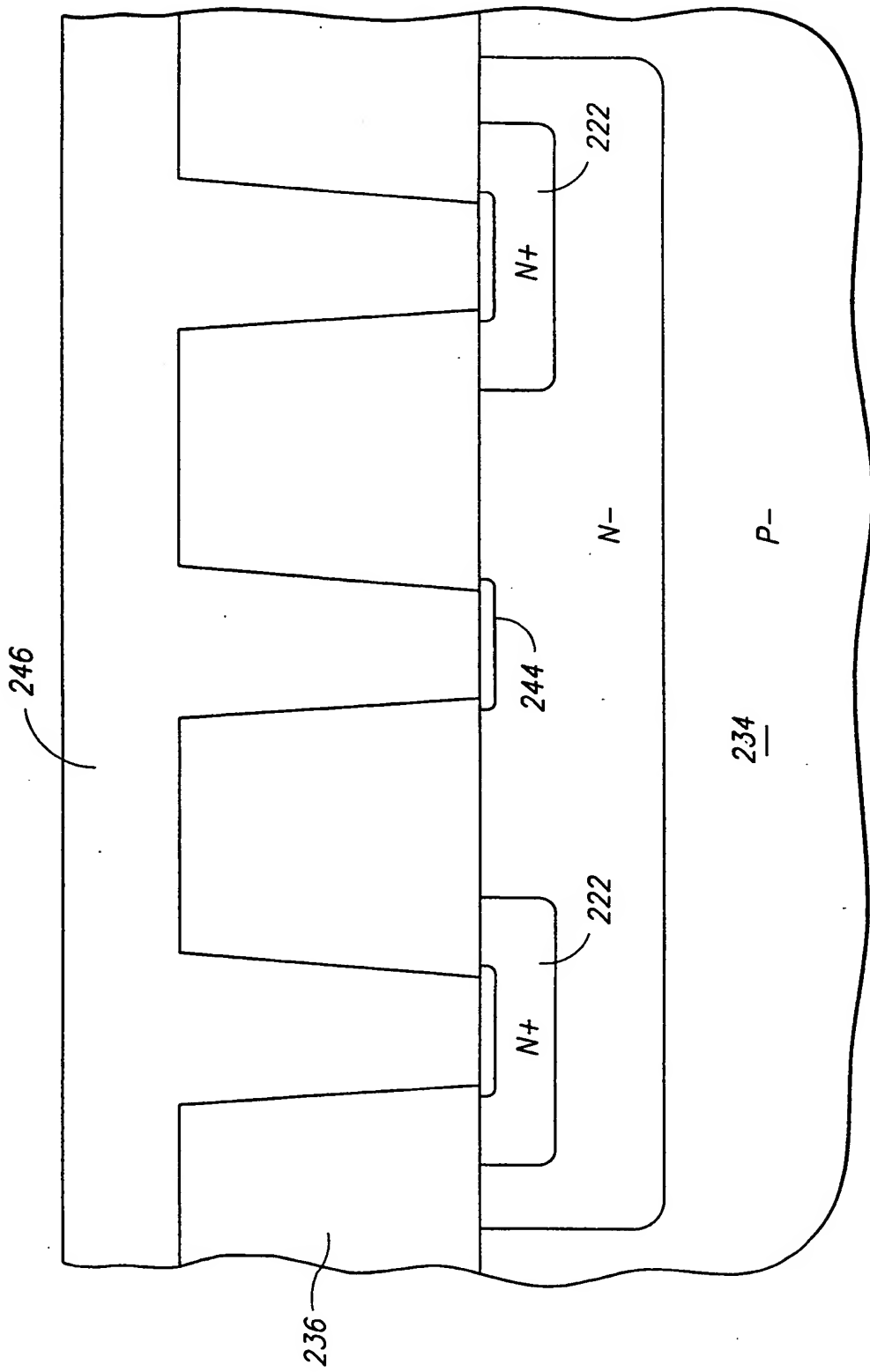
IEEE



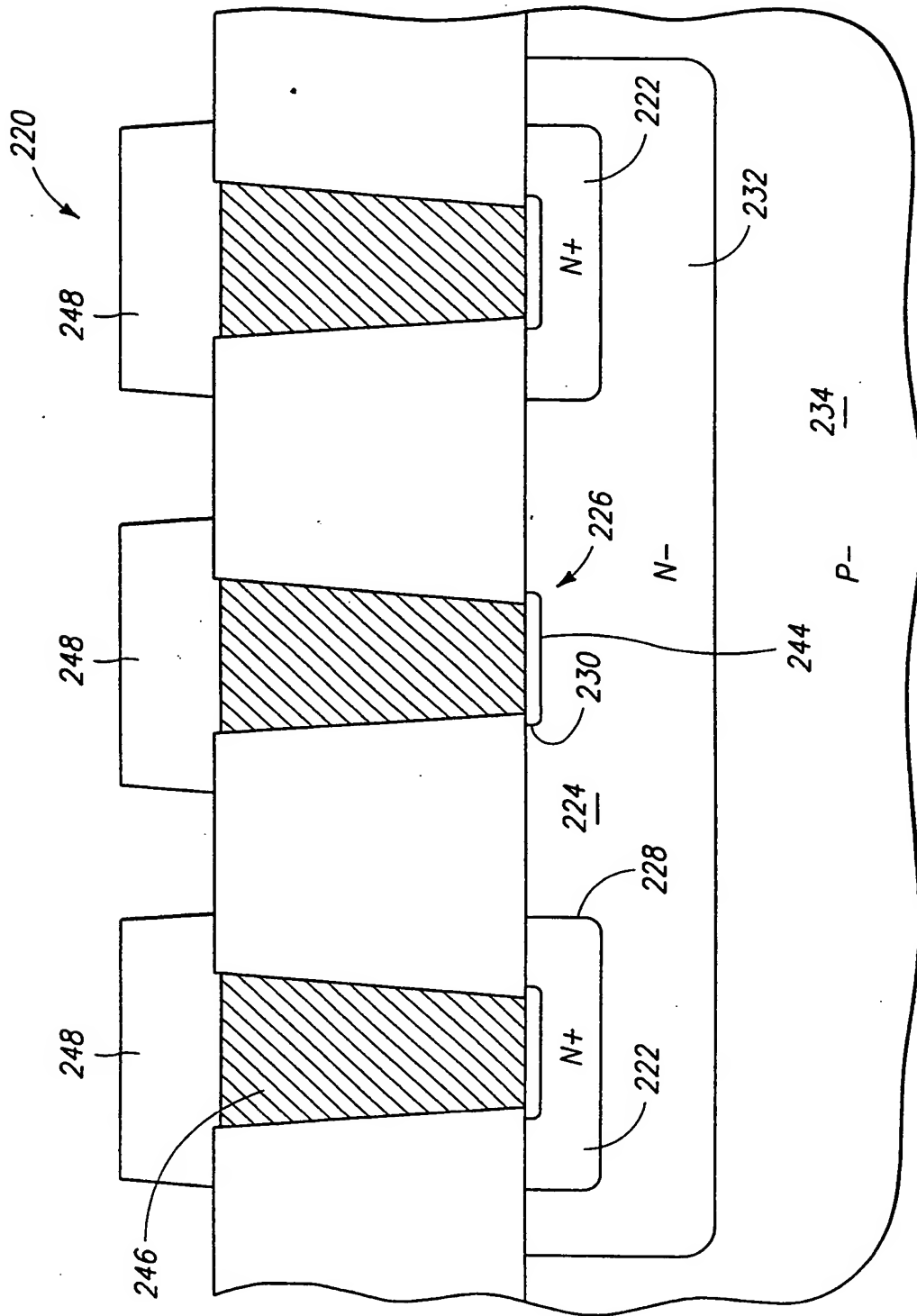
II II II III

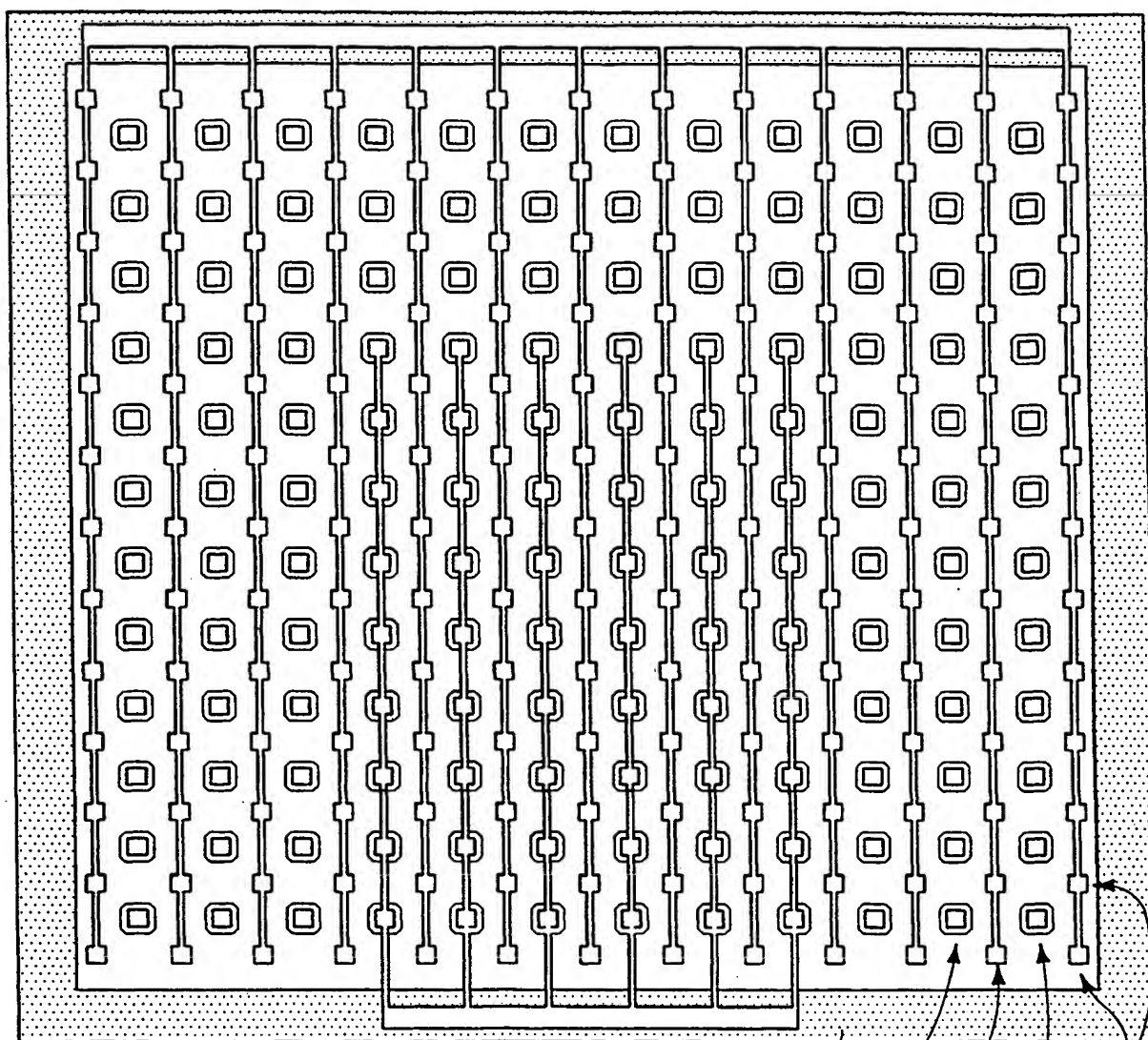


SEE FIG. 3



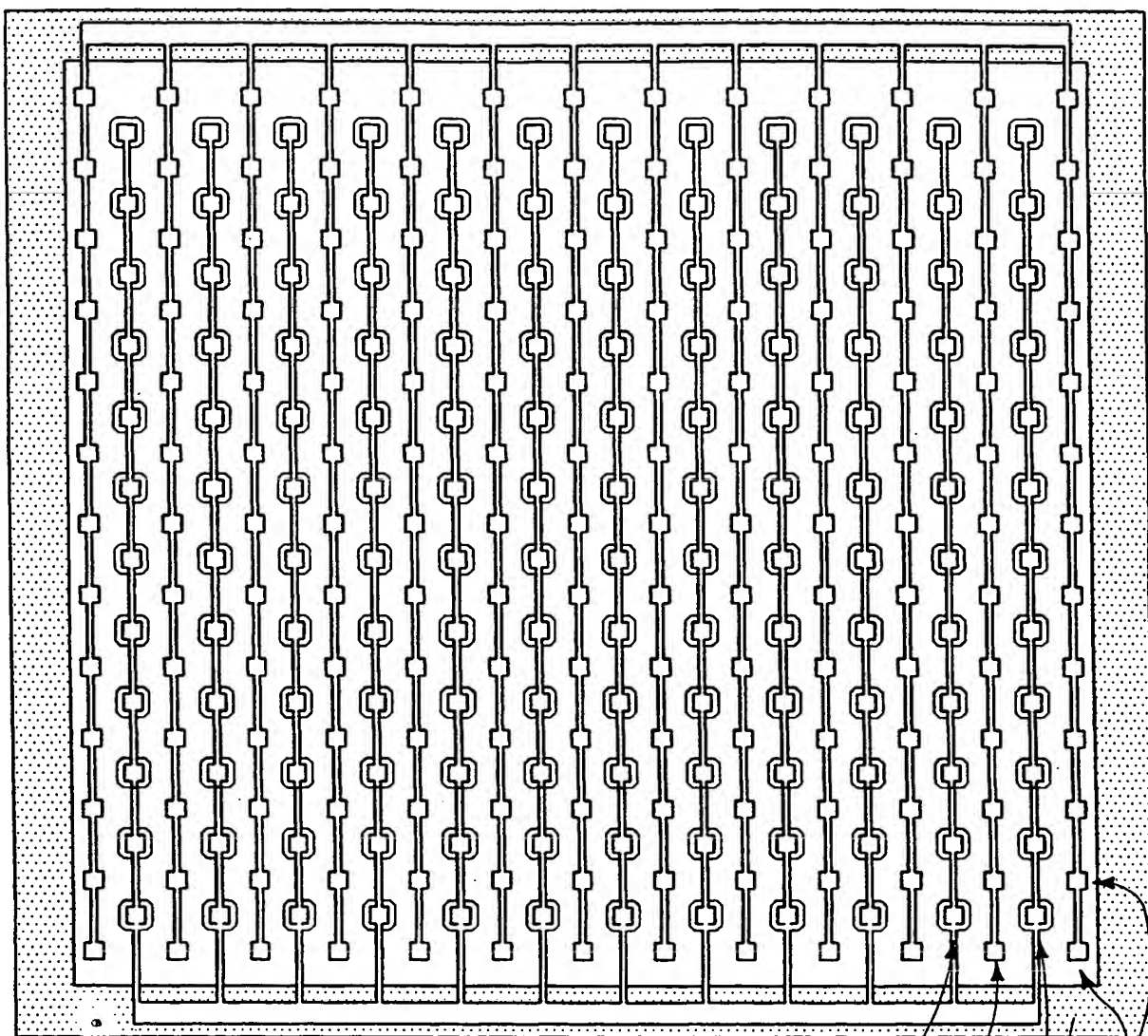
MI 40-030





231 238 222 224 222

FIG. 4



238
224
222
231
222

FIG. 4

260

258

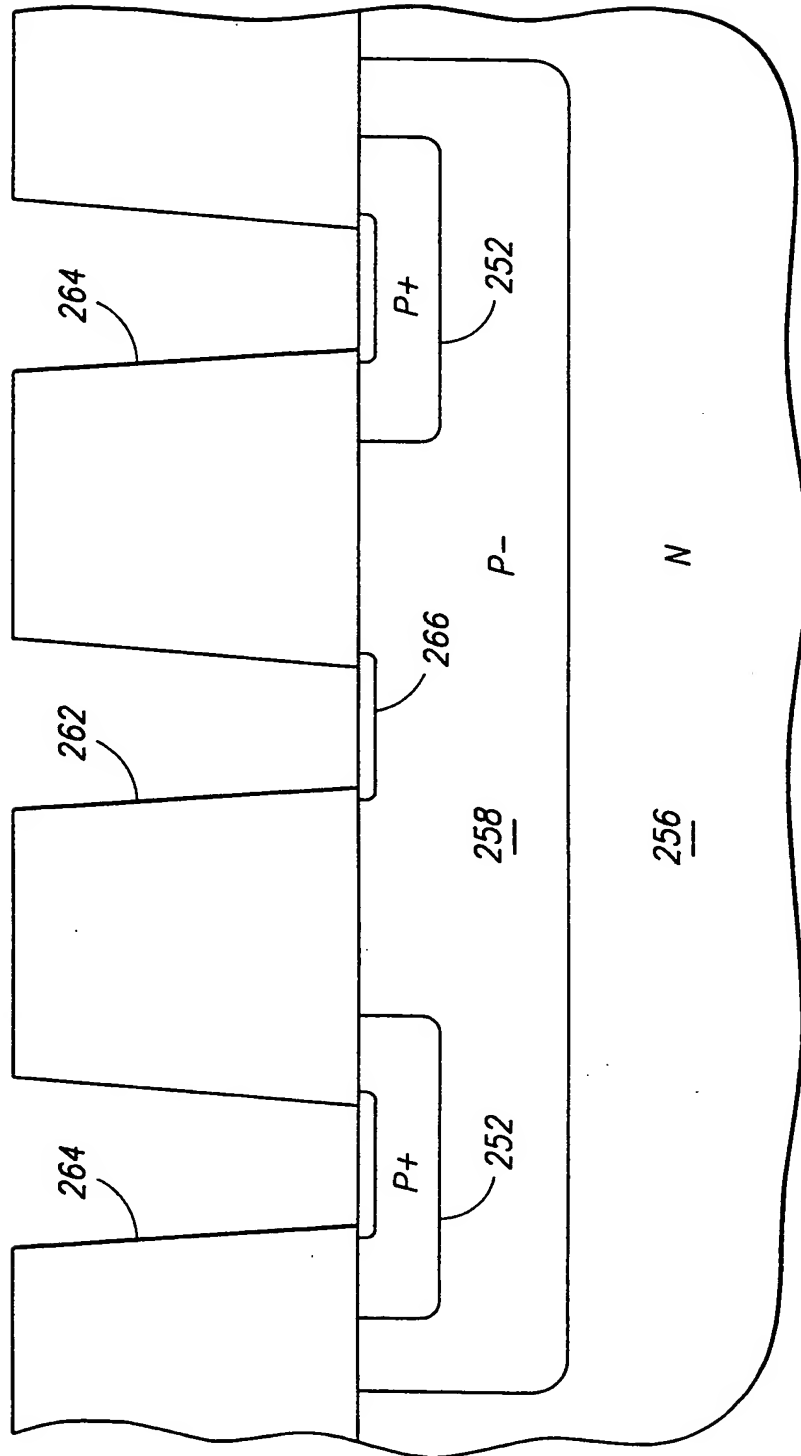
P-

256

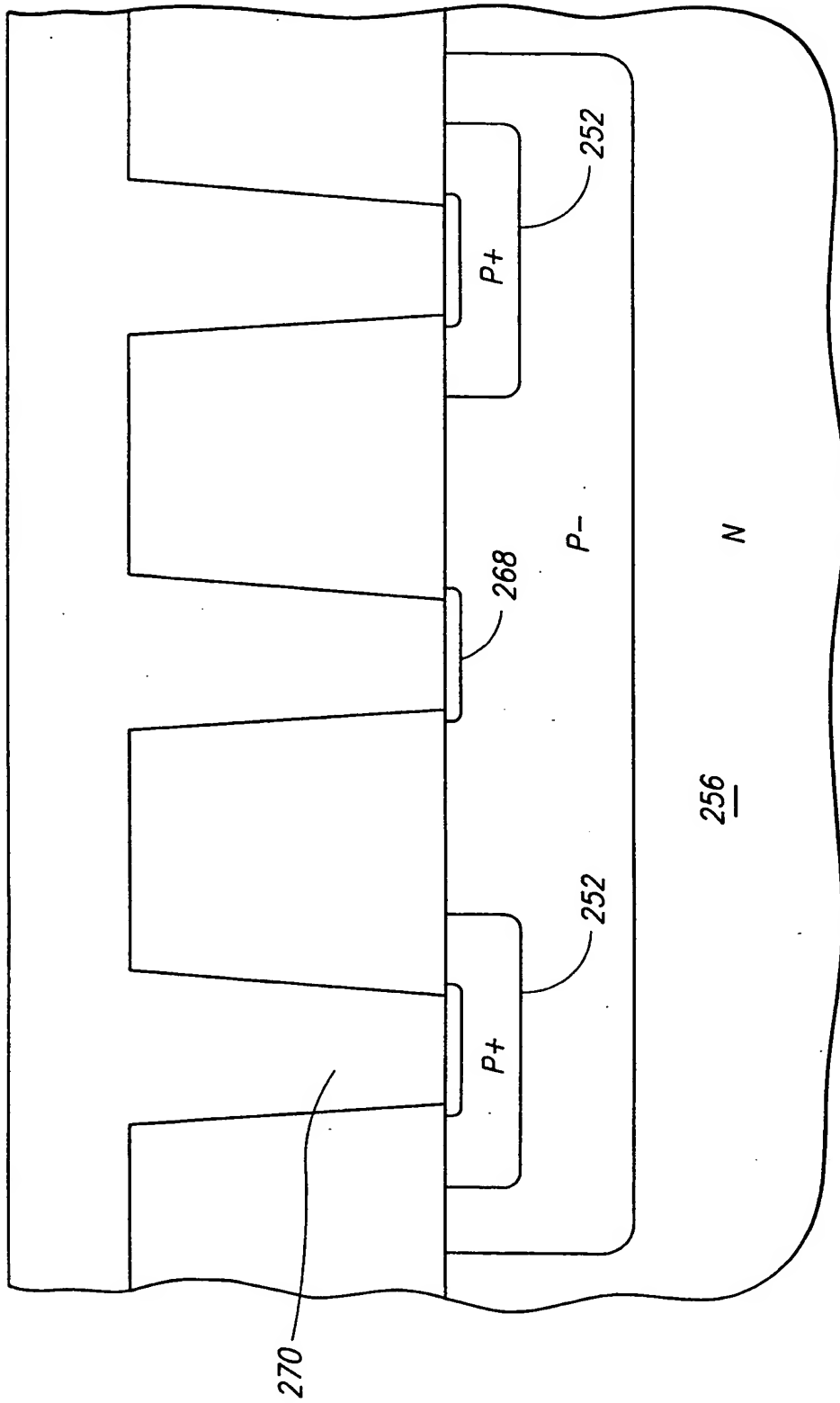
N

9.

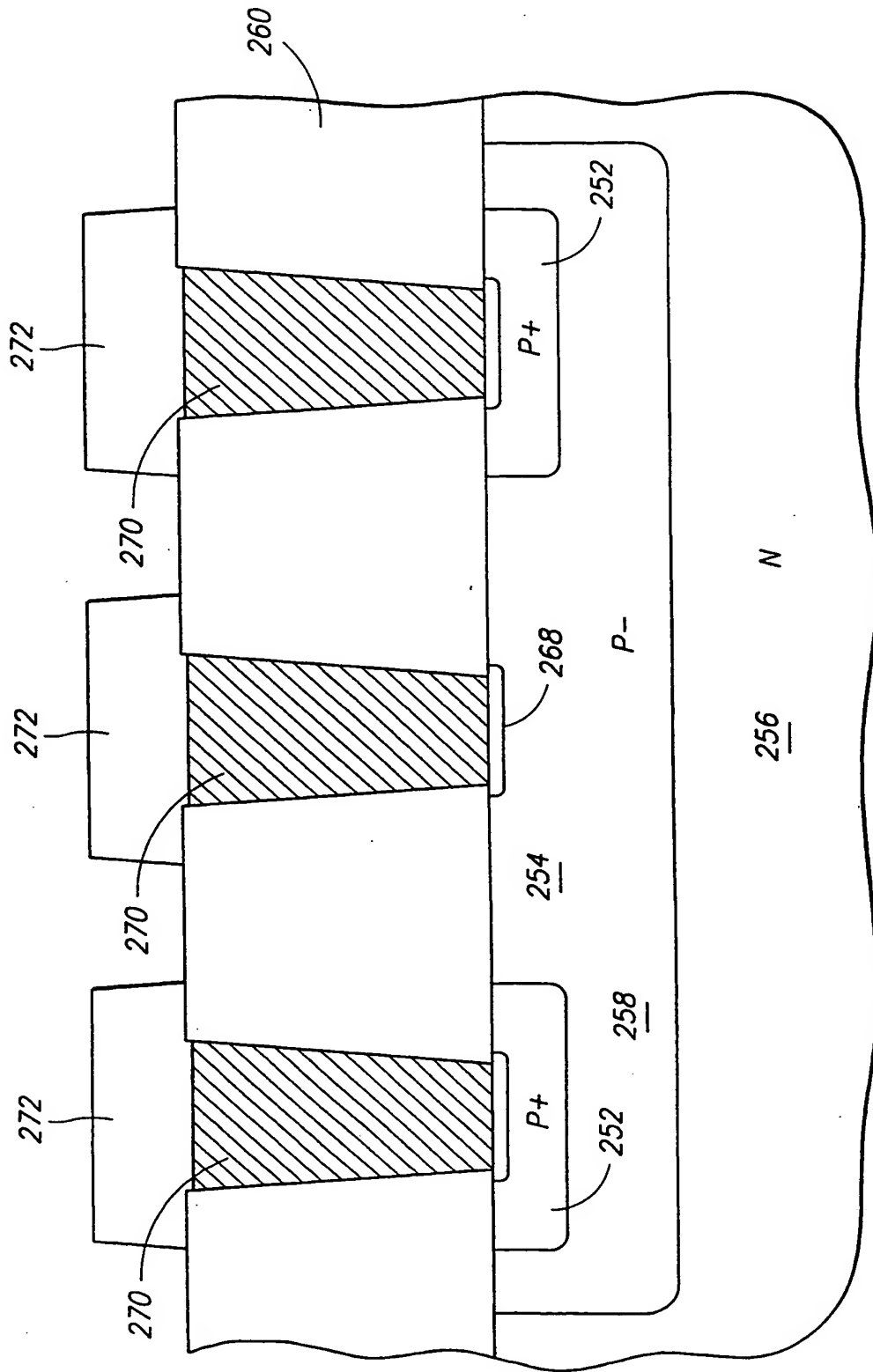
$$\geq$$
Толд



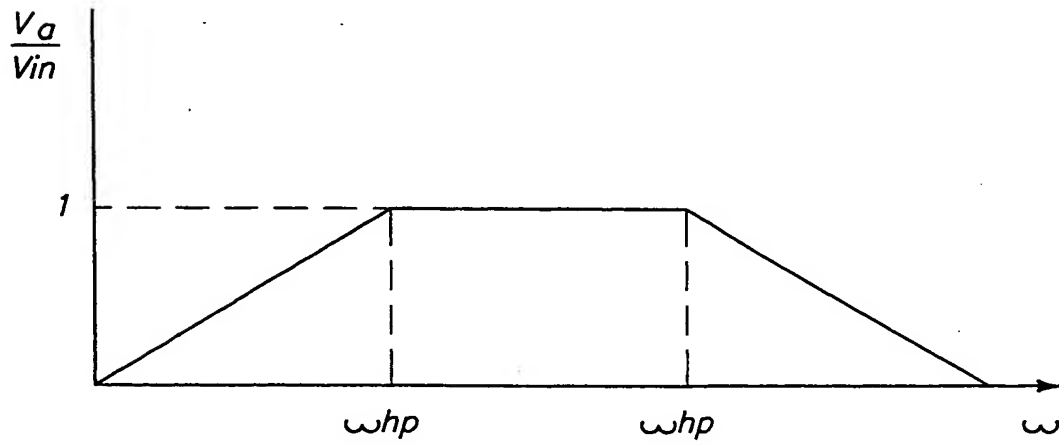
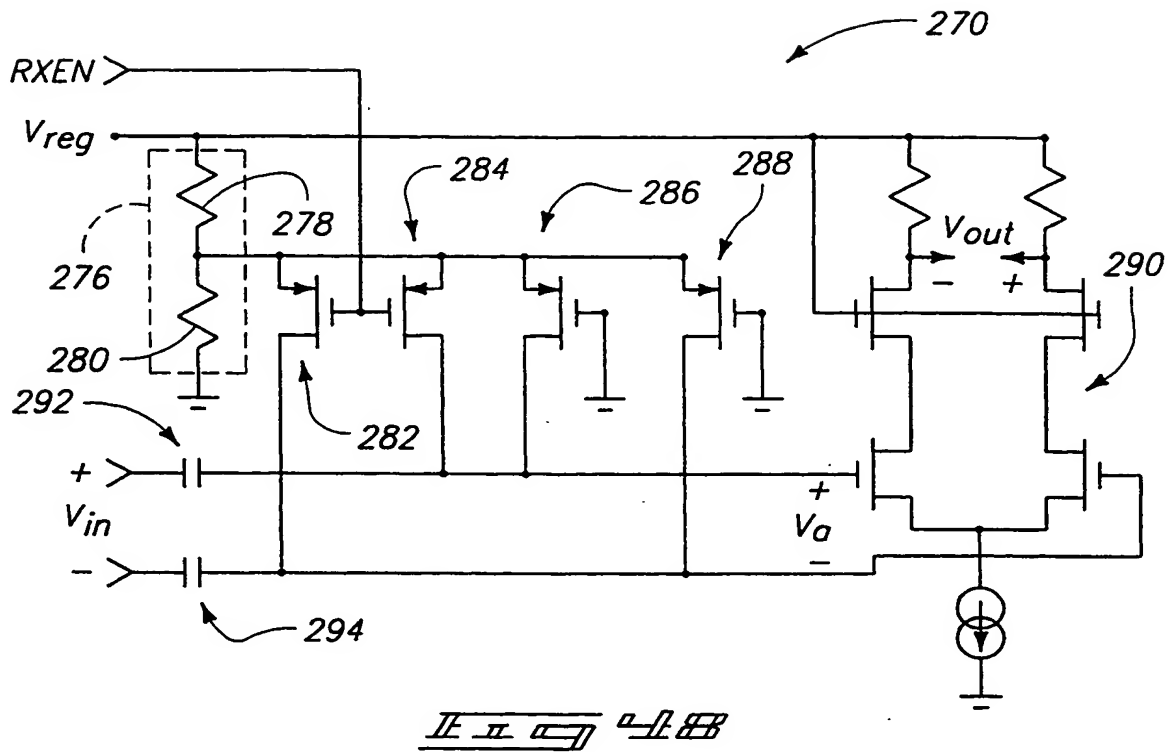
MI 40-030



MI 40



II II II II II



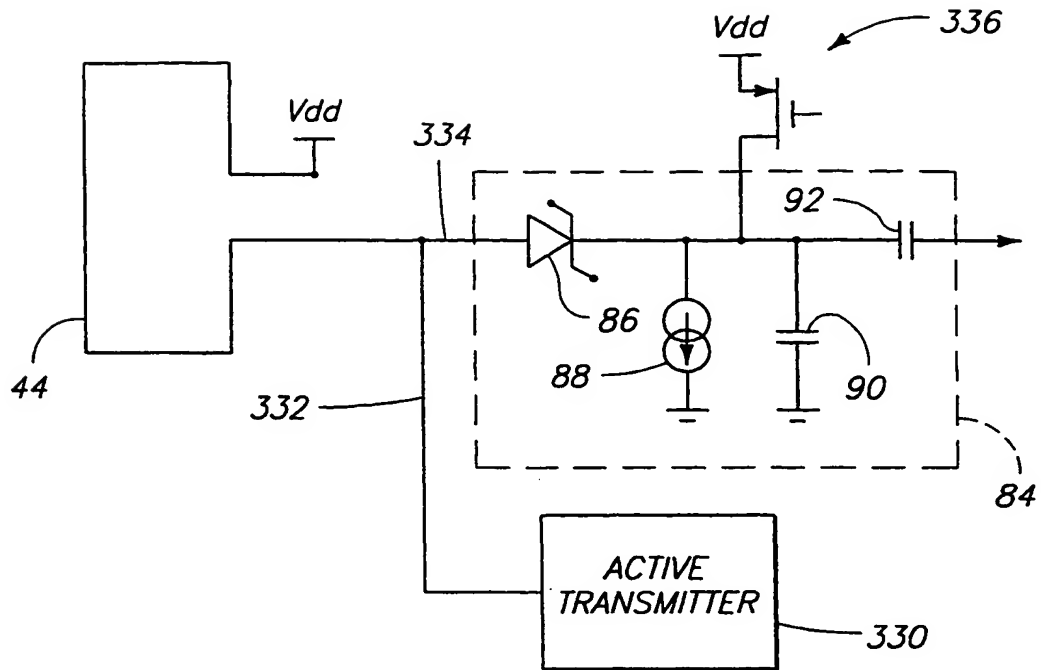


FIG 50

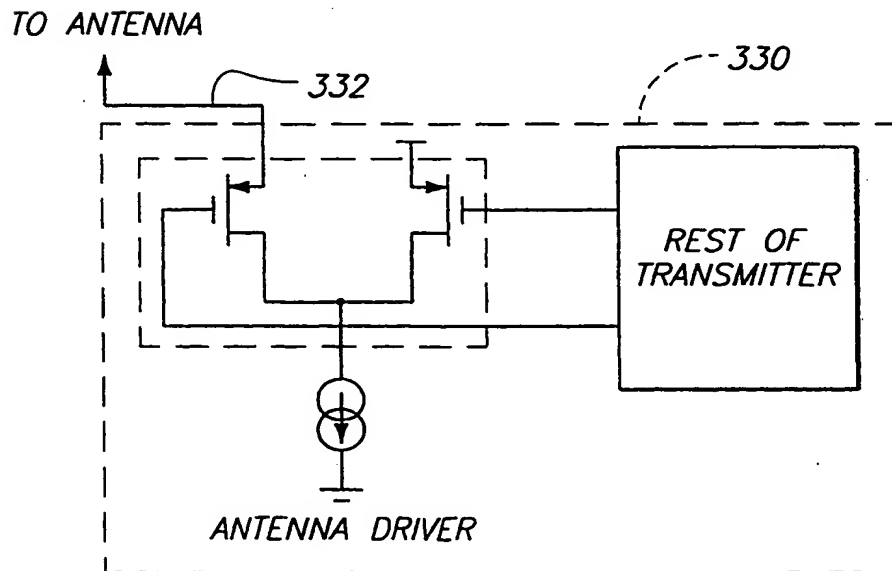


FIG 51

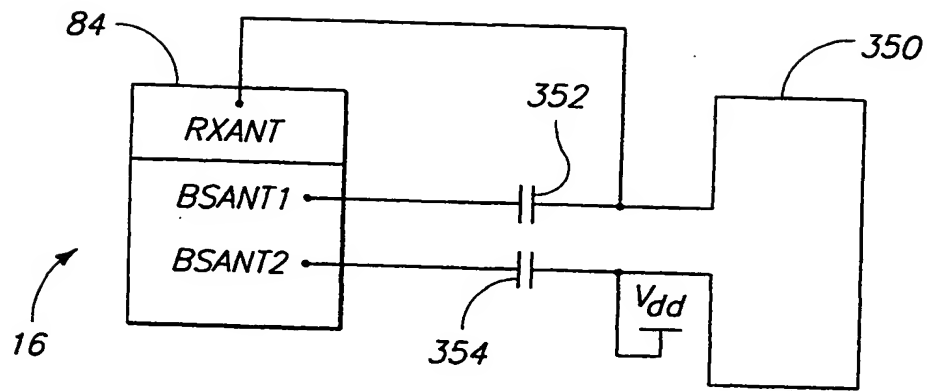


FIG. 52

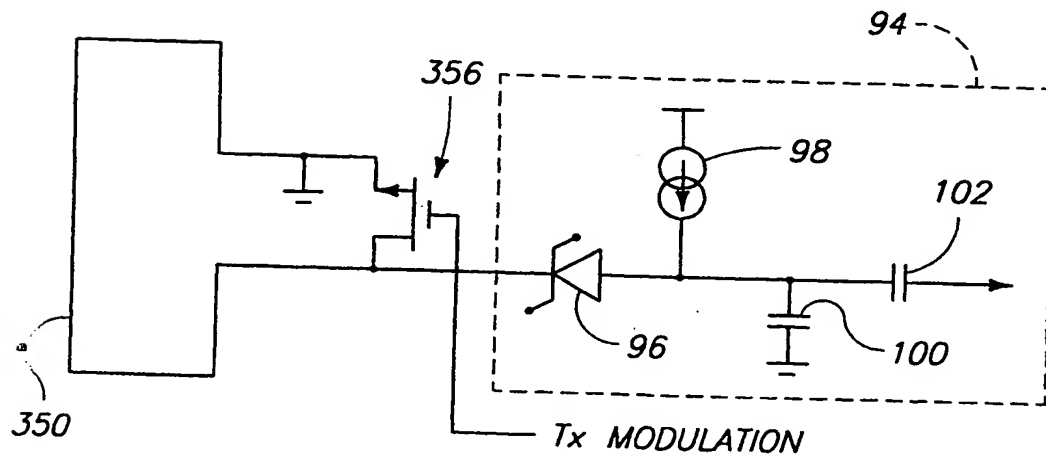
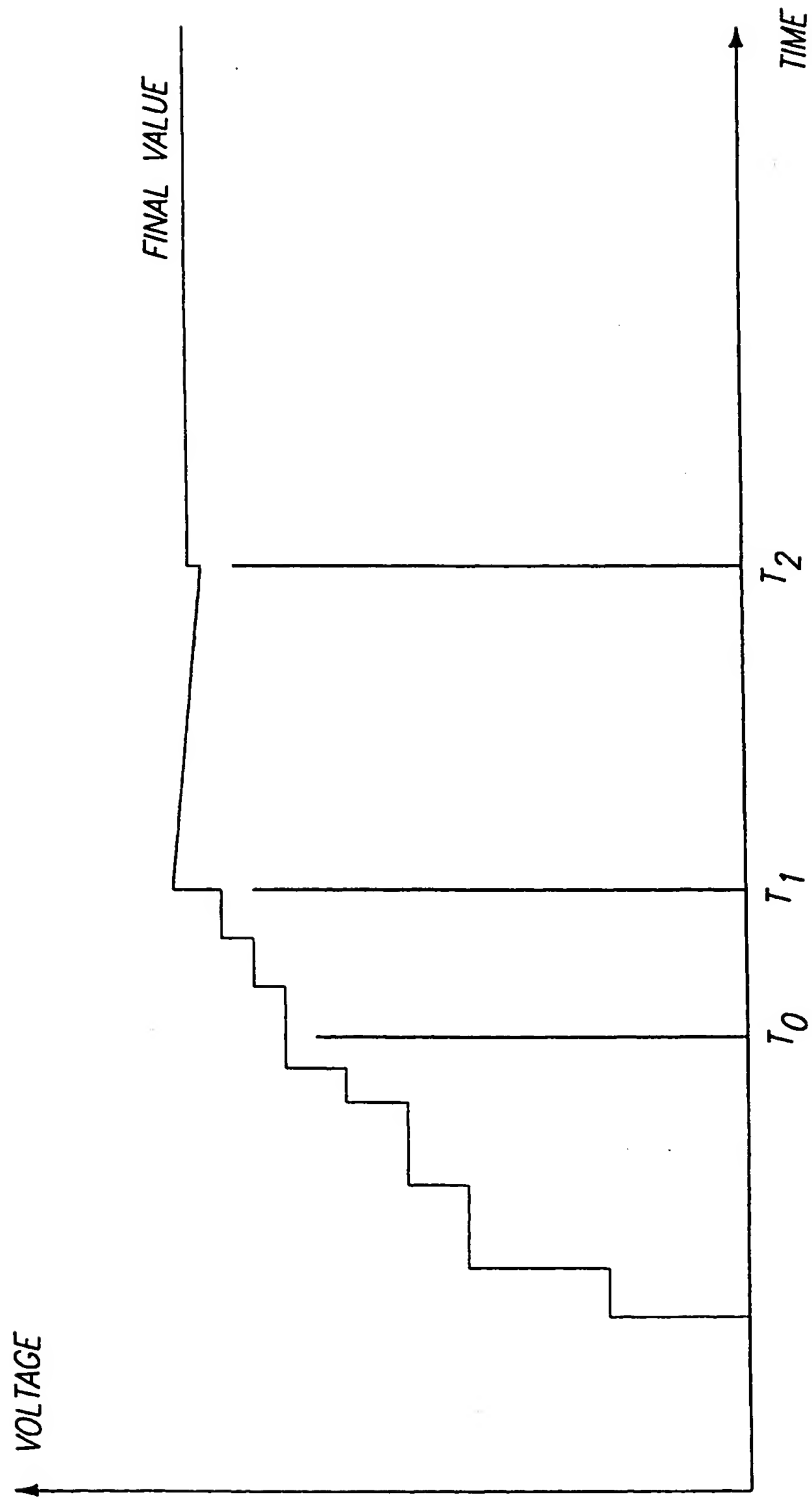
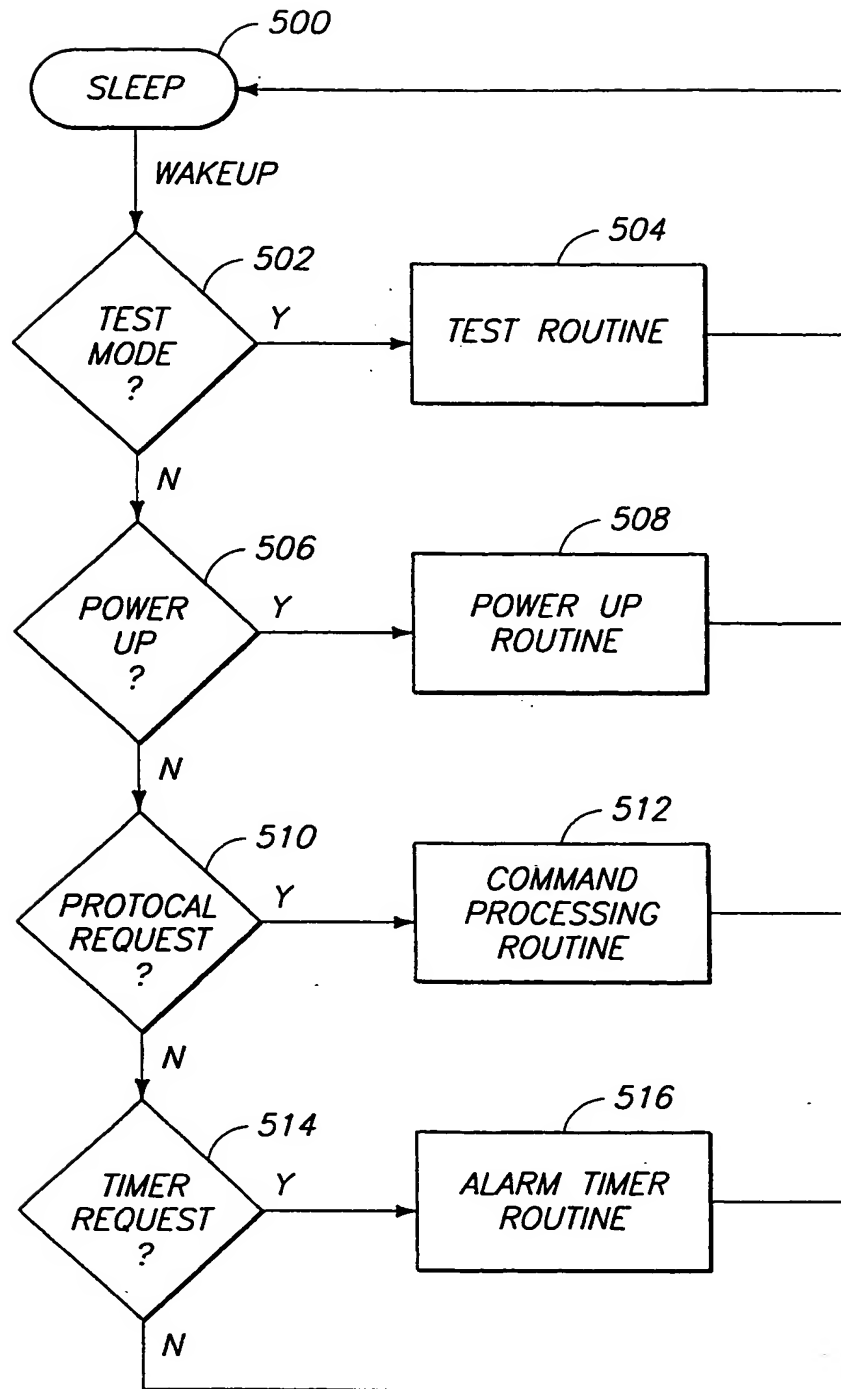
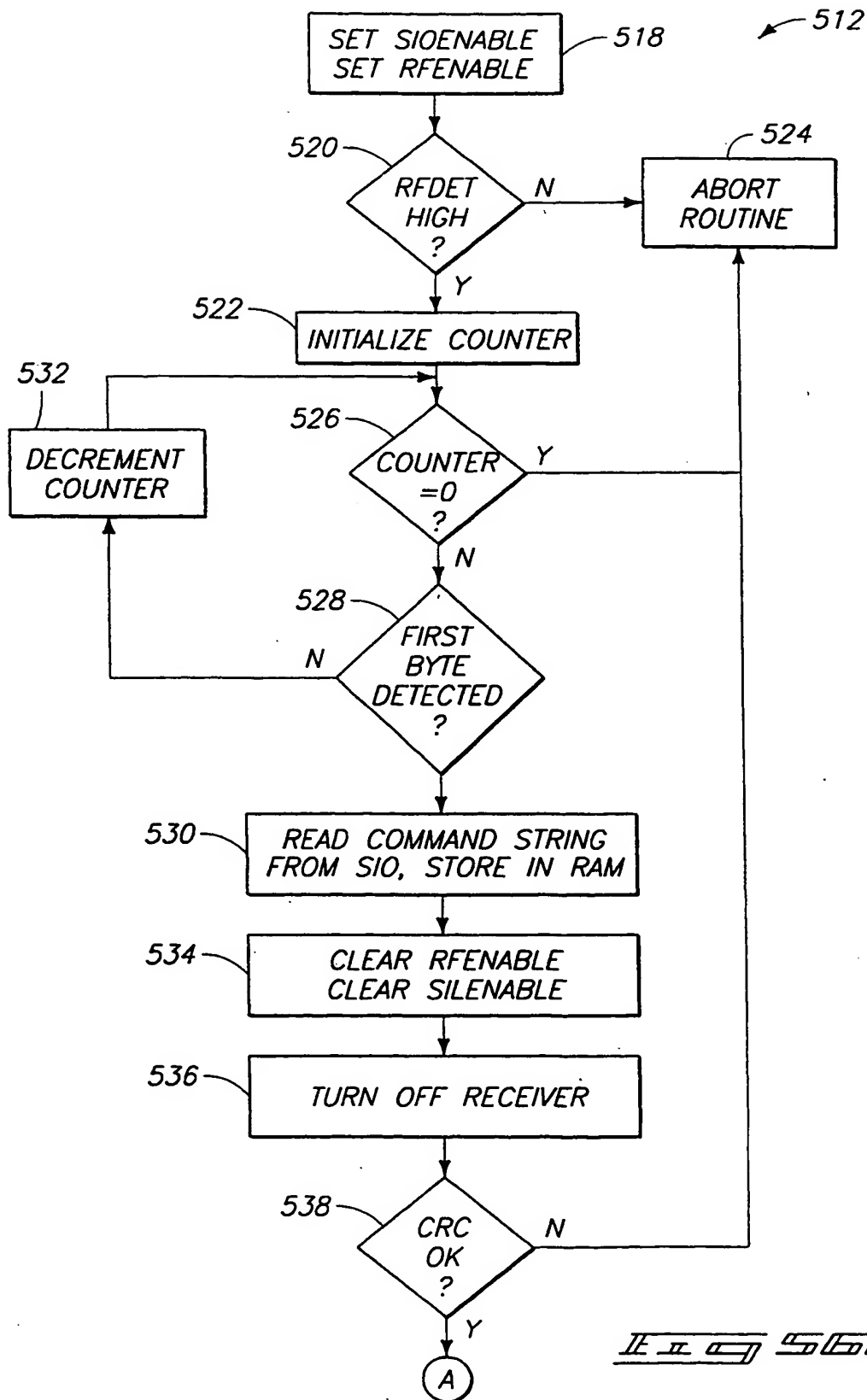


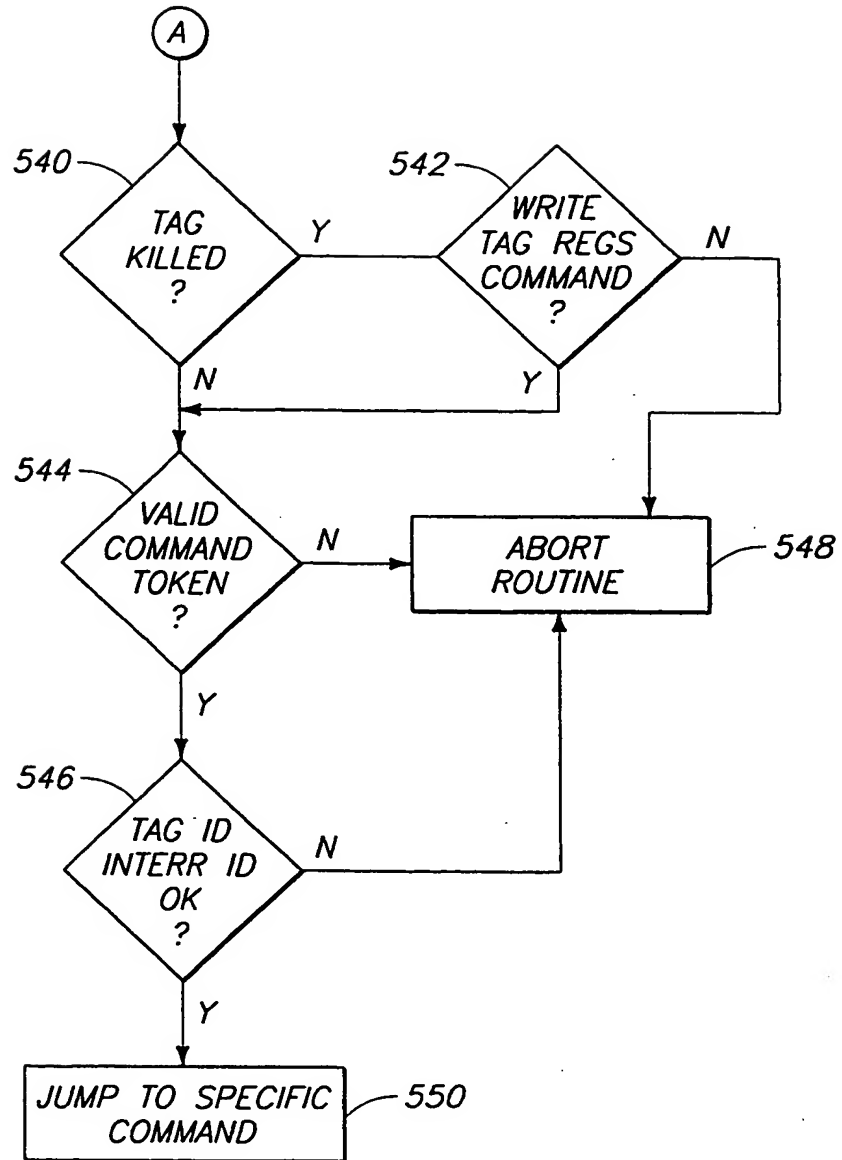
FIG. 53

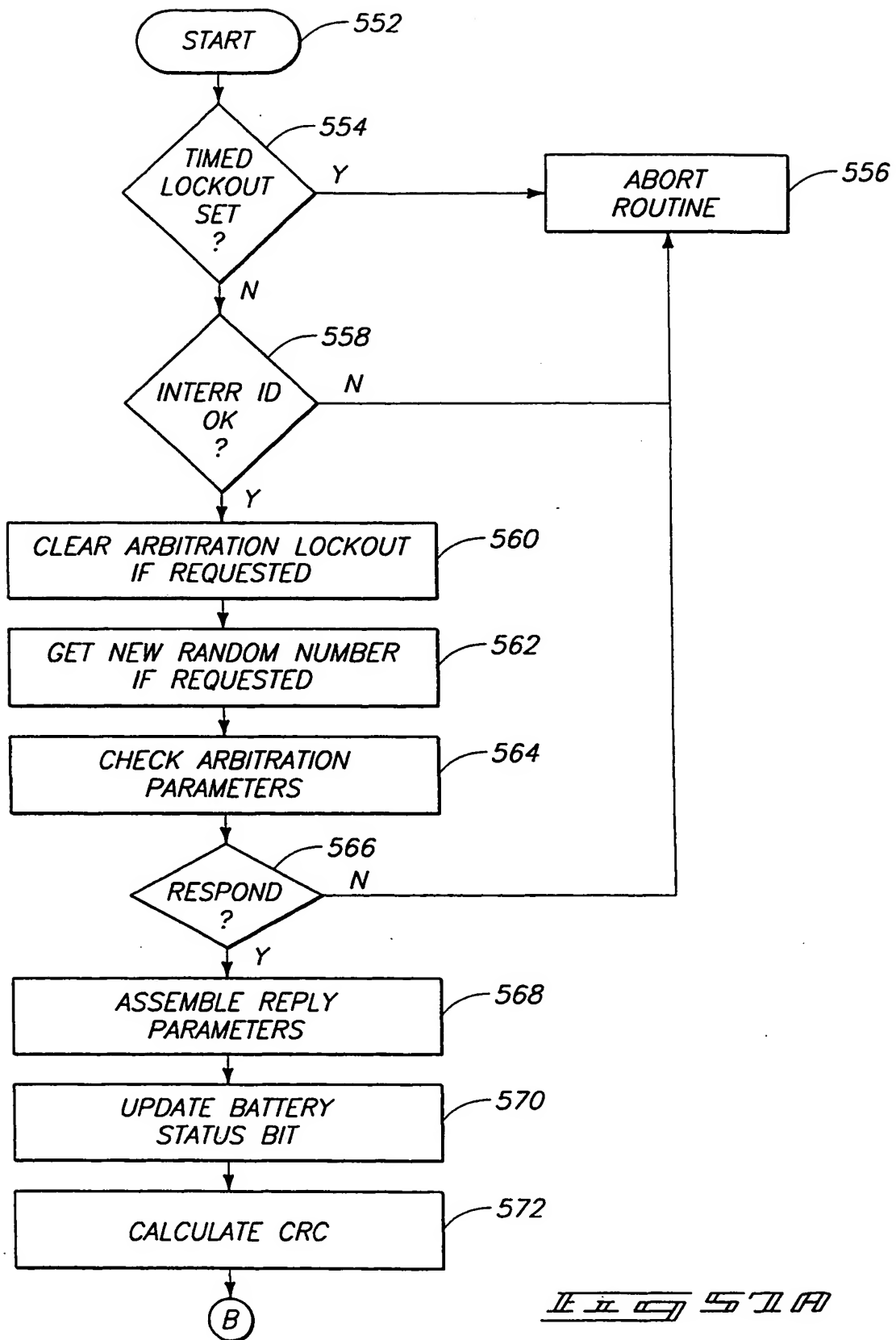


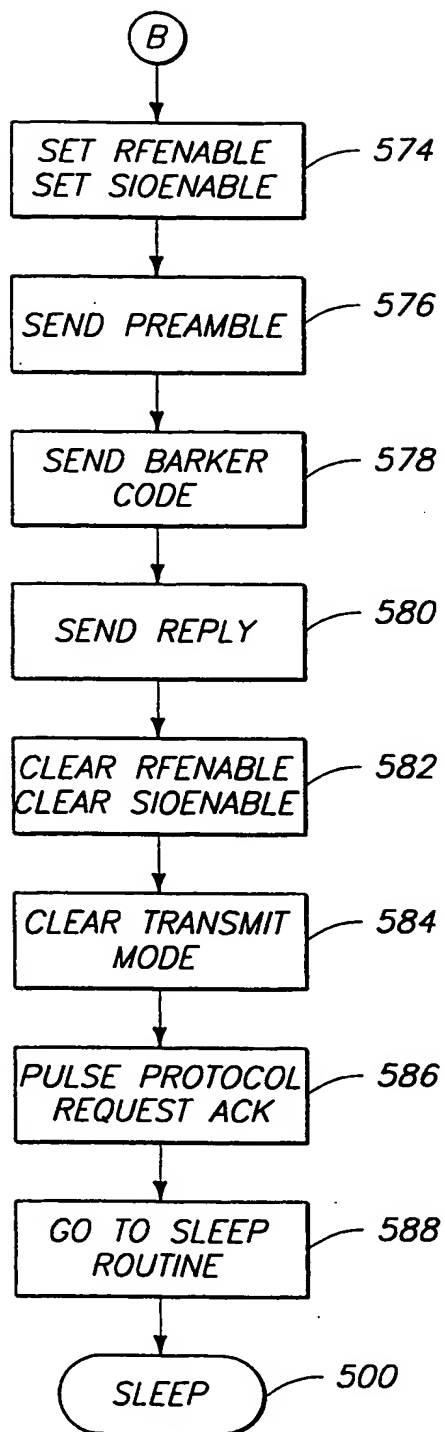
TEST DATA

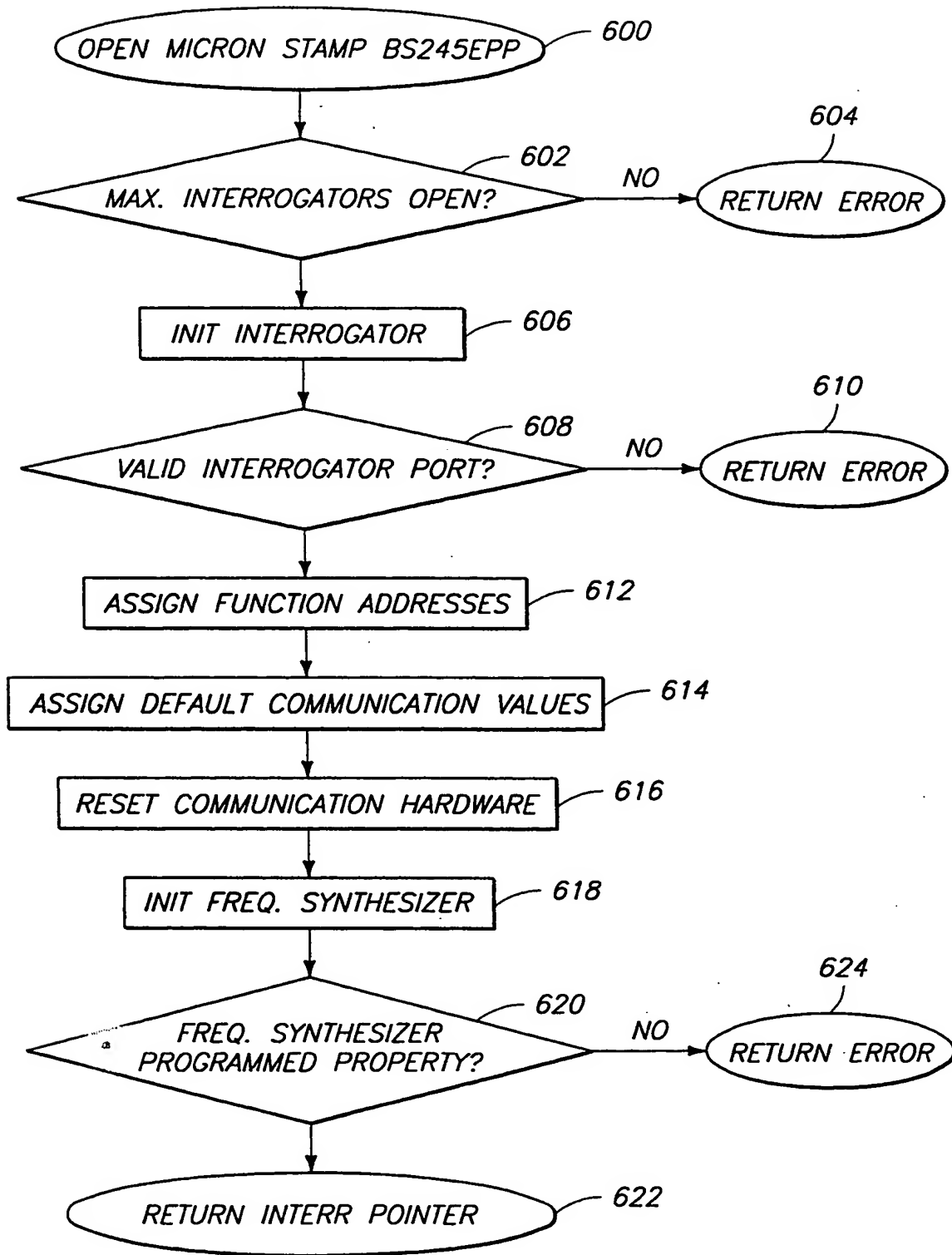


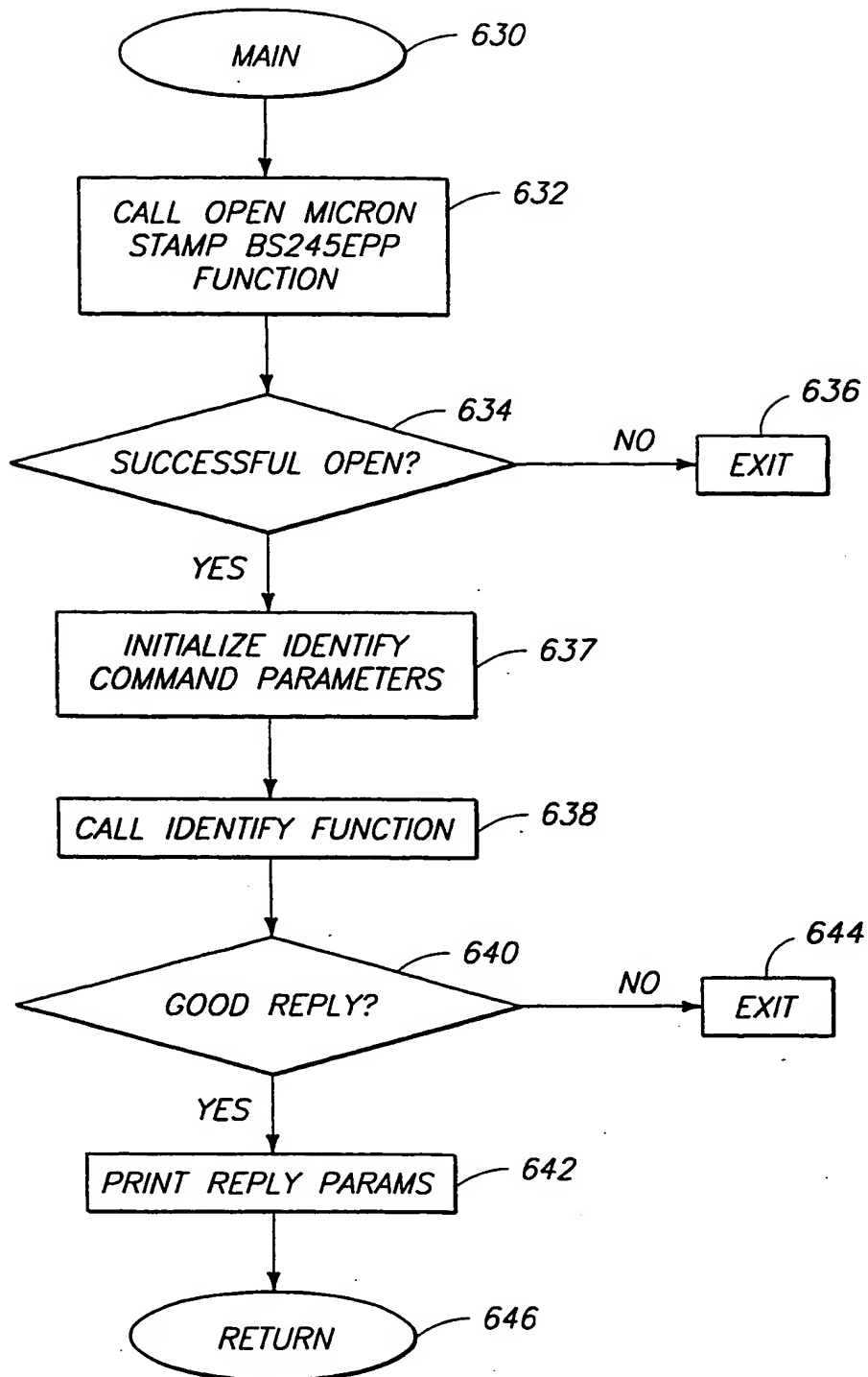
IEEE 56A

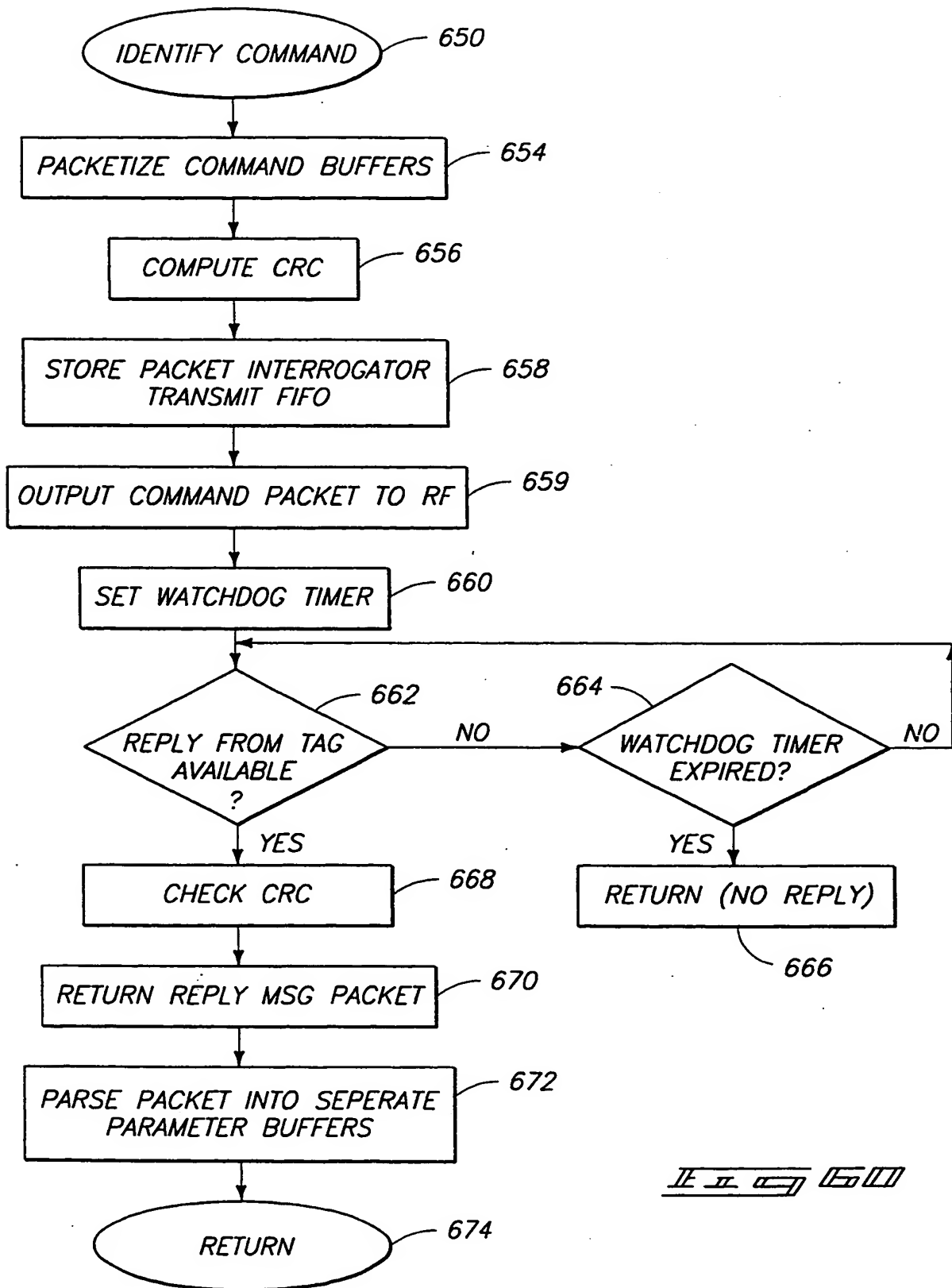
IEEE 56B

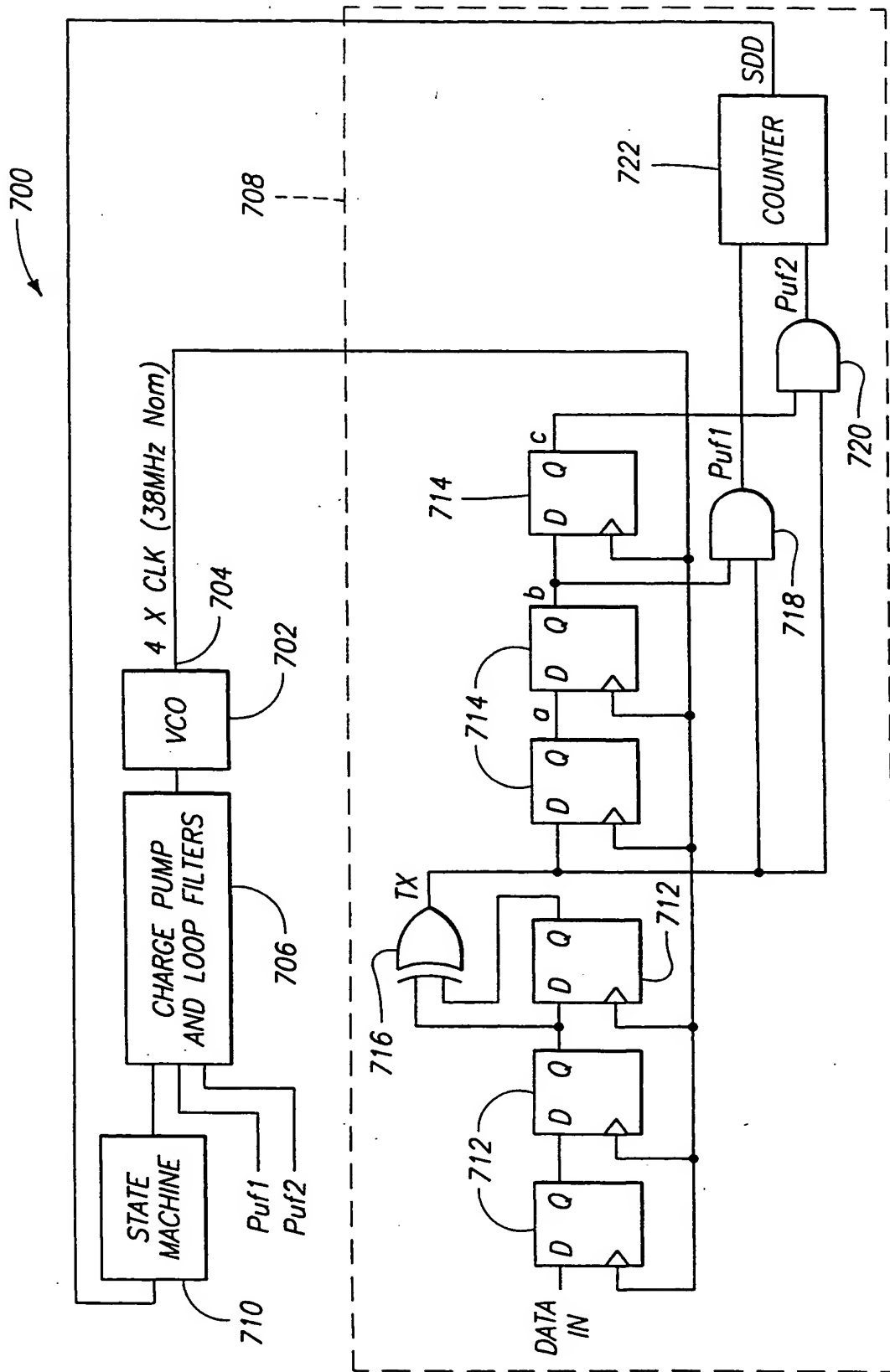








II 5 60



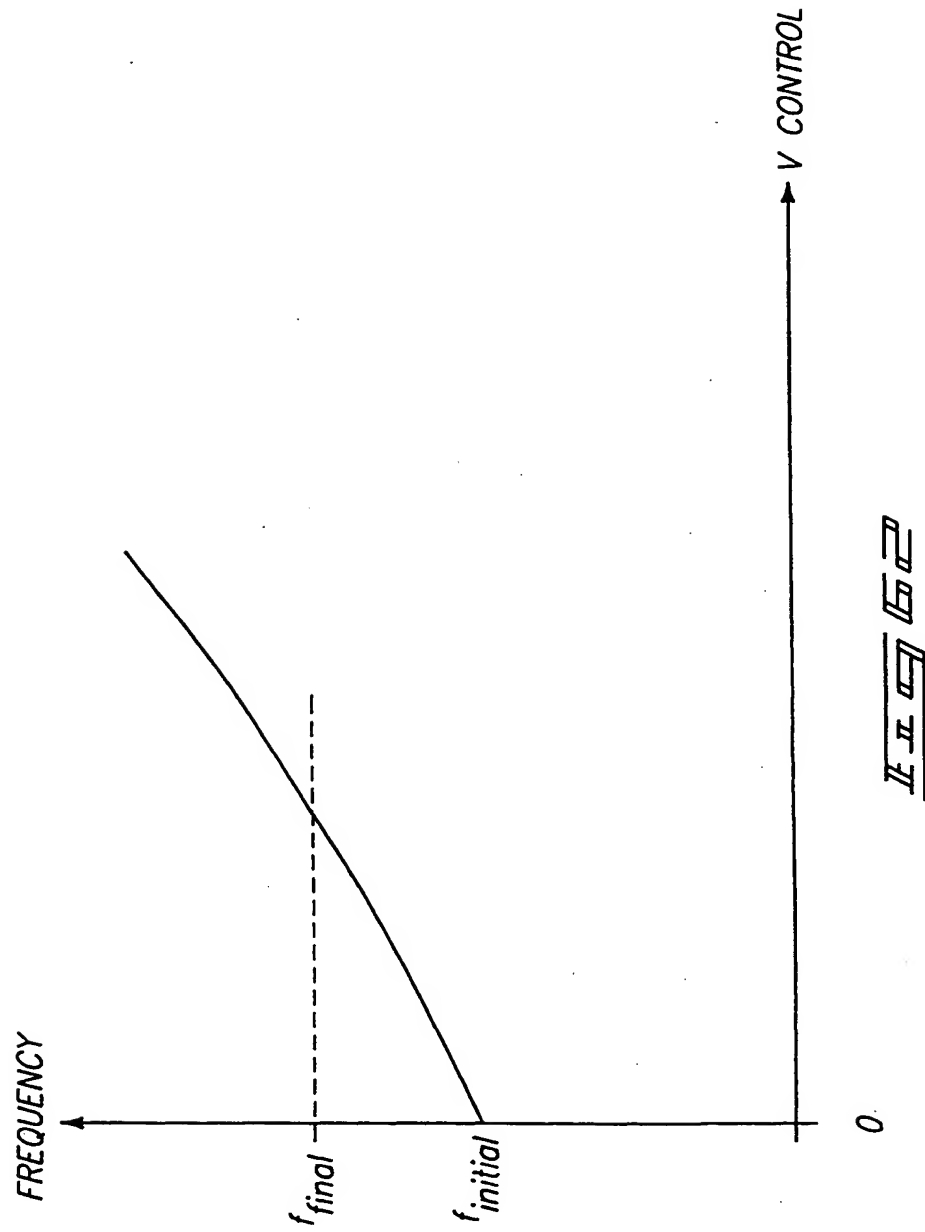
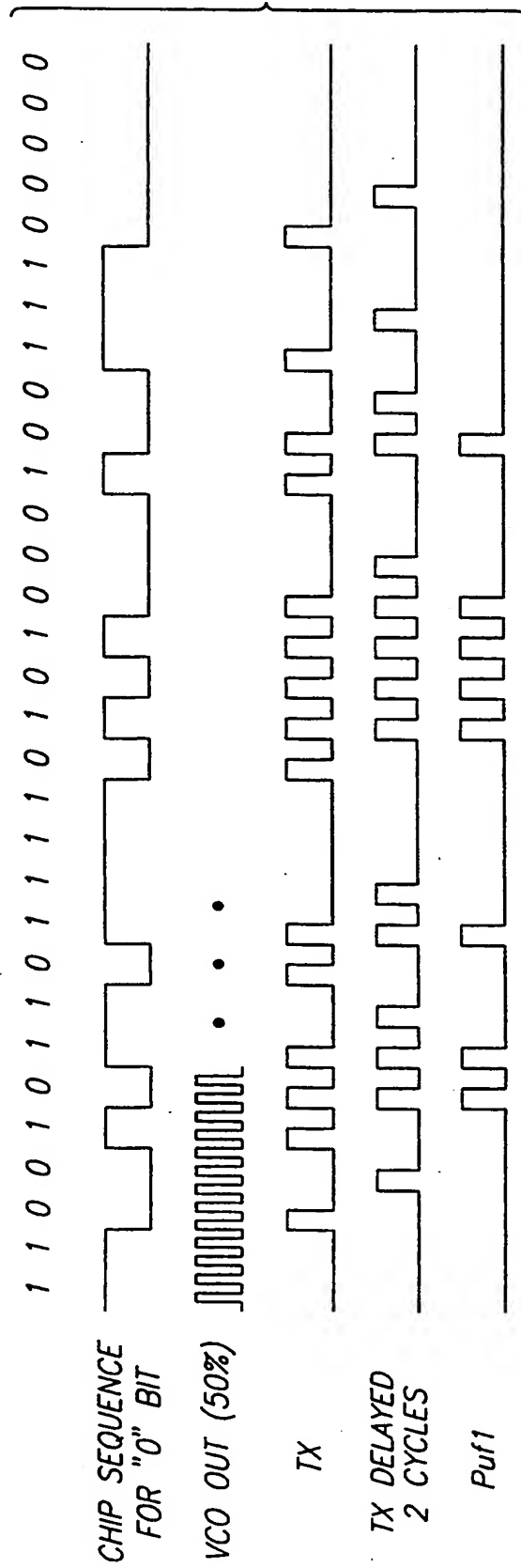
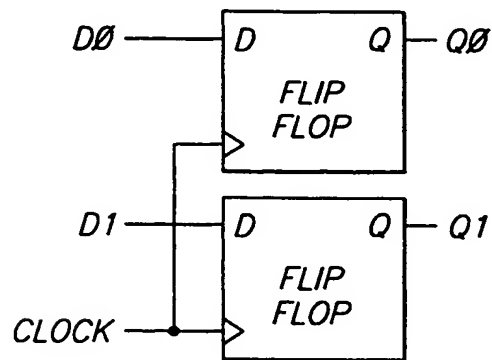
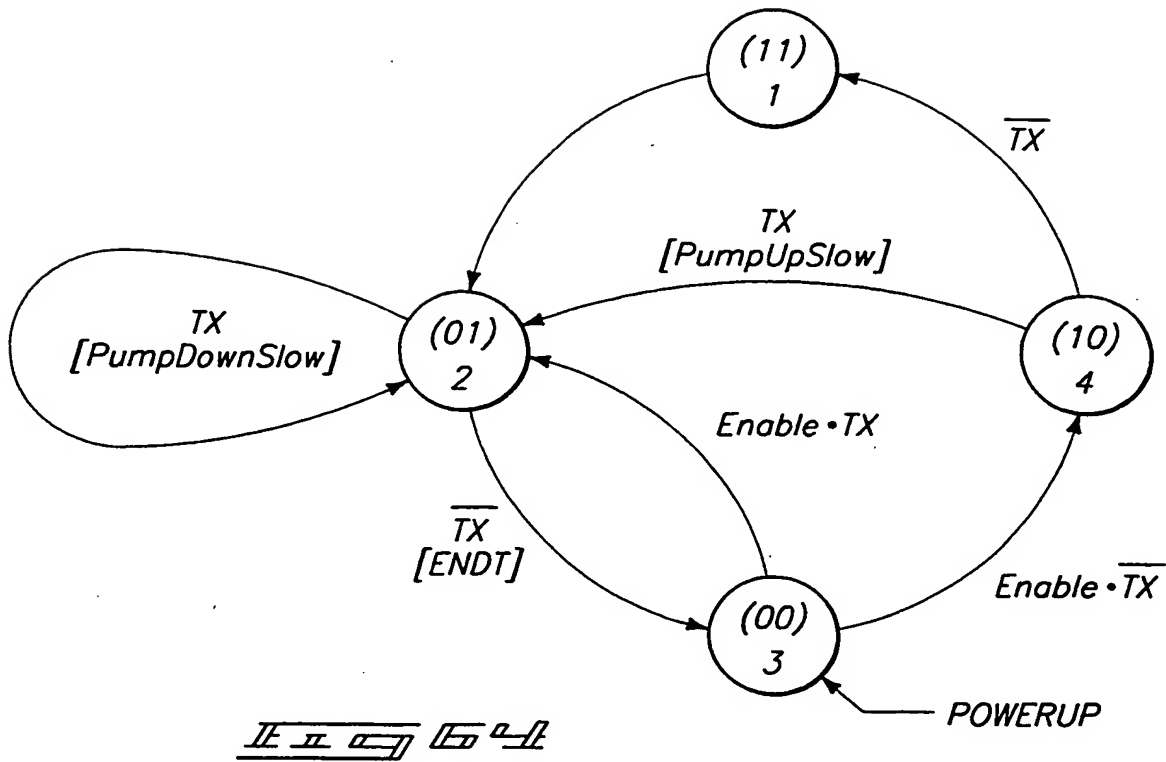


Fig 62

IEEE



PRESENT STATE				NEXT STATE	
ENABLE	TX	Q1	Q0	D1	D0
0	0	0	0	0	0
0	1	0	0	0	0
1	0	0	0	1	0
1	1	0	0	0	1
X	0	0	1	0	0
X	1	0	1	0	1
X	X	1	1	0	1
X	0	1	0	1	1
X	1	1	0	0	1

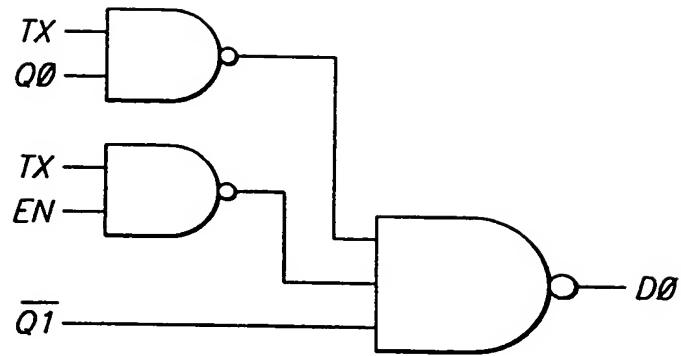
EX 66

En TX		Q1 Q0			
		00	01	11	10
D0:	00	0	0	1	1
	01	0	1	1	1
	11	1	1	1	1
	10	0	0	1	1

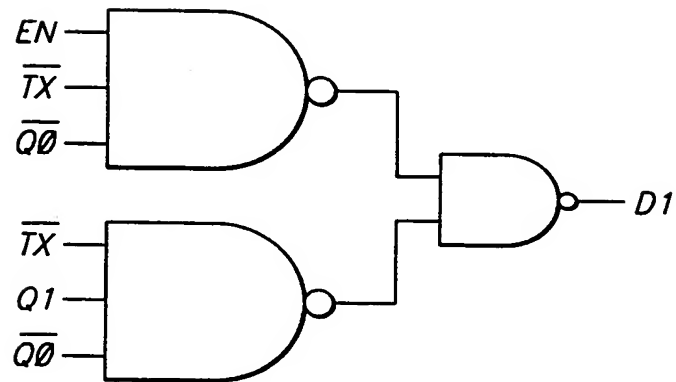
IEEE 67

En TX		Q1 Q0			
		00	01	11	10
D1:	00	0	0	0	1
	01	0	0	0	0
	11	0	0	0	0
	10	1	0	0	1

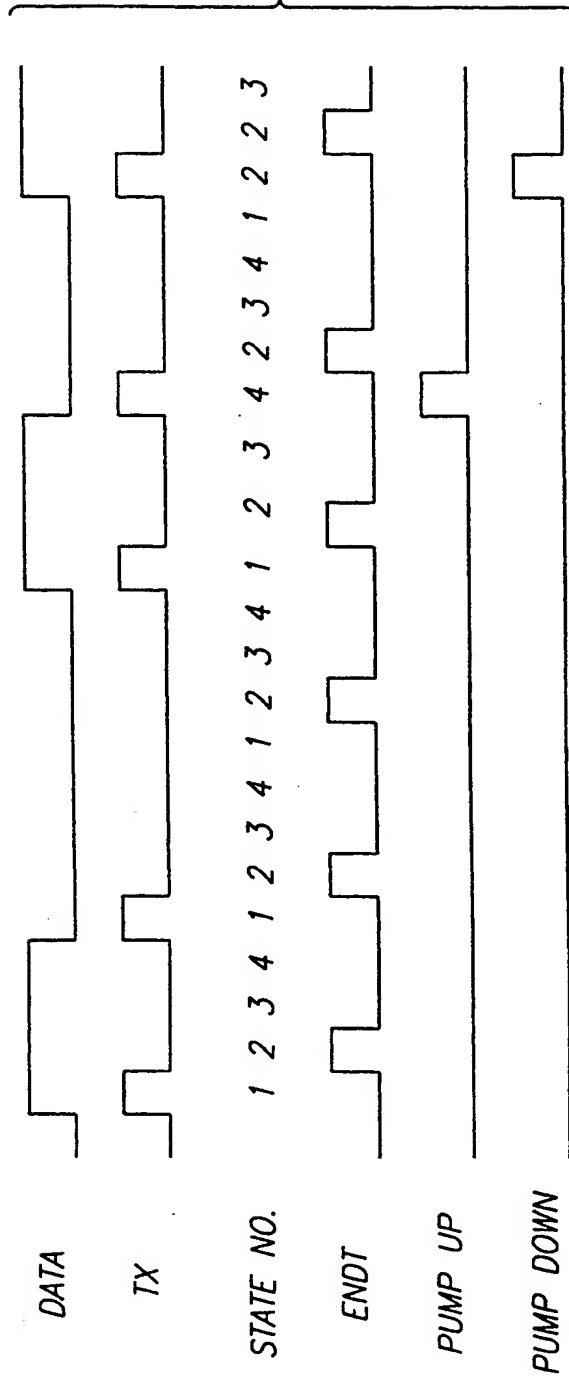
IEEE 68



IEEE 1691



IEEE 1100



IEEE

NAME	CURRENT (μ A)	ΔV (mV)	$\Delta V/V$ CONTROL(NOM) X 100
COARSE	40	160	13.3%
MEDIUM	10	40	3.3
MEDIUM FINE	1	2.6	0.22
FINE	0.1	0.26	0.022

II 09 72